

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM
RELOCATABLE DIAGNOSTIC LOADER - CARD

PART NO. 2196461
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1. PURPOSE

THE 1800 RELOCATABLE DIAGNOSTIC LOADER IS USED TO LOAD THE DIAGNOSTIC MONITOR AND PROGRAMS WHICH RUN UNDER CONTROL OF THE DIAGNOSTIC MONITOR. THE LOADER ALSO LOADS NON-MONITOR PROGRAMS WHOSE OBJECT DECKS ARE IN THE SAME FORM AS THE OUTPUT OF THE 1800 ASSEMBLER (12-4 FORMAT). (THE 1800 RELOCATABLE DIAGNOSTIC LOADER WILL NOT LOAD PROGRAMS WHOSE OBJECT DECK IS COMPATIBLE WITH EITHER THE 1800 BASIC DIAGNOSTIC LOADER OR THE 1800 AUXILIARY STORAGE LOADER.)

2. PREREQUISITES

2.1 PROGRAM PREREQUISITES.

PROGRAMS MAY NOT HAVE ORG ADDRESSES OF /300D TO /75DD SINCE THIS IS RESERVED FOR WAITS AND ERROR TRAPS.

2.2 EQUIPMENT PREREQUISITES

- A. 1801 OR 1802 PROCESSOR CONTROLLER
- B. 1442 CARD READER/PUNCH

3. USE PROCEDURE

3.1 LOADING AND OPERATING

THE 1800 RELOCATABLE LOADER DECK CONSISTS OF SEVEN (8-8 FORMAT) CARDS. THE RELOCATABLE LOADER IS CAPABLE OF LOADING-

- 1. ABSOLUTE BINARY DECKS (12-4 FORMAT)
- 2. RELOCATABLE BINARY DECKS (WHOSE ORIGIN IS /07FF)
- 3. ABSOLUTE HEX CORRECTION CARDS
- 4. RELOCATABLE HEX CORRECTION CARDS
- 5. ABSOLUTE HEX TRANSFER CARDS
- 6. EDIT CARDS (OF THE TYPE REQUIRED FOR PROGRAMS WHICH RUN UNDER

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CONTROL OF THE DIAGNOSTIC MONITORS)

TO LOAD A PROGRAM-

A. AT 1442 CARD READ/PUNCH-

- 1. DEPRESS NPRO PUSHBUTTON TO EJECT ANY CARDS LEFT IN MACHINE.
- 2. PLACE 1800 RELOCATABLE LOADER IN FRONT OF DECK(S) TO BE LOADED AND PLACE CARDS IN HOPPER. PLACE CARDS FACE DOWN WITH NINE EDGE FORWARD.
- 3. DEPRESS START PUSHBUTTON ON 1442. READY INDICATOR SHOULD LIGHT.

B. USE 1800 PC CONSOLE TO CLEAR STORAGE AS FOLLOWS-

- 1. SET DATA ENTRY SWITCHES TO /7DFF.
- 2. SET CHECK STOP SWITCH TO 'OFF'.
- 3. SET WRITE STG PROT SWITCH TO 'YES'.
- 4. HOLD CLEAR STORAGE PUSHBUTTON DOWN AND DEPRESS START PUSHBUTTON TO CLEAR STORAGE.
- 5. DEPRESS STOP PUSHBUTTON TO STOP CLEARING OPERATION.

C. TO INITIATE PROGRAM LOADING-

- 1. SET OPERATIONS MONITOR SWITCH TO 'OFF'.
- 2. SET DISABLE INTERRUPT SWITCH TO 'OFF'.
- 3. SET CHECK STOP SWITCH TO 'ON'.
- 4. SET WRITE STOR PROT BITS SWITCH TO 'YES'.
- 5. SET 1800 PC SENSE/PROGRAM AND DATA ENTRY SWITCHES AS REQUIRED BY PROGRAM TO BE LOADED.
- 6. DEPRESS RESET PUSHBUTTON.
- 7. DEPRESS PROGRAM LOAD PUSHBUTTON. (PROGRAM SHOULD LOAD)

3.2 WAITS

SEE THE WAIT SECTION OF THE LISTING FOR REGISTER VALUES AND WAIT DESCRIPTION.

4. PRINTOUTS (NONE)

5. COMMENTS

5.1 THE FOLLOWING ARE THE MAJOR ELEMENTS OF THE 1800 RELOCATABLE DIAGNOSTIC LOADER-

5.1.1 BOOTSTRAP ROUTINE-- IS A SET OF INSTRUCTIONS ENTERED INTO THE PROCESSOR CONTROLLED BY THE IPL (INITIAL PROGRAM LOADER) MODE WHOSE FUNCTION IS TO READ IN THE REMAINDER OF THE LOADER.

5.1.2 READ ROUTINE -- CHECKS 1442 FOR PROPER STATUS, READS A CARD INTO LOCATION /0000 THROUGH /004F, CHECKS FOR SATISFACTORY COMPLETION OF THE READ OPERATION, AND DETERMINES WHETHER THE CARD READ IS A BINARY CARD OR A HEXADECIMAL (CORRECTION OR EDIT CARD).

5.1.3 BINARY PACK ROUTINE -- TAKES DATA FOUND IN LOCATIONS /0000 THROUGH /004F (12 BITS PER CORE WORD) AND PACKS IT INTO LOCATION /0000 THROUGH /0035 (16 BITS PER CORE WORD).

5.1.4 CHECKSUM ROUTINE -- COMPUTES CHECKSUM OF A BINARY CARD, WAITS IF CHECKSUM IS IN ERROR.

5.1.5 MOVE ROUTINE -- MOVES DATA FROM /0000 THROUGH /0035 TO PROPER CORE LOCATION. CHECKS FOR EXCEEDING CORE SIZE. ADDS IN RELOCATION FACTOR WHEN REQUIRED.

5.1.6 RELOCATABLE HEADER ROUTINE -- ENTERED WHEN A RELOCATABLE HEADER CARD

IS FOUND. COMPUTES A RELOCATION FACTOR FOR PROGRAM THAT FOLLOWS.

- 5.1.7 ABSOLUTE HEADER ROUTINE -- ENTERED WHEN AN ABSOLUTE HEADER CARD IS FOUND. SETS RELOCATION FACTOR TO ZERO.
- 5.1.8 TRANSFER ROUTINE -- ENTERED WHEN A TRANSFER CARD IS FOUND. COMPUTES THE NEXT LOCATION AVAILABLE FOR LOADING IF ANOTHER PROGRAM FOLLOWS. TRANSFERS CONTROL TO THE LOCATION SPECIFIED ON THE TRANSFER CARD.
- 5.1.9 HEX TO BINARY CONVERSION ROUTINE -- CONVERTS A HEXADECIMAL CARD TO BINARY. ADOS IN RELOCATION FACTOR IF REQUIRED.
- 5.2 CARD RECOGNITION
THE FOLLOWING ARE CARDS WHICH CAN BE LOADED BY THE 1800 RELOCATABLE DIAGNOSTIC LOADER.
 - 5.2.1 ABSOLUTE HEADER CARDS HAVE A 1 PUNCH IN COLUMN 4.
 - 5.2.2 RELOCATABLE HEADER CARDS HAVE A 0 (ZERO) PUNCH IN COLUMN 4.
 - 5.2.3 NORMAL DATA CARDS HAVE NO PUNCHES IN ROW 12 IN COLUMN 1. AN ADDRESS IN ROWS 11 THROUGH 9 IN COLUMN 1 AND ROWS 12 THROUGH 1 IN COLUMN 2. A CHECKSUM IN ROWS 2 THROUGH 9 OF COLUMN 2 AND ROWS 12 THROUGH 5 OF COLUMN 3. A 12, 0 PUNCH IN COLUMN 4, A WORD COUNT IN ROWS 4 THROUGH 9 OF COLUMN 4. A RELOCATION FIELD (WHICH MAY BE BLANK) IN COLUMN 5 THROUGH 12. DATA IN COLUMNS 13 THROUGH 72. A SEQUENCE NUMBER IN COLUMNS 73-80.
 - 5.2.4 BINARY TRANSFER CARDS HAVE 12, 11, 0, 1 PUNCHES IN COLUMN 4 AND A WORD COUNT OF ZERO (NO PUNCHES IN ROWS 4 THROUGH 9 IN COLUMN 4).
 - 5.2.5 HEXADECIMAL TRANSFER CARDS HAVE A 12 PUNCH IN COLUMN 1, A TRANSFER ADDRESS IN COLUMN 2 THROUGH 5 AND NO PUNCHES IN COLUMNS 6 AND 7.
 - 5.2.6 HEXADECIMAL CORRECTION CARDS HAVE A 12 PUNCH IN COLUMN 1. AN ADDRESS IN COLUMN 2 THROUGH 5. DATA IN COLUMNS 6 THROUGH 80. DATA IS GROUPED 5 COLUMNS TO ONE CORE WORD. THE FIRST COLUMN OF EACH GROUP SPECIFIES WHETHER OR NOT THE GROUP REQUIRES A RELOCATION FACTOR. IF THE FIRST COLUMN OF A GROUP IS BLANK A RELOCATION FACTOR WILL NOT BE ADDED. IF THE FIRST COLUMN OF A GROUP CONTAINS AN R (11,9 PUNCH) A RELOCATION FACTOR WILL BE ADDED TO THE FIELD. LOADING OF THE CARD IS TERMINATED BY TWO SEQUENTIAL BLANK COLUMNS.
 - 5.2.7 EDIT CARDS HAVE 12, 5 PUNCHES IN COLUMN 1. DATA IS GROUPED 4 COLUMNS PER CORE WORD WITH A BLANK COLUMN AFTER EACH GROUP. LOADING OF THE CARD IS TERMINATED BY TWO SEQUENTIAL BLANK COLUMNS.


```
* THIS RT PACKS BINARY DATA AND LEAVES IT IN
* LOCATIONS 0000-0040.
*
0074 0 61B8 SB05 LDX 1 -72
0075 0 6300 LDX 3 0
0076 0 62FD SB06 LDX 2 -3
0077 0 C268 SB07 LD 2 SL&3
0078 0 0004 STO SB10
0079 0 C149 LO 1 73 COIN&73
007A 0 1800 RTE 16
007B 0 C148 LD 1 72 COIN&72
007C 0 1804 SRA 4
007D 0 1000 SB10 SLA 0
007E 0 0300 STD 3 0 COIN
007F 0 7301 MOX 3 1
0080 0 7101 MOX 1 1
0081 0 7201 MDX 2 1
0082 0 70F4 MOX SB07 FINISHEO
0083 0 7101 MDX 1 1
0084 0 70F1 MOX SB06

*
* THIS RT DETERMINES WHETHER THE DATA CARD IS
* 1# ABSOLUTE HDR CARD 2# RELOCATABLE HDR CARD
*
0085 0 C102 LO 1 2 CK FDR HOR CAROS
0086 0 4C18 0050 BSC L RD05,&- IGNORE BLANK CARD
0088 0 E052 AND LB20
0089 0 9052 S LB25
008A 0 4C18 00E1 BSC L ABHEO,&- BCH IF ABSOL HEAO CARD
008C 0 904F S LB25
008D 0 4C18 000F BSC L RLHEO,&- BCH IF RELOC HEAD CARD

*
008F 0 7400 0008 MOX L SW,0
0091 0 7005 MDX TAKE

*
0092 0 C100 LD 1 0 GET CARD AORS
0093 0 B000 CMP K3000 CMP 3000
0094 0 B068 CMP K74FF GRTR 3000 CMP 74FF
0095 0 7001 MDX TAKE LESS 3000 SAME GRTR 74FF
0096 0 704C MDX PASS EQUAL 3000 SAME LESS 74FF
0097 0 6840 TAKE STX SW LOAO PRDGRAM
*****
0098 ORG 152 CARD 4

*
0098 0 C040 LO CDCT
0099 0 62CA LDX 2 -54
009A 0 8236 HSCA A 2 54
009B 0 4802 BSC C
009C 0 8001 A ONE
009D 0 7201 MOX 2 1
009E 0 70FB MDX HSCA
009F 0 80CE A ONE
00A0 0 4820 BSC Z
00A1 0 30F9 W30F9 DC /30F9 CHECK SUM ERROR
00A2 0 C102 LD 1 2 GET COIN&2
00A3 0 E036 ANO LB15
00A4 0 0039 STO PCAM
00A5 0 4818 BSC &- SKIP IF NOT XFER CARD
00A6 0 703F MDX XFRCD BRANCH TO XFER RT

*
* THIS RT PLACES DATA FIELDS INTO THE CORRECT
* CORE LOCATIONS ANY AORS IN A RELOCATION
* FACTOR IF REQUIRED.
*
00A7 0 6209 LDX 2 9 SET FOR 1ST DATA WO
00A8 0 6100 LDX 1 0
00A9 0 C033 LD HEXSW CK FOR EDIT CARD
00AA 0 1007 SLA 7
```

8B101520
8B101530
8B101540
8B101550
8B101560
8B101570
8B101580
8B101590
8B101600
8B101610
8B101620
8B101630
8B101640
8B101650
8B101660
8B101670
8B101680
8B101690
8B101700
8B101710
8B101720
8B101730
8B101740
8B101750
8B101760
8B101770
8B101780
8B101790
8B101800
8B101810
8B101820
8B101830
8B101840
8B101850
8B101860
8B101870
8B101880
8B101890
8B101900
8B101910
8B101920
8B101930
8B101940
8B101950
8B101960
8B101970
8B101980
8B101990
8B102000
8B102010
8B102020
8B102030
8B102040
8B102050
8B102060
8B102070
8B102080
8B102090
8B102100
8B102110
8B102120
8B102130
8B102140
8B102150
8B102160
8B102170
8B102180
8B102190

```
00AB 0 4CA8 0124 BSC I MECD,&Z XFER IF EDIT CARD 8B102200
00A0 0 6A06 STX 2 LB10&1 8B102210
00AE 0 C100 LD 1 0 8B102220
00AF 0 8077 A UPPER 8B102230
00B0 0 0100 STO 1 0 STO IN CDIN 8B102240
00B1 0 6680 0000 LDX 12 LD22 8B102250
00B3 0 C500 0000 LB10 LO 11 0 8B102260
00B5 0 D200 STO 2 0 8B102270
00B6 0 6A72 STX 2 TEMP CK FOR ECEEOING CORE 8B102280
00B7 0 C071 LO TEMP 8B102290
00B8 0 F060 EOR U&IM 8B102300
00B9 0 4820 BSC Z 8B102310
00BA 0 7002 MOX OVER1 8B102320
00BB 0 30FC W30FC DC /30FC ERROR EXCEEDOED CORE SIZE 8B102330
***** CARD 5 8B102340
00BC DRG 188 8B102350
*
00BC 0 70FE MDX W30FC 8B102360
00BD 0 7201 OVER1 MOX 2 1 8B102370
00BE 0 7101 MOX 1 1 8B102380
00BF 0 74FF 000E MOX L PCAM,-1 SKIP IF WORO COUNT ZERO 8B102390
00C1 0 70F1 MDX LB10 8B102400
00C2 0 C01A LO HEXSW 8B102410
00C3 0 4C28 0050 BSC L RD05,&Z 8B102420
00C5 0 6780 0000 RLOAT LDX 13 LD22 SET X3 TO START ADDR 8B102430
00C7 0 62FA LDX 2 -6 SET FDR 6 CONTROL WD 8B102440
00C8 0 6108 LDX 1 8 8 LOCS PER WO 8B102450
00C9 0 C209 CKFLO LO 2 9 GET RELOC CONTROL 8B102460
00CA 0 1002 SLA 2 8B102470
00CB 0 0209 STO 2 9 8B102480
00CC 0 4C02 0004 BSC L RLCAT,C BRANCH IF RELOC FIELD 8B102490
00CE 0 7301 INCRE MDX 3 1 8B102500
00CF 0 71FF MOX 1 -1 TEST FOR CNTRL WD END 8B102510
00D0 0 70F8 MOX CKFLD NO 8B102520
00D1 0 7201 MDX 2 1 TEST FOR FIN ALL WOS 8B102530
00D2 0 70F5 MDX CKFLO-1 NO 8B102540
00D3 0 700F MOX PASS 8B102550
00D4 0 C300 RLCAT LO 3 0 AOD IN RELOCATIDN FACTDR 8B102560
00D5 0 8051 A UPPER 8B102570
00D6 0 0300 STO 3 0 8B102580
00D7 0 70F6 MOX INCRE 8B102590
00D8 0 0000 SW OC /0000 IF ZERO CK FOR WAITS-TRAPS 8B102600
00D9 0 0000 CDCT DC 0 8B102610
00DA 0 003F LB15 OC /003F 8B102620
00DB 0 0F00 LB20 OC /0F00 8B102630
00DC 0 0100 LB25 DC /0100 8B102640
00DD 0 0000 HEXSW DC 0 8B102650
00DE 0 0000 PCAM OC 0 WORO COUNT 8B102660
* 8B102670
* THIS RT. HANLES RELOCATABLE HDR CARDS. 8B102680
* 8B102690
* RLHEO LD NLOC COMPUTE RELOC FACTDR 8B102700
***** CARO 6 8B102710
* DRG 224 8B102720
*
* S RLBA 8B102730
* 8B102740
* THIS RT. HANDLES ABSOLUTE HOR CAROS. 8B102750
* 8B102760
* 8B102770
* 8B102780
* ABHED STO UPPER 8B102790
* STX 1 CDCT 8B102800
* PASS MOX L COCT,1 INCR CARO COUNT 8B102810
* LDX 80 8B102820
* 8B102830
* THIS RT. HANLES BINARY AND HEX XFER CARDS 8B102840
* 8B102850
* XFRCD LD 1 0 8B102860
* A ONE 8B102870
```


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RELOCATABLE DIAGNOSTIC LOADER (4K-65K CARDS)

```
00E8 0 803E      A      UPPER
00E9 0 D038      STO     NLOC      SET NEXT AVAIL LOC
00EA 0 C103      LD      1 3      SET UP TO XFER
00EB 0 803B      XFR2    A.      UPPER
00EC 0 69E8      STX     1 SW      CLEAR SW
00ED 0 D001      STO     XFER&1
00EE 0 4C00 0000 XFER    BSC    L 0
*
* THIS RT CONVERTS HEX TO BINARY AND LEAVES 1
* IN LOCATIONS 0000-0010 , FIELDS ARE RELOCATED
* IF REQUIRED.
*
00F0 0 61AF      HB05    LDX     1 -81
00F1 0 D0E8      STO     HEXSW      SET FOR HEX OR EDIT CD
00F2 0 1810      SRA     16
00F3 0 D0EA      STO     PCAM
00F4 0 1810      HB06    SRA     16
00F5 0 D032      STO     RLREQ
00F6 0 C151      LD      1 81      CK FOR RELOC FIELD
00F7 0 1001      SLA     1
00F8 0 4828      BSC     &Z
00F9 0 682E      STX     RLREQ
00FA 0 7101      MDX     1 1
00FB 0 7006      MDX     HB07
*
* THIS RT DETERMINES WHETHER A HEX CARD IS A
* 1 DATA CD 2 EDIT CD 3 XFER CARD .
*
00FC 0 6201      LH05    LDX     2 1      SET X2 # CDIN&1
00FD 0 74FF 00DE K74FF  MOX    L  PCAM,-1
00FE 0 70A8      MDX     LB06
0100 0 C2FF      LD      2 -1      CDIN
0101 0 70E9      MDX     XFR2      BRANCH TO XFER RT
0102 0 6204      HB07    LDX     2 4
0103 0 1004      HB10    SLA     4
*****
0104             ORG      260      CARD 7
*
0104 0 D025      STO     TEMP1
0105 0 C151      LD      1 81      COIN&81
0106 0 4C18 00FC BSC     L  LH05,&-
0108 0 6300      LOX     3 0      CONVERT 1 HEX CUL TO BIN
0109 0 4828      BSC     &Z      SKIP IF NOT A-F
010A 0 7309      MDX     3 9      ADD 9 FOR ALPHA
010B 0 1003      SLA     3      ELIMINATE ZONE BITS
010C 0 4C1B 0115 BSC     L  HTBZ,&-
010E 0 7301      MDX     3 1      XFER IF HEX CHAR # 0
010F 0 4C28 0113 HT0B1  BSC     L  HTBX,&Z
0111 0 1001      SLA     1      XFER IF 8IT IS FOUND
0112 0 70FB      MDX     HT081-1  PREPARE TO LK AT NEXT 8IT
0113 0 6B15      HTBX    STX     3 TEMP
0114 0 C014      LD      TEMP
0115 0 E814      HT8Z    OR      TEMP1      LOAD 8INARY 8ITS
0116 0 7101      MDX     1 1      ADD TO PREVIOUS CHARS
0117 0 72FF      MDX     2 -1
0118 0 70EA      MDX     HB10
0119 0 6780 00DE LOX     13 PCAM
011B 0 7400 0128 MDX     L  RLREQ,0
011D 0 8009      A       UPPER
011E 0 D300      STO     3 0
011F 0 7401 00DE MDX     L  PCAM,1
0121 0 70D2      MDX     HB06
*
0122 0 30F8      W30F8  DC      /30FB      EDIT CARD ERROR
*
0123 0 0122      MLCDC  DC      W30F8      **KEEP AT /123, CHG BY USER
0124 0 0122      MECDC  DC      W30F8      **KEEP AT /124, CHG BY USER
0125 0 07FF      NLOC   DC      /07FF      **KEEP AT /125, NEXT LOCATIO
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```
0126 0 1000      ULIM    DC      /1000      **KEEP AT /126, UPPER LIMIT
0127 0 0000      UPPER   DC      /0000      **KEEP AT /127, UPPER LIMIT
0128 0 0000      RLREQ   DC      /0000      I COUNTER STORAGE
0129 0 0000      TEMP    DC      /0000      TEMP STORAGE
012A 0 0000      TEMP1   DC      /0000      TEMP STORAGE
*****
012C 0000      END      LD22      TRANSFER TO START
NO STATEMENTS FLAGGED IN THE ABOVE ASSEMBLY
```

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DATE 28FEB66 01JUL66 17OCT66 15MAY67 14NOV69 30JAN70
EC NO. 415120 415178 415233A 411731 431319 431319A

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RELOCATABLE DIAGNOSTIC LOADER (4K-65K CARDS)

ABHED 00E1 008A
CDCT 00D9 0098 00E2 00E3
CKFLD 00C9 0000 00D2
CT 0011 0009
DSW 0070 0052 0056 005B
DSW1 0014 0001 0004
HB05 00F0 005F
HB06 00F4 0121
HB07 0102 00F8
HB10 0103 0118
HEXSW 00DD 0062 00A9 00C2 00F1
HSCK 009A 009E
HTBX 0113 010F
HTBZ 0115 010C
HTO81 010F 0112
INCRE 00CE 00D7
K300D 0064 0093
K74FF 00F0 0094
LB06 00A8 00FF
LB10 00B3 00A0 00C1
LB15 00DA 00A3
LB20 00DB 0088
LB25 00DC 0089 008C
LD22 0000 000B 005E 006C 0081 00C5 012C
LD25 0001 0002 0010
LD30 000D 0006
LO36 002C 000C
LH05 00FC 0106
MECD 0124 00AB
MLCD 0123 0053
MSK 0068 0050
MSK2 006A 0051
MSK5 0016 000D
NLOC 0125 000F 00E9
DNE 006E 009C 009F 00E7
OVER1 00BD 00BA
PASS 00E3 0096 0003
PCAM 00DE 00A4 00BF 00F3 00FD 0119 011F
RD05 0050 003C 0086 00C3
RD20 0052 0073
RD25 0056 0057
READ 006C 0055
READ1 0012 0000 0007
RLBA 006F 00E0
RLCAT 0004 00CC
RLDAT 00C5
RLHED 00DF 0080
RLREQ 0128 00F5 00F9 011B
SB05 0074 0063
SB06 0076 0084
SB07 0077 0082
SB10 007D 0078
SL 0065 0077
STGCK 0034 002D
STGLP 002F 0036
SW 00D8 008F 0097 00EC
TAKE 0097 0091 0095
TEMP 0129 0086 0087 0113 0114
TEMP1 012A 0104 0115
ULIM 0126 003A 00B8
UPPER 0127 00AF 0005 00E1 00E8 00EB 0110
W30FA 000E 0005 000F 30FA
W30FB 0072 005C 30FB
W30FC 008B 008C 30FC
W30F8 0122 0123 0124 30F8
W30F9 00A1 30F9
XFER 00EE 00ED
XFRCD 00E6 00A6

XFR2 00EB 0101
END OF ASSEMBLY

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INTERVAL TIMER FUNCTION TEST

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INTERVAL TIMER FUNCTION TEST

1. PURPOSE

THE TIMER FUNCTION TEST IS USED TO DETERMINE WHETHER THE INTERVAL TIMER CIRCUITS IN THE 1801 OR 1802 PROCESS/CONTROLLER ARE OPERATING PROPERLY. TIMER STEPPING, TIMER INTERRUPTS, DSW, AND ILSW ARE TESTED.

2. PREREQUISITES

2.1 PROGRAM PREREQUISITES

THE BASIC DIAGNOSTIC LOADER IS REQUIRED TO LOAD THIS PROGRAM.

2.2 EQUIPMENT PREREQUISITES

THE FOLLOWING EQUIPMENT IS REQUIRED.

- A. 1800 PROCESSOR/CONTROLLER.
- B. 1442 CARD READ/PUNCH OR 1054 PAPER TAPE READER.
- C. EITHER A 1053/1816 OR 1443 PRINTER.

3. USE PROCEDURE

3.1 PROGRAM LOADING

REFER TO 1800 BASIC DIAGNOSTIC LOADER DOCUMENTATION PARAGRAPH 3.1, FOR LOADING INSTRUCTIONS.

3.2 PROGRAM OPERATION

AFTER LOADING THE P.C. STOPS AT WAIT 1 (B REG=3001). WITH P.C. STOPPED AT WAIT 1, PROCEED AS FOLLOWS.

- A. SET CHECK STOP SWITCH TO OFF.
- B. SET DISABLE INTERRUPT SWITCH TO OFF.
- C. SET WRITE STORAGE PROTECT SWITCH TO YES.
- D. SELECT PROGRAM OPTIONS. REFER TO TABLE 1 SECTION 3.2.
- E. IF LOOP ROUTINE IS DESIRED, REFER TO LOOP ROUTINE OPTION TABLE 2 SECTION 3.2.
- F. DEPRESS START BUTTON. PROGRAM SHOULD START EXECUTION.

- 1. IF LOOP ROUTINE OR LDDP PROGRAM WERE NOT SPECIFIED, ROUTINES 1 THROUGH 6 WILL BE EXECUTED ONCE. THE PROGRAM WILL PRINT MESSAGE "A002 PROGRAM COMPLETE" AND THEN STOP AT WAIT 2. (B REG=3002).
- 2. IF A ROUTINE WAS SELECTED FOR LOOPING, THEN THAT ROUTINE WILL LOOP UNTIL THE PROGRAM IS TERMINATED, OR THE LOOP ROUTINE FUNCTION IS CHANGED OR CLEARED. IF THE LOOP ROUTINE FUNCTION IS CHANGED, THEN THE NEW ROUTINE SELECTION WILL BE LOADED. IF THE LOOP ROUTINE FUNCTION IS CLEARED, THE PROGRAM WILL CONTINUE FROM THE PRESENT ROUTINE TO COMPLETION.
- 3. IF LDDP PROGRAM WAS SELECTED, ROUTINES 1 THROUGH 6 WILL BE RUN IN SEQUENCE IN A LOOP FASHION. AT THE END OF EACH PROGRAM PASS, MESSAGE "A003 PASS COMPLETE" WILL BE PRINTED.
- 4. IF THE SCOPING ROUTINE WAS SELECTED, MESSAGE "A001 SCOPE RTN SELECTED" WILL BE PRINTED, FOLLOWED BY MESSAGE "C002 ENTER STARTING COUNT". THE PROGRAM WILL STOP AT WAIT 5 (B REG=3005). THE PRINTOUT EXPLANATIONS SHOULD BE CONSULTED FROM THIS POINT FOR THE OPERATION OF THE SCOPING ROUTINE. PRINTOUT SECTION 4.2 COMMAND MESSAGES.

INTERVAL TIMER FUNCTION TEST

TABLE 1
PROGRAM OPTIONS - DATA ENTRY SWITCHES

NOTE
THE OPTIONS FOR SELECTING OUTPUT DEVICE, THE SCOPING ROUTINE, OR THE
CORE SPEED WILL BE HONORED ONLY IF THEY ARE ENTERED WHEN PROGRAM IS STOPPED
AT WAIT 1 (B REG=3001).

DATA ENTRY SWITCHES	OPTION DESCRIPTION
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15	
1	HALT ON ERROR
1	BYPASS ERROR PRINT
1	LOOP ON ERROR
1	LOOP PROGRAM
1	USE 1443 AS OUTPUT DEVICE
1	SELECT SCOPING ROUTINE
1	4 USEC MACHINE

TABLE 2
LOOP ROUTINE OPTION - SENSE/PROGRAM SWITCHES

PROGRAM/SENSE	DESCRIPTION
0 1 2 3 4 5 6 7	
X X X	ROUTINE NUMBER TO LOOP. NUMBER MUST BE IN HEX. NUMBER MAY BE CHANGED AT ANY TIME.

3.3 TERMINATING PROCEDURE

IF THE LOOP-PROGRAM, OR LOOP-ROUTINE OPTIONS ARE NOT SELECTED THE
PROGRAM WILL BE EXECUTED ONCE AND WILL STOP AT WAIT 2 FOLLOWING THE
PROGRAM COMPLETE PRINTOUT. DEPRESSING THE START PUSHBUTTON WILL
BRANCH THE PROGRAM TO WAIT 1, WHICH IS THE BEGINNING OF THE PROGRAM.

IF THE PROGRAM IS IN A LOOPING MODE, IT MAY BE TERMINATED BY:

1. DEPRESSING THE STOP BUTTON. DEPRESSING RESET AND START BUTTONS
WILL RETURN PROGRAM TO WAIT 1.
2. CLEARING THE LOOP FUNCTION, TO ALLOW PROGRAM TO RUN TO ITS
COMPLETION.

3.4 RESTART PROCEDURE

PRESS THE STOP, RESET AND START BUTTONS. THE PROGRAM SHOULD GO TO
WAIT 1. IF THIS DOES NOT OCCUR, THE PROGRAM MUST BE RELOADED.

3.5 PROGRAM HALTS

PROGRAM WAITS ARE USED IN THIS PROGRAM, AND ARE IDENTIFIED BY
REFERENCING THE B REG AND I REG.

A PROGRAM WAIT IS OF THE FORM,

30XX, (B REG).

INTERVAL TIMER FUNCTION TEST

A DESCRIPTION OF THE INDIVIDUAL PROGRAM WAITS CAN BE FOUND AT THE
BEGINNING OF THE PROGRAM LISTING. A TYPICAL WAIT DESCRIPTION FOLLOWS.
IT IS INCLUDED TO SHOW THE FORMAT OF THE LISTING, AND IT IS NOT
NECESSARILY A DESCRIPTION OF AN ACTUAL WAIT.

3001 0 01ED DC WAITI+1 WAIT 1
ONE OF THE METERED I/O UNITS
FAILED TO SEND A RESPONSE
INTERRUPT TO THE PROGRAM. INDEX
REGISTER 1 WILL HAVE THE ADDRESS
OF THE IOCC. THE AREA CODE WILL
INDICATE THE I/O UNIT NOT READY.
IF A 2401/02 DRIVE IS NOT READY,
PROGRAM WILL NOT STOP AT WAIT 1.

B REG, (FIRST 4 DIGIT GROUP) CORRESPONDS TO B REG READING.

I REG, (SECOND 4 DIGIT GROUP) CORRESPONDS TO I REG READING.

4. PRINTOUTS

THE VARIOUS PRINTOUTS THAT MAY OCCUR DURING EXECUTION OF THE TIMER
FUNCTION TEST FOLLOW.

4.1 STATUS MESSAGES

A001 SCOPE RTN SELECTED

THIS PRINTOUT INDICATES THAT THE SCOPE RTN HAS BEEN SELECTED AS A
RESULT OF SETTING ON DATA ENTRY SWITCH NO. 8 WHEN PROGRAM STOPPED AT
WAIT 1.

A002 PROGRAM COMPLETE

FOLLOWING THIS PRINTOUT PROGRAM STOPS AT WAIT 2. DEPRESSING START
PUSHBUTTON CAUSES PROGRAM TO BRANCH TO WAIT 1. AT THIS POINT
PROGRAM MAY BE REPEATED AGAIN.

A003 PASS COMPLETE

PRINTED AT THE END OF EACH PROGRAM PASS WHEN THE LOOP PROGRAM
OPTION IS SELECTED.

4.2 COMMAND MESSAGES

C001 RUN SCOPE RTN

THIS PRINTOUT INSTRUCTS THE OPERATOR TO RUN THE PROGRAM IN SCOPE
RTN, AND IS CAUSED IF THE CONTROL ROUTINE IS UNABLE TO DETERMINE
TIMER INTERRUPT LEVEL DUE TO ALL TIMERS FAILING TO INTERRUPT, OR
STEP.

INTERVAL TIMER FUNCTION TEST

C002 ENTER STARTING COUNT

THIS PRINTOUT IS A SCOPE ROUTINE REQUEST FOR OPERATOR ACTION. ENTER THE DESIRED STARTING COUNT IN THE DATA ENTRY SWITCHES. PUSH START AFTER ENTRY.

DATA ENTRY SWITCHES	DESCRIPTION
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 *	
X X X X X X X X X X X X X X X X	...DESIRED STARTING COUNT IN HEX

C003 ENTER NUMBER OF STEPS

THIS PRINTOUT IS A SCOPE ROUTINE REQUEST FOR OPERATOR ACTION. ENTER THE DESIRED NUMBER OF STEPS IN THE DATA ENTRY SWITCHES. A COUNT OF ZERO IS INVALID AND WILL CAUSE MESSAGE C003 TO BE PRINTED AGAIN. PUSH START AFTER ENTRY.

DATA ENTRY SWITCHES	DESCRIPTION
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 *	
X X X X X X X X X X X X X X X X	...DESIRED NUMBER OF STEPS IN HEX

C004 ENTER TIMER NUMBER

THIS PRINTOUT IS A SCOPE ROUTINE REQUEST FOR OPERATOR ACTION. ENTER THE DESIRED TIMER SELECTION IN DATA ENTRY SWITCHES. ONLY 1 TIMER AT A TIME MAY BE SELECTED. TIMER C IS USED IF NO ENTRY IS MADE. CLEAR ALL UNUSED SWITCHES. PUSH START AFTER ENTRY.

DATA ENTRY SWITCHES	DESCRIPTION
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 *	
0 0 1	...RUN TIMER C
0 1 0	...RUN TIMER B
1 0 0	...RUN TIMER A
1	...CHANGE INPUT PARAMETERS. (SEE NOTE 1)

NOTE 1. AFTER ROUTINE IS LOOPING, AND IT IS DESIRED TO CHANGE STARTING COUNT, NUMBER OF STEPS, OR TIMER NUMBER, SET SWITCH 2 ON. ROUTINE RESTARTS AND PRINTS MESSAGE C002.

C005 REPAIR FAILURE BEFORE CONTINUING

PRINTED FOLLOWING ERRORS E008 AND E009. PROGRAM GOES TO WAIT 1 AFTER THIS PRINTOUT TO ALLOW CE TO SELECT SCOPING ROUTINE. ERRORS E008 AND E009 CAN CAUSE PROGRAM TO LOOSE CONTROL OR INDICATE FALSE ERRORS.

INTERVAL TIMER FUNCTION TEST

4.3 DATA MESSAGES

D001 TIMERS ON INTRP LEVEL XX

THIS PRINTOUT INDICATES THE INTERRUPT LEVEL OF THE TIMERS. XX WILL BE THE ACTUAL INTERRUPT LEVEL NUMBER, IN DECIMAL.

4.4 ERROR MESSAGES

E001 SEQUENCE ERROR

THIS PRINTOUT OCCURS WHEN THE ROUTINE JUST RUN DOES NOT AGREE WITH THE ROUTINE SELECTED BY THE CONTROL ROUTINE.

E002 TIMERS FAIL TO STEP

THIS PRINTOUT OCCURS WHEN THE CONTROL SECTION IS UNABLE TO DETERMINE THE TIMER INTERRUPT LEVEL DUE TO TIMERS NOT STEPPING. THIS IS FOLLOWED BY A **RUN MANUAL MODE** PRINTOUT.

E003 TIMERS FAIL TO INTRP

THIS PRINTOUT OCCURS WHEN THE CONTROL SECTION IS UNABLE TO DETERMINE THE TIMER INTERRUPT LEVEL DUE TO TIMERS FAILING TO INTERRUPT. THIS PRINTOUT IS FOLLOWED BY A **RUN MANUAL MODE** PRINTOUT.

E004 RTN 2 TIMER X FAILED TO TURN ON

THIS PRINTOUT RESULTS IF A TIMER (A, B, OR C) FAILS TO STEP AFTER BEING TURNED ON. 10 CONSECUTIVE PRINTOUTS MAY RESULT, AS 10 TRIES ARE MADE ON EACH TIMER.

E005 RTN 2 TIMER X FAILED TO TURN OFF

THIS PRINTOUT RESULTS WHEN A TIMER (A, B, OR C) CONTINUES TO STEP AFTER A TURN OFF COMMAND. 10 CONSECUTIVE PRINTOUTS ARE POSSIBLE.

E006 RTN 3 TIMER X FAILED TO INTRP

THIS PRINTOUT OCCURS WHEN A TIMER (A, B, OR C) FAILS TO INTERRUPT.

E007 RTN 3 TIMER X DSW XXXX

THIS PRINTOUT OCCURS WHEN THE DSW BIT FOR THE INDICATED TIMER (A, B, OR C) IS IN ERROR.

E008 RTN 3 TIMER X ILSW XXXX

ONE PRINTOUT FOR EACH TIMER WILL OCCUR IF THE ILSW BIT IS NOT THE SAME FOR ALL TIMERS.

E009 RTN 4 TIMER X WAS XXXX SHOULD BE XXXX

THIS PRINTOUT OCCURS WHEN EXPECTED AND ACTUAL TIMER COUNTS DO NOT AGREE.

INTERVAL TIMER FUNCTION TEST

E00A RTN 5 TX WAS XXXX EXPTD XXXX PASS X

THIS PRINTOUT OCCURS WHEN EXPECTED AND ACTUAL TIMER COUNTS DO NOT AGREE. PASS 1 IS RUN WITH INTERRUPT OFF, PASS 2 IS RUN WITH INTERRUPT ON.

E00B RTN1 DOUBLE INCR OF I CTR DURING TMR X CS CYCLE

THE DESIGNATED TIMER CAUSED THE I COUNTER TO BE INCREMENTED DURING THE SECOND TIMER CYCLE STEAL CYCLE. THIS PRINTOUT WILL BE FOLLOWED BY MESSAGE C005.

E00C ILLEGAL RTN ENTRY

A ROUTINE NUMBER OF 7 WAS ENTERED INTO PROGRAM SWITCHES 5, 6, AND 7. SEVEN IS AN INVALID ROUTINE NUMBER. PROGRAM RETURNS TO WAIT 1 AFTER THIS PRINTOUT.

E00D RTN3 TIMER X ILSW WAS ZERO

WHILE CHECKING THE DESIGNATED TIMER, AN INTERRUPT WAS RECEIVED ON THE TIMER INTERRUPT LEVEL AND THE ILSW WAS BLANK.

E00E RTN1 A REG CHANGED ON TMR X CS CYCLE

THE CONTENTS OF THE A REGISTER WERE DESTROYED DURING THE DESIGNATED TIMER CYCLE STEAL CYCLE. THE A REG IS LOADED TO FFFF PRIOR TO TURNING THE TIMER ON.

E00F RTN6 TIMER X FAILED TO INCREMENT

THE TIMER SPECIFIED FAILED TO INCREMENT WITHIN 70 MSEC. AFTER BEING TURNED ON. THE TIMERS ARE STORAGE PROTECTED DURING THIS CHECK.

E010 RTN6 SPV INTRPT ON TIMER X CS CYCLE

A STORAGE PROTECT VIOLATE INTERRUPT WAS RECEIVED DURING THE DESIGNATED TIMER CYCLE STEAL CYCLE. TIMERS ARE STORAGE PROTECTED DURING THIS CHECK.

E011 RTN6 NO INTRP ON VIOLATE TIMER X

A STORE INSTRUCTION WAS ISSUED TO THE DESIGNATED PROTECTED TIMER. A STORAGE PROTECT VIOLATE INTERRUPT DID NOT OCCUR.

5. COMMENTS

THE TIMER FUNCTION TEST CONSISTS OF A CONTROL ROUTINE, SIX TEST ROUTINES, AND A SCOPING ROUTINE.

THE CONTROL ROUTINE DETERMINES THE TIMER INTERRUPT LEVEL AND OUTPUTS THE INFORMATION FOR OPERATOR OBSERVATION. THE CONTROL ROUTINE ALSO SEQUENCES THE TESTING ROUTINES AND ACCOMPLISHES THE PROGRAM OPTIONS SPECIFIED BY THE OPERATOR.

INTERVAL TIMER FUNCTION TEST

ROUTINE 1 CHECKS FOR DOUBLE INCREMENTING OF THE I COUNTER, AND FOR A CHANGE IN A REG CONTENTS DURING A TIMER C.S. CYCLE. IF EITHER OF THESE FAILURES OCCURS, THE ERROR PRINTOUT IS FOLLOWED BY A MESSAGE INSTRUCTING THE CE TO REPAIR THE FAILURE BEFORE CONTINUING. THIS IS DONE SINCE EITHER OF THESE FAILURES CAN CAUSE THE PROGRAM TO LOOSE CONTROL OR INDICATE FALSE ERRORS. IF IT IS DETERMINED BY THE CONTROL ROUTINE, THAT ANY TIMER FAILS TO INTERRUPT, THEN THAT TIMER(S) WILL NOT BE CHECKED IN ROUTINE 1.

ROUTINE 2 CHECKS THE ON-OFF ACTION OF ALL THREE TIMERS. EACH TIMER IS CHECKED 10 TIMES.

ROUTINE 3 CHECKS THE TIMERS FOR INTERRUPT, DSW, AND ILSW.

ROUTINE 4 CHECKS THE TIMERS FOR PROPER STEPPING. EACH TIMER IS ALLOWED TO STEP 50 TIMES WITH 16 DIFFERENT STARTING COUNTS.

ROUTINE 5 IS A TWO PASS ROUTINE. THE FIRST PASS IS WITH TIMER INTERRUPT OFF, AND THE SECOND PASS IS TIMER INTERRUPT ON. ALL TIMERS ARE RUN TOGETHER, AND EACH ONE IS CHECKED FOR PROPER STEPPING FOR 1000 STEPS.

ROUTINE 6 CHECKS THE TIMERS WHILE STORAGE PROTECTED. EACH TIMER IS CHECKED TO INSURE IT INCREMENTS WHILE STORAGE PROTECTED WITHOUT CAUSING A STORAGE PROTECT VIOLATE ERROR. THE CHECK IS MADE 10 TIMES PER TIMER. A FURTHER CHECK IS MADE TO INSURE AN SPV INTERRUPT DOES OCCUR WHEN TRYING TO STORE INTO A PROTECTED TIMER.

THE SCOPING ROUTINE ALLOWS THE OPERATOR TO SELECT A TIMER (A,B, OR C) STARTING COUNT, AND THE NUMBER OF STEPS THE TIMER SHOULD BE STEPPED. AFTER ALL DATA IS ENTERED, AND THE START BUTTON HAS BEEN DEPRESSED, THE ROUTINE WILL LOUP UNTIL DATA ENTRY SWITCH 2 IS TURNED ON, AT WHICH POINT THE ROUTINE RETURNS TO WAIT 5. ONLY ONE TIMER MAY BE RUN AT A TIME. IF A TIMER NUMBER IS NOT ENTERED, TIMER C WILL BE USED.

INTERVAL TIMER FUNCTION TEST

```
028C      ABS
          ORG      /3001      88200010
          *          88200020
          *          88200030
          *          88200040
          *          88200050
          *          88200060
          *          88200070
          *          88200080
          *          88200090
          *          88200100
          *          88200110
          *          88200120
          *          88200130
          *          88200140
          *          88200150
          *          88200160
          *          88200170
          *          88200180
          *          88200190
          *          88200200
          *          88200210
          *          88200220
          *          88200230
          *          88200240
          *          88200250
          *          88200260
          *          88200270
          *          88200280
          *          88200290
          *          88200300
          *          88200310
          *          88200320
          *          88200330
          *          88200340
          *          88200350
          *          88200360
          *          88200370
          *          88200380
          *          88200390
          *          88200400
          *          88200410
          *          88200420
          *          88200430
          *          88200440
          *          88200450
          *          88200460
          *          88200470
          *          88200480
          *          88200490
          *          88200500
          *          88200510
          *          88200520
          *          88200530
          *          88200540
          *          88200550
          *          88200560
          *          88200570
          *          88200580
          *          88200590
          *          88200600
          *          88200610
          *          88200620
          *          88200630
          *          88200640
          *          88200650
          *          88200660
          *          88200670
          *          88200680

          ** PROGRAM WAITS **

3001 0 0147      DC      WT1+1      WAIT 1

          WAIT OCCURS AFTER PROGRAM
          HAS LOADED. ENTER PROGRAM
          OPTIONS IN DATA ENTRY SMS.
          AND DEPRESS START.

3002 0 0187      DC      WT2+1      WAIT 2

          PROGRAM RAN TO COMPLETION.
          DEPRESSING START RETURNS
          PROGRAM TO WAIT 1.

3003 0 0195      DC      WT3+1      WAIT 3

          PROGRAM SEQUENCE ERROR.
          SUPERVISOR SECTION OF
          PROGRAM DETECTED AN ERROR
          IN ROUTINE SEQUENCING.
          DEPRESS START TO RETURN
          TO WAIT 1.

3004 0 03D0      DC      WT4+1      WAIT 4
          TIMER C FAILED TO INTER-
          RUPT IN ROUTINE 4.

3005 0 0460      DC      WT5+1      WAIT 5

          SCOPE ROUTINE WAIT. ENTER
          STARTING TIMER COUNT IN
          DATA ENTRY SWITCHES. PUSH
          START BUTTON.

3006 0 0469      DC      WT6+1      WAIT 6

          SCOPE ROUTINE WAIT. ENTER
          NUMBER OF DESIRED TIMER
          STEPS IN DATA ENTRY SWITCH
          DEPRESS START BUTTON.

3007 0 0474      DC      WT7+1      WAIT 7

          SCOPE ROUTINE WAIT. ENTER
          TIMER NUMBER IN DATA ENTRY
          SWITCHES.
          *
          BIT 4 = TIMER A
          BIT 5 = TIMER B
          BIT 6 = TIMER C
          *
          CLEAR ALL UNUSED BIT SMS.
          DEPRESS START TO CONTINUE.

3008 0 01E7      DC      WT8+1      WAIT 8

          WAIT FOR TIMER INTERRUPT
          IN ROUTINE 1.

3009 0 0538      DC      WT9+1      WAIT 9

          HALT ON ERROR REQUESTED.
          PUSH START TO CONTINUE.
```

INTERVAL TIMER FUNCTION TEST

```
300A 0 0551      DC      WTA+1      WAIT A

          1443 NOT READY. MAKE READY
          AND PUSH START.

300B 0 0553      DC      WTB+1      WAIT B

          1443 BUSY. THIS IS AN
          ERROR CONDITION. DETERMINE
          CAUSE, THEN PUSH START TO
          CONTINUE.

300C 0 056C      DC      WTC+1      WAIT C

          1053/1016 NUMBER 1 NOT
          READY. MAKE 1053/1016
          READY AND DEPRESS START.

300D 0 064E      DC      WTD+1      WAIT D

          DSW FAILED TO RESET AFTER
          INTERRUPT IN ROUTINE 2.

300E 0 06D1      DC      WTE+1      WAIT E

          AN INTERNAL INTERRUPT WAS
          RECEIVED. THE I CTR. AT
          INTERRUPT IS IN THE Q REG.
          THE ILSW IS IN THE A REG.
          DEPRESS START BUTTON TO
          RESTART THE PROGRAM.

300F 0 06DC      DC      WTF+1      WAIT F

          INTERNAL INTERRUPT, OTHER
          THAN SPV WAS RECEIVED
          DURING ROUTINE 6. I COUNT
          IS IN Q REG. ILSW IN A REG
          PRESS START TO RESTART
          PROGRAM.

3010      ORG      300

012C 0 8200      DC      /8200      P10
          *****
          *
          * TIMER FUNCTION TEST *
          * ** TIMFT ** *
          *
          *****

012D 0 1010      TISRT SLA      16
012E 00 D4000181      STO L RTNNO      CLR ROUTINE NUMBER
0130 00 D40003E5      STO L INTSW      CLEAR INTERRUPT SW
0132 00 D4000214      STO L TMA      *AND INOP INDICATORS
0134 00 D4000215      STO L TMA+1
0136 00 D4000216      STO L TMA+2
0138 00 D4000213      STO L ERRSW

013A 0 611F      CTLO1 LDX      1 31
013B 00 D5000003      STO L1 /0003      CLEAR INTERVAL TIMER
013C 0 71FF      MDX      1 -1      AND INTERRUPT LOCATN
013E 0 70FC      MOX      CTLO1+1

013F 0 C866      LDD      RESRT      SET RESTART INSTRUEN
0140 00 0C000000      STO L /0000
```


INTERVAL TIMER FUNCTION TEST

```
0142 0 10A0      SLT    32      CLEAR A AND G
0143 0 C05F      LO     CONST+2  SET LEVEL ERROR
0144 00 04000008 ST0 L  /0008  INTERRUPT ADDRESS
0146 0 3001      *WT1  WAIT    1      OP ENTER CONTROL
                                *      INFORMATION PUSH
                                *      START TO CONTINUE
0147 0 0864      *      XIO     BSW      READ BIT SWITCHES
0148 0 C067      *      LD      BSWA
0149 0 188B      *      SRT     11
014A 0 1010      *      SLA     16
014B 0 1081      *      SLT     1      SET CORE SPEED
014C 0 D067      *      STO     SPEED  *INOCATOR
014D 0 1084      *      SLT     4
014E 0 100F      *      SLA     15
014F 0 D063      *      STO     OPINO  SET OPT DEVICE INOIC
0150 00 2C400004 STS L  4, /40  CLEAR STORAGE
0152 00 2C400005 STS L  5, /40  *PROTECT BITS
0154 00 2C400006 STS L  6, /40
0156 0 C059      *      LO      BSWA      SAVE SW INPUT
0157 0 10G8      *      SLA     8
0158 00 4C280179 *      BSC L  CTL03,+2 BRNCH IF MANUAL MOOE
015A 00 440004AD *      BSI L  TIINT  GO DETERMINE TIMER SRC
                                *      INTERRUPT LEVEL
                                *      SET UP INTERRUPT ADDRESSES
015C 00 0C00026A *      XIO L  MASKO  MASK INTERRUPT LVLS
015E 00 0C00026C *      XIO L  MASK1
0160 0 621A      *      LDX     2 26
0161 0 C042      *      LD      CONST+3 ADDRESS SVINT
0162 00 06000008 *      STD L2 8
0164 0 72FF      *      MOX     2 -1
0165 0 70FC      *      MOX     -4
0166 0 0841      *      XIO     UMSKO  UNMASK INTERRUPTS
0167 0 0842      *      XIO     UMSK1
0168 0 0845      *      RTRN  XIO     SNSWS  CHECK IF A ROUTINE
0169 0 E038      *      AND     CONST+1 *IS SPECIFIED TO
016A 0 1808      *      SRA     8 *LOOP
016B 0 4808      *      BSC     +      SKIP IF LOOP ROUTINE
016C 0 7002      *      MDX     CTL04
016D 0 0043      *      STD     RTNNO  SET IN ROUTINE SW
016E 0 7006      *      MO      CTL02+2 GO EXECUTE ROUTINE
016F 0 C041      *      CTLO4 LD     RTNNO  CK IF ALL ROUTINES
0170 0 9030      *      S      CONST  HAVE RUN
0171 0 4818      *      BSC     +
0172 0 7008      *      MOX     CTLO5  BRNCH IF ALL RTN RUN
0173 00 740101B1 *      CTLO2 MOX L  RTNNO,1 ADO 1 TO ROUTINE NO
0175 00 658001B1 *      LDX II RTNNO
0177 00 40800195 *      BSC II RTN-1 EXIT TO ROUTINE
0179 00 4400053C *      *      MANUAL MODE SELECTED
017B 0 0710      *      CTLO3 BSI L  LOG  PRINT MANUAL MODE SRC
                                *      OC  TMM01 SELECTED
017C 00 4C000456 *      *      BSC L  TIMAN  GO TO MANUAL ROUTINE
```

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```
017E 0 082D      *      CTLO5 XIO  BSW      READ BIT SWITCHES
017F 0 C030      *      LD      BSWA      CHECK IF LOOP PROGRAM
0180 0 1804      *      SRA     4      IS SPECIFIED
0181 0 4804      *      BSC     E      BRANCH IF LOOP PGRM
0182 0 7005      *      MDX     LPPGM
0183 00 44C0053C *      *      ALL ROUTINES HAVE RUN
0185 0 072C      *      BSI L  LOG  PRINT PROGRAM SRC
                                *      OC  TMM03 COMPLETE
0186 0 3002      *      *      WT2  WAIT    2      PROGRAM COMPLETE
0187 0 70A5      *      *      MOX     TISRT  PUSHING START RESULT
                                *      IN BRANCH TO START
                                *      OF PROGRAM
0188 0 1010      *      *      LPPGM SLA  16  CLEAR ROUTINE NUMBER
0189 0 D027      *      *      STO     RTNNO
018A 00 4400U53C *      *      BSI L  LOG  PRINT PASS COMPLETE SRC
018C 0 08B6      *      *      OC  TMM23
018D 0 70E5      *      *      MOX     CTLO2  LOOP PROGRAM
018E 0 C023      *      *      ROUTINES RETURN HERE
018F 0 4818      *      *      RTRN  LD      SEQCK
0190 0 70D7      *      *      BSC     +
                                *      MOX     RTRN  BRANCH ON GOOD CHECK
0191 00 4400053C *      *      BSI L  LOG  PRINT SEQUENCE ERROR SRC
0193 0 073A      *      *      OC  TMM04
0194 0 3003      *      *      WT3  WAIT    3      SEQUENCE ERROR
0195 0 7097      *      *      MDX     TISRT  PUSH START FOR RESRT
0196 0 018A      *      *      ROUTINE ADDRESSES
0197 0 0217      *      *      RTN  DC     TIM00  ROUTINE 1
0198 0 0270      *      *      DC     TIM01  ROUTINE 2
0199 0 02E0      *      *      OC     TIM02  ROUTINE 3
019A 0 0355      *      *      DC     TIM03  ROUTINE 4
019B 0 03EB      *      *      DC     TIM04  ROUTINE 5
019C 0 019D      *      *      OC     TIM05  ROUTINE 6
                                *      OC     TIMER  ILLEGAL ROUTINE
019D 00 4400053C *      *      TIMER BSI L  LOG  PRINT ILLEGAL ENTRY SRC
019F 0 087A      *      *      DC     TMM20  MESSAGE ID
01A0 0 70A5      *      *      MOX     WT1
01A1 0 0006      *      *      CONST DC  6
01A2 0 0700      *      *      DC     /0700
01A3 0 06CB      *      *      DC     ERINT  LVL ERR INTRUPT AORS
01A4 0 06DF      *      *      OC     SVINT
```

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INTERVAL TIMER FUNCTION TEST

```
01A6 0000      BSS E 0
01A6 0 4C00    RESRT DC /4C00    RESTART INSTRUCTION
01A7 0 0120      OC TISRT
01A8 0 0000    UMSKO DC /0000    UNMASK INTERRUPT
01A9 0 0480      DC /0480      IOCC
01AA 0 0000    UMSK1 OC /0000
01AB 0 0481      DC /0481
01AC 0 0180    BSW DC BSWA      READ BIT SWITCH IOCC
01AD 0 0240      OC /0240
01AE 0 0000    SNSWS DC /0000    READ SENSE SW IOCC
01AF 0 0760      DC /0760
01B0 0 0000    BSWA OC 0        BIT SW READ IN AREA
01B1 0 0000    RTNNO DC 0        ROUTINE NUMBER
01B2 0 0000    SEQCK DC 0
01B3 0 0000    OPINO OC 0        OUTPUT DEVICE INDCTR
01B4 0 0000    SPEED DC 0        CORE SPEED INDICATOR
01B5 0 0444    TIBCN DC TIMAI
01B6 0 2000      OC /2000
01B7 0 3100      DC /3100    TIMER A
01B8 0 3200      OC /3200    TIMER B
01B9 0 3300      DC /3300    TIMER C
*****
***** ROUTINE NUMBER ONE *****
***** CHECK FOR DOUBLE INCR *****
***** OF I CTR DURING TIMER *****
***** CYCLE STEAL CYCLE *****
*****
01BA 00 0C00026A TIM00 XIO L MASKO MASK INTERRUPTS
01BC 00 0C00026C XIO L MASK1
01BE 0 1010      SLA 16        CLEAR ERROR SWITCH
01BF 0 0053      STO ERRSW
01C0 0 00F4      LD TIBCN
01C1 00 678C06CA LDX 13 INVLV *TRANSFER VECTOR
01C3 00 07000000 STO L3 0
01C5 00 6700FFFF LOA L3 /FFFF
01C7 00 6F000004 STX L3 4    SET ALL TIMERS TO
01C9 00 6F000005 STX L3 5    *FFFF
01CB 00 6F000006 STX L3 6
01CC 00 0C0001A0 XIO L UMSKO UNMASK INTERRUPTS
01CF 0 080A      XIO UMSK1
01D0 0 6103      LDX 13
01D1 0 00E4      LO TIBCN+1
01D2 00 04000500 STO L NIOCC
01D4 0 7005      MOX RTN00+5
01D5 00 04000500 RTN00 LD L NIOCC MODIFY IOCC FOR
01D7 0 1001      SLA 1        *NEXT TIMER
01D8 00 04000500 STO L NIOCC
01DA 00 05000186 LD L1 TIBCN+1 THM NMBR TO MESSAGE
01DC 00 0400085E STO L THM18+24
01DE 00 05000213 RTN01 LD L1 TINA-1 CHECK IF TIMER OPRTV
01E0 0 4820      BSC Z        SKIP IF OK
01E1 0 7016      MOX RTN05
01E2 00 0400026A LD L MASKO SET A TO FFFF
01E4 00 0C000500 XIO L NIOCC TURN TIMER ON
01E6 0 3008      MT8 WAIT 8    WAIT FOR INTERRUPT
01E7 0 7008      MOX RTN02
01E8 00 0C000502 XIO L FIOCC NORMAL INTRP RETURN
ERROR INTRP RETURN
```

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INTERVAL TIMER FUNCTION TEST

```
01EA 00 74010213 MDX L ERRSW,1 SET ERROR INDICATOR
01EC 00 44000511 BSI L ERROR PRINT ERROR SRC
01EE 0 0846      DC THM18 MESSAGE ID
01EF 0 01DE      DC RTN01 LOOP INTRP RETURN
01F0 00 0C000502 RTN02 XIO L FIOCC TIMERS OFF
01F2 00 040004AC LO L ACS CHECK IF ACCUMULATOR
01F4 00 0400026A EOR L MASKO *WAS DESTROYED ON
01F6 0 4820      BSC Z        *TIMER C.S.CYCLE
01F7 0 700F      MOX RTN04
01F8 0 71FF      RTN05 MOX 1 -1
01F9 0 700B      MOX RTN00 BRN TO CHECK NXT TMR
01FA 0 0018      LO ERRSW
01FB 00 04180202 BSC L RTN03,+ BRANCH IF NO ERROR
01F0 00 4400053C BSI L LOG PRINT FIX COMMAND SRC
01FF 0 0864      DC THM19 MESSAGE ID
0200 00 04000146 BSC L MT8
0202 00 04000181 RTN03 LD L RTNNO PREPARE SEQUENCE
0204 0 F000      EOR RT00 *CHECK
0205 0 00AC      STO SEQCK
0206 0 70B7      MOX RTNRT RETURN TO CONTROL SX
0207 00 05000186 RTN04 LO L1 TIBCN+1 SET TMR NMBR IN MSG
0209 00 0400088D STO L THM22+19
020B 00 44000511 BSI L ERROR PRINT A DESTROYED SRC
020D 0 0890      OC THM22
020E 0 010E      DC RTN01
020F 00 74010213 MDX L ERRSW,1 SET ERROR SWITCH
0211 0 70E6      MOX RTN05
0212 0 0001      RT00 DC 1
0213 0 0000      ERRSW DC 0 ANY FAILURE SWITCH
0214 0 0000      TINA OC 0 A INOP SW
0215 0 0000      DC 0 B INOP SW
0216 0 0000      DC 0 C INOP SW
0217 0 0852      ROUTINE NUMBER TWO
0219 0 0853      CHECK ON/OFF ACTION OF
TIMERS
021A 00 04000004 SLA 16 CLEAR ALL TIMERS
021C 00 04000005 STO L /0004
021E 00 04000006 STO L /0005
0220 0 6103      LDX 13 TIMER INDEX
0221 0 0094      LD TIBCN+1 SET TIMER IOCC
0222 00 04000500 STO L NIOCC
0224 0 1010      RTN10 SLA 16 CLEAR TIMER COUNT
0225 0 0048      STO THMCT WORK LOCATION
```

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```
0226 0 620A      LDX 2 IC      PASS INDEX
0227 00 C5000186  LD  L1 TIBCN+1    SET TIMER NUMBER
0229 00 04000784  STO L TMM08+11    IN MESSAGE
0228 00 0400079A  STO L TMM09+11
0220 00 0C000500  RTN11 XIO L NIOCC      TURN TIMER ON
022F 00 44000504  BSI L DEL20      GO DELAY SRC
0231 00 0C000502  XIO L FIOCC      TURN TIMER OFF
0233 00 C5000003  LD  L1 /0003      GET TIMER CONTENTS
0235 0 F038      EOR TMCNT      CHECK IF COUNT CHNGD
0236 00 4C180256  BSC L RTN12,+    BRANCH IF ZERO
0238 00 C5000003  LD  L1 /0003      SET PRESENT COUNT IN
023A 0 0033      STO TMCNT      WORK AREA
023B 00 44000504  BSI L DEL20      GO DELAY SRC
023D 00 C5000003  LD  L1 /0003      GET TIMER CONTENTS
023F 0 F02E      EOR TMCNT      CHECK IF COUNT CHNGD
0240 00 4C200250  BSC L RTN13,2    BRANCH IF NOT ZERO
0242 00 C5000003  LD  L1 /0003      SAVE PRESENT COUNT
0244 0 0029      STO TMCNT
0245 0 72FF      RTN15 MDX 2 -1
0246 0 70E6      MDX RTN11      GO MAKE ANOTHER PASS
0247 0 71FF      MDX 1 -1
0248 0 7018      MDX RTN14      SET UP FOR NEXT TIMR
0249 00 0C0006C8  XIO L DSM        PREVENT INTERRUPT
024B 00 0C0001A8  XIO L UNSKO      UNMASK INTERRUPTS
024D 00 0C0001AA  XIO L UNSK1
024F 00 C4000181  LD  L RTNND      PREPARE SEQUENCE
0251 0 F01D      EOR RT100      CHECK
0252 00 040001B2  STO L SEQCK
0254 00 4C00018E  BSC L RTNRT      RETURN TO CONTROL
0256 00 44000511  RTN12 BSI L ERROR  PRINT TIMER FAILED SRC
0258 0 0779      DC TMM08      TO TURN ON
0259 0 0220      DC RTN11      LOOP-ERROR-RETURN
025A 0 080F      XIO MASK0      REMASK AFTER PRINT
025B 0 0810      XIO MASK1
025C 0 70E8      MDX RTN15
0250 00 44000511  RTN13 BSI L ERROR  PRINT TIMER FAILED SRC
025F 0 078F      DC TMM09      TO TURN /FF
0260 0 022D      DC RTN11      LOOP ERROR RETURN
0261 0 0808      XIO MASK0      REMASK AFTER PRINT
0262 0 0809      XIO MASK1
```

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INTERVAL TIMER FUNCTION TEST

```
0263 0 70E1      MDX RTN15
0264 00 C4000500  RTN14 LD L NIOCC      SET IOCC FOR NEXT
0266 0 1001      SLA 1      TIMER
0267 00 D4000500  STO L NIOCC
0269 0 708A      MDX RTN10      GO CHECK NEXT TIMER
026A 0000      BSS E 0      CONSTANTS
026A 0 FFFF      MASK0 DC /FFFF      MASK INTERRUPT IOCC
026B 0 0480      DC /0480
026C 0 FFFF      MASK1 DC /FFFF
026D 0 0481      OC /0481
026E 0 0000      TMCNT DC 0
026F 0 0002      RT100 OC 2
0270 0 C06A      TIM02 LD RT201      SET TRAP ROUTINE
0271 00 678006CA  LOX 13 INLVL      ADDRESS IN INTERRUPT
0273 00 07000000  STO L3 0      LOCATION
0275 0 6103      LOX 1 3      TIMER INDEX
0276 00 C4000186  LD  L1 TIBCN+1    SET TIMER IOCC TO C
0278 00 D4000500  STO L NIOCC
027A 0 7005      MDX RTN20+5
027B 00 C4000500  RTN20 LD L NIOCC      SET IOCC FOR NEXT
027D 0 1001      SLA 1      TIMER
027E 00 D4000500  STO L NIOCC
0280 00 C5000186  LD  L1 TIBCN+1    SET TIMER NUMBER
0282 00 D4000781  STO L TMM10+11   IN MESSAGE
0284 00 040007C7  STO L TMM11+11
0286 0 C053      RTN21 LD RT200      SET TIMER TO FFFF
0287 00 05000003  STO L1 /0003
0289 00 0C000500  XIO L NIOCC      TURN TIMER ON
028B 00 44000504  BSI L DEL20      GO WAIT FOR INTERRUPT SRC
028C 00 0779      DC TMM08      TO TURN ON
028D 00 0220      DC RTN11      LOOP-ERROR-RETURN
028E 00 0C000502  XIO L FIOCC      TURN TIMER OFF
028F 00 44000511  RTN12 BSI L ERROR  PRINT TIMER FAILED
0291 0 07A6      OC TMM10      TO INTERRUPT
0292 0 0286      OC RTN21      LOOP ERROR RETURN
0293 0 7018      MDX RTN27      GO CHECK IF DONE
0294 00 078F      DC TMM09      TO TURN /FF
0295 00 022D      DC RTN11      LOOP ERROR RETURN
0296 00 0808      XIO MASK0      REMASK AFTER PRINT
0297 00 0809      XIO MASK1
```

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```
0294 00 C4000653 RTN22 LO L TRP02 CHECK IF PROPER DSW
0296 00 F50002DC EOR L1 RT202
0298 0 4818 BSC +- SKIP IF NOT ZERO
0299 0 700E MOX RTN23 OSM OK CONTINUE
*
*          WRONG DSW AFTER INTERRUPT
*
029A 00 C4000653 LD L TRP02 SET OSM FOR CONVRSM
029C 00 D4000629 STO L HEXWD
*
*****
029E 00 44000609 BSI L HEXCV CONVERT HEX TO 43 CD SRC
*****
*
02A0 00 CC00062E LOD L HEXCO ERROR OSM TO MESSAGE
02A2 00 DC0007CA STO L TMM11+14
*
*****
02A4 00 44000511 BSI L ERROR PRINT OSM ERROR SRC
02A6 0 078C OC TMM11
02A7 0 0286 DC RTN21 LOOP ERROR RETURN
*****
*
02A8 00 C500064F RTN23 LO L1 TRP01-1 CHECK IF TIMER ILSW
02AA 0 4818 BSC +- *IS ZERO
02AB 0 7025 MOX RTN26
02AC 0 71FF RTN27 MOX 1 -1 MAKE ANCTHER PASS IF
02AD 0 70CD MOX RTN20 ALL TIMERS NOT CKD
*
*          ALL TIMERS CKO CK ILSW
*
02AE 00 C4000650 LD L TRP01 CHECK THAT ALL TIMER
02B0 00 F4000651 EOR L TRP01+1 *ILSW EITS ARE THE
02B2 00 E4000652 AND L TRP01+2 *SAME
02B4 0 4818 BSC +-
02B5 0 7014 MOX RTN25 ILSW OK EXIT
*
*          ILSW ERROR
*
02B6 0 6303 LDX 3 3
02B7 00 C700064F RTN24 LD L3 TRP01-1 SET ILSW FOR CONVRSM
02B9 00 0400C629 STO L HEXWD
*
*****
02B8 00 44000609 BSI L HEXCV CONVERT HEX TO 43 CD SRC
*****
*
02B0 00 CC00062E LOD L HEXCO SET ILSW IN MESSAGE
02BF 00 0C00070E STO L TMM12+16
02C1 00 C7000186 LO L3 TIBCN+1 TIMER NMBR TO MESSAG
02C3 00 040007D9 STO L TMM12+11
*
*****
02C5 00 4400053C BSI L LOG PRINT ILSW SRC
02C7 0 07CE OC TMM12
*****
*
02C8 0 73FF MDX 3 -1
02C9 0 70ED MDX RTN24
*
*          ALL TIMERS CHECKED EXIT
*
02CA 00 C4000181 RTN25 LD L RTNNO
02CC 0 F00F EOR RT202
02CD 00 D4000182 STO L SEQCK
*
02CF 00 4C00018E BSC L RTNRT RETURN TO CONTROL
*
02D1 00 C5000186 RTN26 LD L1 TIBCN+1
```

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INTERVAL TIMER FUNCTION TEST

```
02D3 00 04000894 STO L TMM21+11
*
*****
02D5 00 44000511 BSI L ERROR PRINT ILSW ZERO SRC
02D7 0 0889 OC TMM21 MESSAGE ID
02D8 0 0286 DC RTN21 LOOP ERROR RETURN
*****
*
02D9 0 70D2 MDX RTN23+4
*
*          CONSTANTS
*
02DA 0 FFFF RT200 DC /FFFF
02DB 0 0640 RT201 DC TRAP2 INTERRUPT ADDRESS
02DC 0 0003 RT202 DC 3
02DD 0 8000 DC /8000 A DSW
02DE 0 4000 DC /4000 B DSW
02DF 0 2000 OC /2000 C DSW
*
*****
*          ROUTINE FOUR
*          CHECK TIMERS FOR
*          PROPER STEPPING
*
02E0 00 0C00026A TIM03 XIO L MASK0 MASK INTERRUPTS
02E2 00 0C00026C XIO L MASK1
02E4 0 6103 LOX 1 3 TIMER INDEX
02E5 00 C4000186 LO L TIBCN+1 IOCC TO TIMER C
02E7 00 040005D0 STO L NIOCC
02E9 0 7005 MOX RTN30+5
*
02EA 00 C4000500 RTN30 LD L NIOCC SET IOCC FOR NEXT
02EC 0 1001 SLA 1 TIMER
02ED 00 04000500 STO L NIOCC
02EF 0 6210 LDX 2 16 SET PASS INDEX
02F0 00 C5000186 LO L1 TIBCN+1 SET TIMER NUMBER
02F2 00 040007ED STO L TMM13+11 IN ERROR MESSAGE
*
02F4 0 6332 RTN31 LOX 3 50 SET STEP INDEX
02F5 00 C6000344 LD L2 CTT8L-1 SET STARTING COUNT
02F7 00 05000003 STO L1 /0003 *IN TIMER AND IN
02F9 0 0048 STO RT300 *CHECK REGISTER
*
02FA 00 0C000500 XIO L NIOCC TURN TIMER ON
*
*          ** CHECK STEPPING **
*
02FC 00 C5000003 RTN32 LD L1 /0003 GET TIMER CONTENTS
02FE 0 9043 S RT300 *LOOP UNTIL TIMER
02FF 0 4818 BSC +- *AND CHECK REG ARE
0300 0 70FB MOX RTN32 *UNLIKE
*
0301 0 F042 EOR RT302 CHECK IF OIFF IS 1
0302 0 4818 BSC +-
0303 0 7026 MDX RTN33 COUNT OK CONTINUE
*
*          ** COUNT IN ERROR **
*
0304 00 0C000502 XIO L FIOCC TURN TIMER OFF
0306 00 0C0006C8 XIO L DSW RESET OSM AVOID INTP
*
0308 0 C039 LO RT300 CONVERT EXPECTED
0309 0 803A A RT302 *COUNT
030A 00 D4000629 STO L HEXWD
*
*****
030C 00 44000609 BSI L HEXCV GO CONVERT WORD SRC
```

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INTERVAL TIMER FUNCTION TEST

```

*****
030E 00 CC00062E      LDD L HEXCD      SET EXPECTED COUNT
0310 00 DC0007F8      STD L TMM13+22    IN MESSAGE
*****
0312 00 C5000003      LD  L1 /0003     CONVERT ACTUAL
0314 00 04000629      STO L HEXW0     COUNT TO 1443 HEX
*****
0316 00 44000609      BSI L HEXCV     GO CONVERT WCR0      SRC
*****
0318 00 CC00062E      LDD L HEXCD      SET ACTUAL COUNT
031A 00 DC0007F0      STO L TMM13+14    IN MESSAGE
*****
031C 00 44000511      BSI L ERROR     GO PRINT COUNT ERROR SRC
031E 0  07E2          DC      TMM13
031F 0  02F4          DC      RTN31     LOOP ERROR ADORESS
*****
0320 00 0C00026A      XIO L MASK0     REMASK AFTER PRINT
0322 00 0C00026C      XIO L MASK1
*****
0324 00 C5000003      LD  L1 /0003     SET PRESENT TIMER
0326 0  0018          STO RT300      *COUNT IN CHECK REG
*****
0327 00 0C000500      XIO L NIOCC     TURN TIMER ON
0329 0  7002          MDX RTN33+2
*****
** CHECK STEP,PASS AND **
** TIMER INDEX FOR RTN **
** COMPLETE **
*****
032A 00 74010342      RTN33 MDX L RT300,I    CHECK REG TO NEXT CT
032C 0  1000          NOP
032D 0  73FF          MDX 3 -1
032E 0  70CD          MDX RTN32     BRNCH NOT 50 STEPS
032F 0  72FF          MDX 2 -1
0330 0  70C3          MDX RTN31     BRNCH NOT 16 PASSES
*****
0331 00 0C000502      XIO L FIOCC     TURN TIMER OFF
*****
0333 0  71FF          MDX 1 -1
0334 0  70B5          MDX RTN30     BRNCH NOT ALL TIMRS
*****
ROUTINE COMPLETE
*****
0335 00 C40001B1      LD  L RTNNO     PREPARE SEQUENCE
0337 0  F00B          EOR RT301     CHECK
0338 00 040001B2      STO L SEQCK
*****
033A 00 0C0006C8      XIO L DSM      PREVENT INTERRUPT
033C 00 0C0001A8      XIO L UMSK0     UNMASK INTERRUPTS
033E 00 0C0001AA      XIO L UMSK1
*****
0340 00 4CD0018E      BSC L RTNRT     RETURN TO CONTROL
*****
CONSTANTS
*****
0342 0  0000          RT300 DC      0
0343 0  0004          RT301 DC      4
0344 0  0001          RT302 DC      1
*****
TIMER STARTING COUNT TABL
*****
0345 0  FFFF          CTTBL DC     /FFFF
0346 0  FFF0          DC          /FFF0

```

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```

0347 0  FFF0          OC          /FFF0
0348 0  FFF0          DC          /FFF0
0349 0  F0FF          DC          /F0FF
034A 0  F0F0          OC          /F0F0
034B 0  F0F0          DC          /F0F0
034C 0  F000          DC          /F000
034D 0  0FFF          DC          /0FFF
034E 0  0FF0          DC          /0FF0
034F 0  0F0F          DC          /0F0F
0350 0  0F00          DC          /0F00
0351 0  00FF          DC          /00FF
0352 0  00F0          DC          /00F0
0353 0  000F          DC          /000F
0354 0  0000          DC          /0000
*****
ROUTINE NUMBER FIVE
CHECK TIMERS WHILE STEP-
ING TOGETHER,AND WHILE
IN INTERRUPT MODE
*****
0355 00 678006CA      TIM04 LOX I3 INVL     SET TIMER INTERRUPT
0357 00 C40003DE      LD  L RT400     LEVEL
0359 00 0700000D      STO L3 0
035B 0  1010          SLA 16
035C 00 040003E5      STO L INTSW    CLEAR INTERRUPT PASS
035E 00 C400030F      LD  L RT401     *INDICATOR
0360 00 D4000813      STO L TMM14+23  SET PASS I IN ERROR
*****
0362 00 0C00026A      XIO L MASK0     MASK INTERRUPTS FOR
0364 00 0C00026C      XIO L MASK1     *1ST PASS
*****
0366 0  6103          RTN40 LDX I 3      SET TIMER INDEX
0367 00 C40004F0      LO  L T1103     SET IOCC TO ALL THRS
0369 00 04000500      STO L NIOCC
*****
036B 00 C50001B6      LD  L1 T1BCN+1    TIMER NUMBER TO
036D 00 D4000805      STO L TMM14+9    *ERROR MESSAGE
*****
036F 00 660003E8      RTN41 LDX L2 /03E8    SET STEP INDEX 1000
0371 0  C070          LO  RT404     SET ALL TIMERS AND
0372 0  D06E          STO RT403     *THE CHECK REGISTER
0373 0  6303          LDX 3 3      *TO HEX FE0B
0374 00 07000003      STO L3 3
0376 0  73FF          MDX 3 -1
0377 0  70FC          MDX *-4
*****
0378 00 0C000500      XIO L NIOCC     TURN ALL TIMERS ON
*****
037A 00 C5000003      RTN42 LJ L1 3      GET TIMER CONTENTS
037C 0  9064          S RT403      *LOOP UNTIL TIMER
037D 0  4818          BSC +       *AND CHECK REG ARE
037E 0  70F8          MDX RTN42    *UNLIKE
*****
037F 0  F064          EOR RT406     CHECK IF DIFF IS 1
0380 0  4818          BSC +
0381 0  702C          MDX RTN43     COUNT OK CONTINUE
*****
** COUNT IN ERROR **
*****
0382 00 0C000502      XIO L FIOCC     TURN TIMERS OFF
0384 0  C060          LO  INTSW
0385 00 4C200389      BSC L +2,2
0387 00 0C0006C8      XIO L DSM      RESET DSM IF 1ST PAS

```

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Address	Operation	Register/Value	Description
0389 00	C057	LD RT403	CONVERT EXPECTED
038A 00	8059	A RT406	*COUNT
038B 00	04000629	STO L HEXWD	

0380 00	44000609	BSI L MEXCV	GO CONVERT SRC

038F 00	CC00067E	LD L MFXCD	
0391 00	DC00080E	STD L TMM14+18	EXPECTED WORD TO ERR MESSAGE

0393 00	C5000003	LD L1 /0003	ACTUAL COUNT TO
0395 00	04000629	STO L HEXWO	CONVERT

0397 00	44000609	BSI L MEXCV	GO CONVERT SRC

0399 00	CC00062E	LD L MFXCD	
039B 00	0C000808	STD L TMM14+12	ACTUAL COUNT TO ERR MESSAGE

0390 00	44000511	BSI L ERROR	PRINT ERROR SRC
039F 00	07FC	OC TMM14	
03A0 00	036F	DC RTN41	LOOP ERROR ADDRESS

03A1 00	C5000003	LD L1 3	SET PRESENT TIMER
03A3 00	003D	STO RT403	*CNT IN CHECK REG

03A4 00	C040	LD INTSW	
03A5 00	4C2003AB	BSC L *+4,Z	
03A7 00	0C00026A	XIO L MASK0	REMASK AFTER PRINT
03A9 00	0C00026C	XIO L MASK1	*ON 1ST PASS

03AB 00	0C000500	XIO L NIOCC	TURN TIMERS ON
03AD 00	7002	MOX RTN43+2	

03AE 00	740103E1	RTN43 MDX L RT403,1	SET CHECK REG FOR
03B0 00	1000	NOP	*NEXT COUNT
03B1 00	72FF	MDX 2 -1	
03B2 00	70C7	MDX RTN42	BRNCH NOT 1000 STEPS

03B3 00	0C000502	XIO L FIOCC	TURN TIMERS OFF

03B5 00	71FF	MDX 1 -1	
03B6 00	70B4	MDX RTN41-4	BRANCH NOT ALL TIMRS

03B7 00	C02D	LD INTSW	
03B8 00	4820	BSC Z	SKIP IF 1ST PASS
03B9 00	7019	MOX RTN44	BRNCH IF INT PASS

03BA 00	0C0006C8	XIO L OSW	
03BC 00	0C0001A8	XIO L UMSK0	UNMASK INTERRUPT
03BE 00	0C0001AA	XIO L UMSK1	LEVELS
03C0 00	740103E5	MDX L INTSW,1	INDICATE INT PASS
03C2 00	C010	LD RT402	SET PASS 2 IN ERROR
03C3 00	04000813	STD L TMM14+23	*MESSAGE
03C5 00	C40001B6	LD L TIBCN+1	SET TIMER C IOCC
03C7 00	04000500	STO L NIOCC	
03C9 00	6700FFFF	LX L3 /FFFF	GET TIMER INTERRUPT
03CB 00	6F000006	STX L3 6	LEVEL SET

03CC 00	0C000500	XIO L NIOCC	TIMER ON FOR INTRPT

03CF 00	3004	WAIT 4	WAIT FOR INTEPRUPT

03D0 00	0C000502	XIO L FIOCC	TURN TIMER OFF

C

88208170
88208180
88208190
88208200
88208210
88208220
88208230
88208240
88208250
88208260
88208270
88208280
88208290
88208300
88208310
88208320
88208330
88208340
88208350
88208360
88208370
88208380
88208390
88208400
88208410
88208420
88208430
88208440
88208450
88208460
88208470
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88208490
88208500
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88208660
88208670
88208680
88208690
88208700
88208710
88208720
88208730
88208740
88208750
88208760
88208770
88208780
88208790
88208800
88208810
88208820
88208830
88208840

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```

0302 0 7093          *      MDX      RTN40      RERUN ROUTINE
                      *
                      *      ** ROUTINE COMPLETE **
03D3 00 0C0006C8    RTN44 XIO L DSW      RESET DSW
0305 00 4C400307          BOSC L *      TURN INTERRUPT OFF
                      *
03D7 00 4C0001B1      LO L RTNND      PREPARE SEQUENCE
03D9 0 F009          EOR RT405      *CHECK
030A 00 040001B2      STO L SEQCK
                      *
03DC 00 4C0001BE      BSC L RTNRT      RETURN TO CONTROL
                      *
                      *      CONSTANTS
                      *
030E 0 03E6          RT400 DC      TRA4A      TIMER LEVEL
03DF 0 0100          RT401 OC      /0100      43 CODED 1
03E0 0 0200          RT402 OC      /0200      43 CODED 2
03E1 0 0000          RT403 DC      0
03E2 0 FE0B          RT404 DC      /FE0B      TIMER STARTING COUNT
03E3 0 0005          RT405 OC      5
03E4 0 0001          RT406 OC      1
03E5 0 0000          INTSW OC      0
                      *      INTERRUPT SWITCH
03E6 0 0000          TRA4A DC      0
03E7 00 0C0005C6      XIO L 1LSW      TIMER TRAP RCLTINF
03E9 00 4C8003E6          BSC I TRA4A
                      *
*****
*      ROUTINE SIX
*      CHECK STORAGE PRO-
*      TECTED TIMERS
*****
03E8 00 67000603    TIM05 LDX L3 SPVTP      LOAD TRAP ADDRESS
03ED 00 6F000008          STX L3 8
03EF 00 670004A4          LDX L3 TIMAI
03F1 00 6F8006CA          STX I3 INLVL
03F3 0 6103          LOX L 3
03F4 0 1010          SLA 16
03F5 0 005E          STO SPVCK      SET TIMER INOEX
                                CLEAR ALL TIMERS
                                *AND VIOLATE SWITCH
03F6 00 04000004          STO L 4
03F8 00 04000005          STO L 5
03FA 00 04000006          STO L 6
03FC 00 2C410004          STS L 4, /41
03FE 00 2C410005          STS L 5, /41      STORAGE PROTECT ALL
                                *TIMERS
0400 00 2C410006          STS L 6, /41
0402 00 4C0001B6          LO L TIBCN+1
0404 00 04000500          STO L NIOCC      INITIALIZE TIMER
                                *IOCC
0406 0 7005          MOX RTN50+5
                      *
0407 00 4C000500      RTN50 LC L NIOCC      MODIFY IOCC FOR
0409 0 1001          SLA 1      *NEXT TIMER
040A 00 04000500          STO L NIOCC
040C 0 1010          SLA 16
040D 0 0047          STO INCCY      CLEAR INCREMENT
                                *SWITCH
040E 0 620A          LOX 2 10
040F 00 4C0001B6          LO L1 TIBCN+1      SET PASS INOEX
0411 00 040008CE          STO L TMM24+11      SET TIMER NUMBER IN
0413 00 040008EC          STO L TMM25+18      *MESSAGES
                      *
0415 00 0C000500      RTN51 XIO L NIOCC      TURN TIMER ON
0417 00 44000504          BSI L DEL20      ALLOW FOR INCREMENT
0419 00 0C000502          XIO L FIOCC      TURN TIMER OFF
                      *
041B 00 4C000003      LO L1 3
041D 0 F037          EOR INCCY      CHECK IF TIMER
                                *INCREMENTED

```

88208850
88208860
88208870
88208880
88208890
88208900
88208910
88208920
88208930
88208940
88208950
88208960
88208970
88208980
88208990
88209000
88209010
88209020
88209030
88209040
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88209100
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88209120
88209130
88209140
88209150
88209160
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88209210
88209220
88209230
88209240
88209250
88209260
88209270
88209280
88209290
88209300
88209310
88209320
88209330
88209340
88209350
88209360
88209370
88209380
88209390
88209400
88209410
88209420
88209430
88209440
88209450
88209460
88209470
88209480
88209490
88209500
88209510
88209520

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```

041E 0 4820      BSC      Z
041F 0 7004      MOX      RTN52      OK CONTINUE

*****
0420 00 44000511  BSI L ERROR      PRINT ERROR      SRC
0422 0 08C3      OC      TMM24
0423 0 0415      OC      RTN51      LOOP ERROR RETURN

*****
0424 00 C5000003  RTN52 LO L1 3      MODIFY INCREMENT
0426 0 002E      STO      INCCT      *SWITCH
0427 0 72FF      MDX      2 -1      SKIP IF 10 PASSES
0428 0 70EC      MOX      RTN51
0429 0 71FF      MOX      1 -1      SKIP IF LAST TIMER
042A 0 70DC      MOX      RTN50      GO CK NEXT TIMER

*****
                                VIOLATE PROTECTED TIMERS
*****
042B 00 74010454  MDX L SPVCK,1      SET CHECK SWITCH
0420 0 610J      LOX      1 3      SET TIMER INOEX
042E 00 C5000186  RTN53 LO L1 T18CN+1      SET TIMER NUMBER
0430 00 D4000907  STO L TMM26+21      *IN MESSAGE

*****
0432 00 J5000003  STO L1 3      VIOLATE TIMER

*****
0434 00 44000511  BSI L ERROR      PRINT INTRP FAILED      SRC
0436 0 08F2      OC      TMM26
0437 0 042E      OC      RTN53      LOOP ERROR RETURN

*****
0438 0 71FF      RTN54 MOX 1 -1      SKIP IF ALL TIMERS
0439 0 70F4      MDX      RTN53      *CHECKED

*****
043A 00 670006CB  LDX L3 ERINT      RESTORE INTERRUPT
043C 00 6F000008  STX L3 8      *VECTOR
043E 00 C40001B1  LO L RTNNO      PREPARE SEQUENCE
0440 0 F012      EOR      RT500      *CHECK
0441 00 040001B2  STO L SEQCK
0443 00 2C400004  STS L 4,740      CLEAR PROTECTED
0445 00 2C400005  STS L 5,740      *TIMERS
0447 00 2C400006  STS L 6,740
0449 00 4C00018E  BSC L RTNRT      RETURN TO CONTROL

*****
                                INTERRUPT RETURN
*****
044B 0 C008      RTN55 LO SPVCK      CHECK IF SPV PASS
044C 0 4820      BSC      Z      SKIP IF ERROR
044D 0 70EA      MOX      RTN54

*****
044E 00 44000511  BSI L ERROR      PRINT ERROR      SRC
0450 0 08DA      OC      TMM25
0451 0 042E      OC      RTN53      LOOP ERROR RETURN

*****
0452 0 7001      MOX      RTN52

*****
                                CONSTANTS
*****
0453 0 0006      RT500 OC 6      CONSTANT 6
0454 0 0000      SPVCK OC 0      SPV CK SWITCH
0455 0 0000      INCCT OC 0      INCREMENT SWITCH

*****
                                SCOPING ROUTINE
*****

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```

0456 0 631B      TIMAN LDX 3 27      SE
0457 0 C042      LD      TIX01
0458 00 D7000007  TIMAA STO L3 7      SET ALL INTRs FOR
045A 0 73FF      MOX      3 -1      *RETURN TO MANUAL
045B 0 70FC      MOX      TIMAA      *ROUTINE

*****
                                REQUEST SW ENTRY FOR START
                                TIME CT
*****
045C 00 4400053C  BSI L LOG      GO PRINT REQUEST      SRC
045E 0 0815      OC      TMM15      AORS OF MSG
045F 0 3005      WT5      WAIT 5
0460 00 0C0001AC  XIO L BSW      READ BIT SWITCHS
0462 00 C40001B0  LD L BSWA      GET BIT SWITCHS
0464 0 D036      STO      TIX02

*****
                                REQUEST SW ENTRY FOR
                                NUMBER OF STEPS
*****
0465 00 4400053C  TIMAB BSI L LOG      GO PRINT REQUEST      SRC
0467 0 0825      OC      TMM16      AORS OF MSG
0468 0 3006      WT6      WAIT 6
0469 00 0C0001AC  XIO L BSW      READ BIT SWS
046B 00 C40001B0  LD L BSWA      GET BIT SWS
046D 0 002E      STO      TIX03
046E 0 4818      BSC      +
046F 0 70F5      MOX      TIMAB      WAS ENTRY ZERO
                                YES-REQ AGAIN

*****
                                REQUEST SW ENTRY FOR TIMER
*****
0470 00 4400053C  BSI L LOG      GO PRINT REQUEST      SRC
0472 0 0836      OC      TMM17      AORS OF MSG
0473 0 3007      WT7      WAIT 7
0474 00 0C0001AC  XIO L BSW      READ BIT SWS
0476 00 C40001B0  LD L BSWA      GET BIT SWS
0478 0 E029      AND      TIX07      SAVE BITS 4 5 AND 6
0479 0 180A      SRA      10      SET UP TIMER INOEX
047A 0 F028      EOR      TIX08
047B 0 0021      STO      TIX04

*****
                                SET UP TIMER TO DESIRED
                                VALUE
*****
047C 0 C020      TIMAL LO TIX04      GET TIMER ENTRY
047D 0 0001      STO      TIMAC+1
047E 00 67000000  TIMAC LOX L3 0      IX 3 = TIMER
0480 0 C01A      LD      TIX02      GET STARTING COUNT
0481 00 D7000003  STO L3 3      SET IN TIMER
0483 0 8018      A      TIX03      ADD DESIRED COUNTS
0484 0 0019      STO      TIX05      SAVE

*****
                                TURN ON TIMER FOR DESIRED
                                NUMBER OF COUNTS
*****
0485 00 C700049E  LD L3 TIX06-1      GET TURN ON CONSTANT
0487 00 D4000500  STO L NIOCC      SET IN IOCC
0489 00 0C000500  XIO L NIOCC      TURN ON TIMER

*****
                                CK TIMER FOR COUNT
*****
048B 00 C7000003  TIMAO LO L3 3      GET COUNTER CONTENTS
048D 0 9010      S      TIX05      SUB START + NO CNTS
048E 0 4820      BSC      Z      SKIP = DESIRED COUNT
048F 0 70FB      MOX      TIMAD      LOOP

*****
                                GOT DESIRED CT
*****
0490 00 0C000502  XIO L FIOCC      TURN OFF CTR

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```
*
04FA 0 0654      T1100 DC      INTRP      TRAP RTNS START ADRS
04F8 0 0004      T1101 DC      /0004
04FC 0 2000      T1102 DC      /2000
04F0 0 E000      T1103 DC      /E000
04FE 0 FFFF      T1104 DC      /FFFF
*
0500 0000      BSS E 0
*
0500 0 0000      N10CC DC      /0000      TURN TIMER ON 10CC
0501 0 0420      OC      /0420
*
0502 0 0000      F10CC OC      /0000      TURN TIMER OFF 10CC
0503 0 0420      OC      /0420
*
*          70 MILLISEC DELAY ROUTINE
*
0504 0 0000      DEL20 DC      0
0505 00 740001B4 MOX L SPEED,0      SKIP IF 2 USEC CORE
0507 0 7003      MOX      *+3
0508 00 67003680 LOX L3 /3680      2 USEC INDEX
050A 0 7002      MDX      *+2
050B 00 670032C8 LOX L3 /32C8      4 USEC INDEX
0500 0 73FF      MDX      3 -1
050E 0 70FE      MOX      *-2
050F 00 4C800504 BSC I DEL20
*
*****
*          ERROR ROUTINE
*****
0511 0 0000      ERROR DC      0
0512 00 44800511 LD I ERROR      SET MESSAGE ADDRESS SE
0514 0 000F      STO      ERROR+1      IN LOG CALL
0515 00 74010511 MDX L ERROR,1
0517 00 44800511 LD I ERROR      LOAD LOOP ON ERROR
0519 0 0020      STO      LPERR+1      ADDRESS
*
051A 00 7401053B MDX L ERRIO,1      SET ERR CALL INDCTOR
*
051C 00 0C0001AC X10 L BSW      READ BIT SWITCHES
051E 00 4C0001B0 LD L BSWA      CHECK IF BYPASS
0520 0 1802      SRA      2      *ERROR PRINT
0521 0 4804      BSC      E
0522 0 7002      MDX      ERROR2
*
*****
0523 0 4018      ERROR1 BSI LOG      GO PRINT ERROR SRC
0524 0 0000      DC      0
*****
0525 0 1010      ERROR2 SLA 16      CLEAR ERROR CALL
0526 0 0014      STO      ERRIO      INDICATOR
*
0527 00 4C0001B0 LD L BSWA      CHECK IF HALT ON ERR
0529 0 1801      SRA      1
052A 0 4804      BSC      E
052B 0 700B      MOX      WTR9      HALT BIT ON
*
052C 00 0C0001AC ERROR3 X10 L BSW      READ BIT SWITCHES
052E 00 4C0001B0 LD L BSWA      CHECK IF LOOP ON
0530 0 1803      SRA      3      *ERROR REQUESTED
0531 0 4804      BSC      E
0532 0 7006      MOX      LPERR      LOOP ERROR
*
0533 00 74010511 MDX L ERROR,1      ADD 1 TO RETURN
0535 00 4C800511 BSC I ERROR      RETURN TO USER SX
```

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```
*
*          ERROR HALT REQUESTED
*
0537 0 3009      WTR9 WAIT 9      ERROR HALT REQUESTED
0538 0 70F3      MDX      ERROR3
*
*          LOOP ERROR REQUESTED
*
0539 00 4C000000 LPERR BSC L 0
*
053B 0 0000      ERRID DC      0      ERROR CALL INDICATOR
*****
*          LOG ROUTINE
*****
053C 0 0000      LOG DC      0 SE
*
053D 0 681D      LOGO1 STX 3 LOGO6+1      SAVE IX 3
053E 00 0C00026A X10 L MASK0      MASK INTERRUPTS
0540 00 0C00026C X10 L MASK1
*
0542 00 4C0001B3 LO L OPINO      CK WHICH OUTPUT DVC
0544 00 4C180564 BSC L TWRTR,+      *BRANCH IF 1053/1816
*
0546 00 4C80053C LD I LOG      GET MESSAGE ADDRESS
0548 0 0055      STO      PRWRT      SET IN 10CC
*
0549 0 0850      LOGO2 X10 PRSNS      CHECK PRINTER READY
054A 00 4C040550 BSC L WTA,E      BRANCH IF NOT READY
054C 0 1801      SRA      1
054D 00 4C040552 BSC L WTR,E      BRANCH IF BUSY
054F 0 7004      MDX      LOGO5      READY AND NOT BUSY
*
0550 0 300A      WTA WAIT 10      NOT READY
0551 0 70F7      MDX      LOGO2      CHECK AGAIN
*
0552 0 3008      WTB WAIT 11      BUSY
0553 0 70F5      MDX      LOGO2      CHECK AGAIN
*
0554 0 0849      LOGO5 X10 PRWRT      OUTPUT MESSAGE
*
0555 0 0846      X10 PRSN      CHECK FOR OP COMPLY
0556 0 1002      SLA      2
0557 0 4810      BSC      -
0558 0 70FC      MOX      *-4
0559 0 0840      X10 PRSNS      RESET DSW
*
*          PRINTING COMPLETE
*
055A 00 67000000 LOGO6 LDX L3 0      RESTORE IX 3
055C 00 0C0001A8 X10 L UNSK0      UNMASK INTERRUPTS
055E 00 0C0001AA X10 L UNSK1
0560 00 7401053C MOX L LOG,1      BUMP RETURN
*
0562 00 4C80053C BSC I LOG      RETURN TO USER SX
*
0564 0 1010      TWRTR SLA 16
0565 0 0032      STO      WRDSW
0566 0 0839      X10 TWSNS      CHECK IF TYPEWRITER
0567 0 1005      SLA      5      READY
0568 0 180F      SRA      15
0569 00 4C180560 BSC L TWRTR,+
*
056B 0 300C      WTC WAIT 12      NOT READY
056C 0 70F9      MDX      TWRTR+2
*
056D 0 0028      TWR01 LD TWRTO      CARRIAGE RETURN AND
056E 0 002A      STO      IOARA      LINE SPACE TO IO ARA
```

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```

056F 0 0832      *      XIO      TWMRT      CARG RETURN/LINE SP      88213610
*                  *                  *                  *      88213620
0570 0 082F      *      XIO      TWSNS      HANG TILL NOT BUSY      88213620
0571 0 1808      *      SRA      11          *                  *      88213640
0572 0 4804      *      BSC      E          *                  *      88213650
0573 0 70FC      *      MDX      *-4        *                  *      88213660
*                  *                  *                  *      88213670
0574 0 6301      *      LDX      3 1          *                  *      88213680
0575 00 C480053C *      LD      1 LOG      *                  *      88213690
0577 0 0001      *      STO      TWR02+1    *                  *      88213700
*                  *                  *                  *      88213710
0578 00 C7000000 *      TWR02 LO L3 0          *                  *      88213720
057A 00 D4000504 *      STO L CODVD      *                  *      88213730
057C 0 F01A      *      EOR      TWR1      *                  *      88213740
057D 00 4C18055A *      BSC L LOG06,+    *                  *      88213750
*                  *                  *                  *      88213760
*                  *                  *                  *      88213770
*                  *                  *                  *      88213780
057F 00 440005A4 *      BSI L CODCV      *                  *      88213790
*                  *                  *                  *      88213800
*                  *                  *                  *      88213810
*                  *                  *                  *      88213820
*                  *                  *                  *      88213830
*                  *                  *                  *      88213840
*                  *                  *                  *      88213850
0584 0 081D      *      XIOWR XIO      TWMRT      WRITE CHARACTER      88213860
*                  *                  *                  *      88213870
0585 0 081A      *      XIOSN XIO      TWSNS      HANG ON BUSY      88213880
0586 0 1808      *      SRA      11          *                  *      88213890
0587 0 4804      *      BSC      E          *                  *      88213900
0588 0 70FC      *      MOX      XIOSN      BUSY      88213910
*                  *                  *                  *      88213920
*                  *                  *                  *      88213930
*                  *                  *                  *      88213940
*                  *                  *                  *      88213950
0589 0 CODE      *      LO      WRDSW      GFT 1/2 WORD SWITCH      88213960
058A 0 4804      *      BSC      E          *                  *      88213970
058B 0 7006      *      MOX      TWR03      GO SET UP NEXT WORD      88213980
*                  *                  *                  *      88213990
*                  *                  *                  *      88214000
*                  *                  *                  *      88214010
*                  *                  *                  *      88214020
058C 0 C00C      *      LD      IOARA      *                  *      88214030
058D 0 1008      *      SLA      8          *                  *      88214040
058E 0 000A      *      STO      IOARA      *                  *      88214050
058F 00 7401059B *      MOX L WRDSW,1    *                  *      88214060
0591 0 70F2      *      MOX      XIOWR      GO WRITE 2ND 1/2 WO      88214070
*                  *                  *                  *      88214080
*                  *                  *                  *      88214090
*                  *                  *                  *      88214100
0592 0 7301      *      TWR03 MOX 3 1          *                  *      88214110
0593 00 7401059B *      MOX L WRDSW,1    *                  *      88214120
0595 0 70E2      *      MOX      TWR02      *                  *      88214130
*                  *                  *                  *      88214140
*                  *                  *                  *      88214150
*                  *                  *                  *      88214160
*                  *                  *                  *      88214170
*                  *                  *                  *      88214180
*                  *                  *                  *      88214190
*                  *                  *                  *      88214200
*                  *                  *                  *      88214210
*                  *                  *                  *      88214220
059A 0 0000      *      BSS E 0          *                  *      88214230
*                  *                  *                  *      88214240
059A 0 0000      *      PRSNS OC /0000      *                  *      88214250
059B 0 3701      *      OC      /3701      *                  *      88214260
059C 0 0000      *      PRSN OC 0          *                  *      88214270
059D 0 3700      *      OC      /3700      *                  *      88214280
059E 0 0000      *      PRWRT OC /0000      *                  *
059F 0 3500      *      OC      /3500      *

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```

05A0 0 0000      TWSNS DC /0000      TYPEWTR SENSE IOCC      88214290
05A1 0 0F03      DC /0F03          *                  *      88214300
05A2 0 0599      TWMRT DC IOARA      TYPEWTR WRITE IOCC      88214310
05A3 0 0902      DC /0902          *                  *      88214320
*                  *                  *                  *      88214330
*                  *                  *                  *      88214340
*                  *                  *                  *      88214350
*                  *                  *                  *      88214360
*                  *                  *                  *      88214370
*                  *                  *                  *      88214380
05A4 0 0000      CODCV OC 0          *                  *      88214390
05A5 0 6927      STX 1 CODC4+1      SAVE INDEX REGS      SE      88214400
05A6 0 6A28      STX 2 CODC4+3      *                  *      88214410
05A7 0 6829      STX 3 CODC4+5      *                  *      88214420
*                  *                  *                  *      88214430
*                  *                  *                  *      88214440
05A8 0 1010      SLA 16          *                  *      88214450
05A9 0 0028      STO LHINO          *                  *      88214460
05AA 0 6300      LOX 3 0          *                  *      88214470
*                  *                  *                  *      88214480
05AB 0 C028      CODC1 LD CODWD      GET WORD TO CONVERT      88214490
05AC 0 1890      SRT 16          *                  *      88214500
05AD 0 C027      LD LHINO          *                  *      88214510
05AE 0 4820      BSC 2          *                  *      88214520
05AF 0 1088      SLT 8          *                  *      88214530
*                  *                  *                  *      88214540
05B0 0 1010      SLA 16          *                  *      88214550
05B1 0 1084      SLT 4          *                  *      88214560
05B2 0 0023      STO COD00          *                  *      88214570
05B3 00 659005D6 *      LOX 11 COD00      IX 1 = ZONE      88214580
*                  *                  *                  *      88214590
05B5 0 1010      SLA 16          *                  *      88214600
05B6 0 1084      SLT 4          *                  *      88214610
05B7 0 001E      STO COD00          *                  *      88214620
05B8 00 66800506 *      LOX 12 COD00      IX 2 = DIGIT      88214630
*                  *                  *                  *      88214640
05BA 00 C50005D9 *      LD L1 ZONE          *                  *      88214650
05BC 0 0001      STO CODC2+1      GET ZONE TABLE ADDRS      88214660
*                  *                  *                  *      88214670
05BD 00 C6000000 *      CODC2 LO L2 0          *                  *      88214680
05BF 00 D70005D7 *      STO L3 COD01      GET CONVERTED CODE      88214690
*                  *                  *                  *      88214700
05C1 0 C013      LD LHINO          *                  *      88214710
05C2 00 4C2005C8 *      BSC L CODC3,2      BRNCH IF RIGHT HALF      88214720
05C4 00 740105D5 *      MDX L LHIND,1      *                  *      88214730
05C6 0 7301      MDX 3 1          *                  *      88214740
05C7 0 70E3      MDX CODC1          *                  *      88214750
*                  *                  *                  *      88214760
05C8 0 C00E      CODC3 LO COD01      PACK CONVERTED CODES      88214770
05C9 0 1008      OR 8          *                  *      88214780
05CA 0 E800      STO COD02          *                  *      88214790
05CB 0 D008      STO CODWD          *                  *      88214800
*                  *                  *                  *      88214810
05CC 00 65000000 *      CODC4 LOX L1 0      RESTORE INDEX REGS      88214820
05CE 00 66000000 *      LOX L2 0          *                  *      88214830
05D0 00 67000000 *      LOX L3 0          *                  *      88214840
*                  *                  *                  *      88214850
05D2 00 4C8005A4 *      BSC 1 CODCV      RETURN TO USER      SX      88214860
*                  *                  *                  *      88214870
*                  *                  *                  *      88214880
*                  *                  *                  *      88214890
*                  *                  *                  *      88214900
05D4 0 0000      CODWD OC 0          *                  *      88214910
05D5 0 0000      LHINU DC 0          *                  *      88214920
05D6 0 0000      COD00 DC 0          *                  *      88214930
05D7 0 0000      COD01 DC 0          *                  *      88214940
05D8 0 0000      COD02 DC 0          *                  *      88214950
*                  *                  *                  *      88214960

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INTERVAL TIMER FUNCTION TEST

```

*
*      1443 TO 1816/1053 CODE
*      CONVERSION TABLES
*
05D9 0 05D0      ZONE DC      ZONE1 NO ZONE
05DA 0 05FA      DC      ZONE1 0 ZONE
05DB 0 05F3      DC      ZONE2 11 ZONE
05DC 0 05FD      DC      ZONE3 12 ZONE
*
05DD 0 0021      ZONE1 DC      /0021 SPACE
05DE 0 00FC      DC      /00FC 1
05DF 0 0008      DC      /0008 2
05E0 0 00DC      DC      /00DC 3
05E1 0 00F0      DC      /00F0 4
05E2 0 00F4      DC      /00F4 5
05E3 0 00DD      DC      /00DD 6
05E4 0 0004      DC      /0004 7
05E5 0 00E4      DC      /00E4 8
05E6 0 00E0      DC      /00E0 9
05E7 0 00C4      DC      /00C4 0
05E8 0 0000      ZONE1 DC      0
05E9 0 0000      DC      0
05EA 0 009A      DC      /009A S
05EB 0 009E      DC      /009E T
05EC 0 0082      DC      /0082 U
05ED 0 0086      DC      /0086 V
05EE 0 0092      DC      /0092 W
05EF 0 0096      DC      /0096 X
05F0 0 00A6      DC      /00A6 Y
05F1 0 00A2      DC      /00A2 Z
05F2 0 0021      DC      /0021 SPACE
05F3 0 0000      ZONE2 DC      0
05F4 0 007E      DC      /007E J
05F5 0 005A      DC      /005A K
05F6 0 005E      DC      /005E L
05F7 0 0072      DC      /0072 M
05F8 0 0076      DC      /0076 N
05F9 0 0052      DC      /0052 O
05FA 0 0056      DC      /0056 P
05FB 0 0066      DC      /0066 Q
05FC 0 0062      DC      /0062 R
05FD 0 0000      ZONE3 DC      0
05FE 0 003E      DC      /003E A
05FF 0 001A      DC      /001A B
0600 0 001E      DC      /001E C
0601 0 0032      DC      /0032 D
0602 0 0036      DC      /0036 E
0603 0 0012      DC      /0012 F
0604 0 0016      DC      /0016 G
0605 0 0026      DC      /0026 H
0606 0 0022      DC      /0022 I
0607 0 0086      DC      /0086 D ERROR
0608 0 0000      DC      /0000 PERIOD

```

```

*****
*      HEXADECIMAL TO 1443 CODED*
*      HEXADECIMAL CONVERSION *
*      ROUTINE
*****
0609 0 0000      HEXCV DC      0
060A 0 6A19      STX      2 HEXC2+1 SAVE INDEX 2 AND 3 SE
060B 0 681A      STX      3 HEXC2+3
060C 0 6204      LDX      2 4 CONVERSION INDEX
*
060D 0 C018      LD      HEXWD GET WORD TO CONVERT
060E 0 1890      SRT      16 SET A IN Q
060F 0 1D10      SLA      16
0610 0 1084      HEXC1 SLT      4 GET CHARACTER

```

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```

0611 0 D001      STO      HEXC1+3
0612 00 67000000 LOX      L3 0 SET CODE TABLE INDEX
*
0614 00 C7000630 LO      L3 CODEM GET CODED CHARACTER
0616 00 06000629 STO      L2 HEX00-1 AND SAVE
0618 0 1010      SLA      16
*
0619 0 72FF      MOX      2 -1 CHECK IF DONE
061A 0 70F5      MOX      HEXC1
*
0618 0 C011      LD      HEX00+3 PACK CODED WORDS
061C 0 1008      SLA      8
061D 0 E80E      DR      HEX00+2
061E 0 D00F      STO      HEXCD
061F 0 C00B      LO      HEX00+1
0620 0 1008      SLA      8
0621 0 E808      OR      HEX00
0622 0 D00C      STO      HEXCD+1
*
0623 00 66000000 HEXC2 LOX      L2 0 RESTORE INDEX
0625 00 67000000 LOX      L3 0
*
0627 00 4CB00609 BSC      I HEXCV RETURN TO USER SX
*
*      CONSTATS
*
0629 0 0000      HEXWD DC      0 WORD TO CONVERT
062A 0 3000      HEX00 OC      0
062B 0 0000      OC      0
062C 0 0000      DC      0
062D 0 D000      OC      0
*
062E 0000      BSS      E 0
*
062E 0 00C0      HEXC0 OC      0
062F 0 0000      OC      0
*
*      CONVERSION TABLE
*
0630 0 D00A      CUDCH OC      /000A 0
0631 0 0001      DC      /0001 1
0632 0 0002      DC      /0002 2
0633 0 0003      DC      /0003 3
0634 0 0004      DC      /0004 4
0635 0 0005      DC      /0005 5
0636 0 0006      DC      /0006 6
0637 0 0007      DC      /0007 7
0638 0 0008      DC      /0008 8
0639 0 0009      DC      /0009 9
063A 0 0031      DC      /0031 A
063B 0 0032      DC      /0032 B
063C 0 0033      DC      /0033 C
063D 0 0034      DC      /0034 D
063E 0 0035      DC      /0035 E
063F 0 0036      DC      /0036 F
*
*****
*      ROUTINE TWO INTERRUPT *
*      TRAP ROUTINE
*****
0640 0 000D      TRAP2 OC      0
0641 00 0C000502 XID      L FIOCC TURN TIMER OFF
*
0643 00 DC0006C6 XIO      L ILSW SENSE ILSW AND SAVE
0645 00 D500064F STO      L1 TRPD1-1
*
0647 00 0C0006C8 XIO      L DSW SENSE DSW AND SAVE

```

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```
0649 0 D009      STO      TRP02
064A 00 0C0006C8  *      XIO L DSW      CHECK IF DSW RESET
064C 0 4820      BSC      Z      ON FIRST SENSE
064D 0 300D      *      WTD WAIT 13      DSW DID NOT RESET
064E 00 4C400294  *      BOSC L RTN22      DSW IN ACCUMULATOR
0650 0 0000      *      TRP01 OC 0      RETURN TO CALLER
0651 0 0000      *      DC 0      ILSM
0652 0 0000      *      DC 0
0653 0 0000      *      TRPD2 DC 0      DSW
0654 0 000D      *      INTRP DC 0      TRAP ROUTINES TO DETERMINE
0655 0 405E      *      BSI 0      TIMER INTERRUPT LEVEL
0656 0 0A0A      *      DC 0      ** LEVEL 0 **
0657 0 000B      *      DC 0      CMTRP COMN RTN
0658 0 000D      *      DC 0      /0A0A 00 1443
0659 0 405A      *      DC 0      /000B INTRP ADRS
065A 0 0A01      *      DC 0      ** LEVEL 1 **
065B 0 00DC      *      DC 0      CMTRP COMN RTN
0660 0 0000      *      BSI 0      /0A01 01 1443
0661 0 4052      *      DC 0      /000C INTRP ADRS
0662 0 0A03      *      DC 0      ** LEVEL 2 **
0663 0 000E      *      DC 0      CMTRP COMN RTN
0664 0 0000      *      BSI 0      /0A02 02 1443
0665 0 404E      *      DC 0      /0000 INTRP ADRS
0666 0 0A04      *      DC 0      ** LEVEL 3 **
0667 0 000F      *      DC 0      CMTRP COMN RTN
0668 0 0000      *      BSI 0      /0A03 03 1443
0669 0 404A      *      DC 0      /000E INTRP ADRS
066A 0 0A05      *      DC 0      ** LEVEL 4 **
066B 0 0010      *      DC 0      CMTRP COMN RTN
066C 0 0000      *      BSI 0      /0A04 04 1443
066D 0 4046      *      DC 0      /000F INTRP ADRS
066E 0 0A06      *      DC 0      ** LEVEL 5 **
066F 0 0011      *      DC 0      CMTRP COMN RTN
0670 0 0000      *      BSI 0      /0A05 05 1443
0671 0 401E      *      DC 0      /0010 INTRP ADRS
0672 0 0106      *      DC 0      ** LEVEL 6 **
0673 0 001B      *      DC 0      CMTRP COMN RTN
0674 0 0000      *      BSI 0      /0A06 06 1443
0675 0 403E      *      DC 0      /0011 INTRP ADRS
0676 0 0106      *      DC 0      ** LEVEL 7 **
0677 0 001B      *      DC 0      CMTRP COMN RTN
0678 0 0014      *      DC 0      /001B INTRP ADRS
```

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```
0670 0 0000      *      DC 0
0671 0 4042      *      BSI 0      CMTRP COMN RTN
0672 0 0A07      *      DC 0      /0A07 07 1443
0673 0 0012      *      DC 0      /0012 INTRP ADRS
0674 0 0000      *      DC 0      ** LEVEL 8 **
0675 0 403E      *      BSI 0      CMTRP COMN RTN
0676 0 0A08      *      DC 0      /0A08 08 1443
0677 0 0013      *      DC 0      /0013 INTRP ADRS
0678 0 0000      *      DC 0      ** LEVEL 9 **
0679 0 403A      *      BSI 0      CMTRP COMN RTN
067A 0 0A09      *      DC 0      /0A09 09 1443
067B 0 0014      *      DC 0      /0014 INTRP ADRS
067C 0 0000      *      DC 0      ** LEVEL 10 **
067D 0 4036      *      BSI 0      CMTRP COMN RTN
067E 0 010A      *      DC 0      /010A 10 1443
067F 0 0015      *      DC 0      /0015 INTRP ADRS
0680 0 0000      *      DC 0      ** LEVEL 11 **
0681 0 4032      *      BSI 0      CMTRP COMN RTN
0682 0 0101      *      DC 0      /0101 11 1443
0683 0 0016      *      DC 0      /0016 INTRP ADRS
0684 0 0000      *      DC 0      ** LEVEL 12 **
0685 0 402E      *      BSI 0      CMTRP COMN RTN
0686 0 0102      *      DC 0      /0102 12 1443
0687 0 0017      *      DC 0      /0017 INTRP ADRS
0688 0 0000      *      DC 0      ** LEVEL 13 **
0689 0 402A      *      BSI 0      CMTRP COMN RTN
068A 0 0103      *      DC 0      /0103 13 1443
068B 0 0018      *      DC 0      /0018 INTRP ADRS
068C 0 0000      *      DC 0      ** LEVEL 14 **
068D 0 4026      *      BSI 0      CMTRP COMN RTN
068E 0 0104      *      DC 0      /0104 14 1443
068F 0 0019      *      DC 0      /0019 INTRP ADRS
0690 0 0000      *      DC 0      ** LEVEL 15 **
0691 0 4022      *      BSI 0      CMTRP COMN RTN
0692 0 0105      *      DC 0      /0105 15 1443
0693 0 001A      *      DC 0      /001A INTRP ADRS
0694 0 0000      *      DC 0      ** LEVEL 16 **
0695 0 401E      *      BSI 0      CMTRP COMN RTN
0696 0 0106      *      DC 0      /0106 16 1443
0697 0 001B      *      DC 0      /001B INTRP ADRS
```

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INTERVAL TIMER FUNCTION TEST

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```

      *
      *
      *
0698 0 0000          OC      0
0699 0 401A         BSI     CMTRP      COMN RTN
069A 0 0107          DC     /0107      17 1443
069B 0 D01C          DC     /001C      INTRP AORS

      *
      *
      *
069C 0 0000          DC      0
069D 0 4016         BSI     CMTRP      COMN RTN
069E 0 0108          DC     /0108      18 1443
069F 0 001D          DC     /001D      INTRP AORS

      *
      *
      *
06A0 0 0000          DC      0
06A1 0 4012         BSI     CMTRP      COMN RTN
06A2 0 FCE0          DC     /FCE0      19 1443
06A3 0 001E          DC     /001E      INTRP AORS

      *
      *
      *
06A4 0 0000          DC      0
06A5 0 400E         BSI     CMTRP      COMN RTN
06A6 0 020A          DC     /020A      20 1443
06A7 0 001F          DC     /001F      INTRP AORS

      *
      *
      *
06A8 0 0000          OC      0
06A9 0 400A         BSI     CMTRP      COMN RTN
06AA 0 0201          DC     /0201      21 1443
06AB 0 0020          DC     /0020      INTRP AORS

      *
      *
      *
06AC 0 0000          OC      0
06AD 0 4006         BSI     CMTRP      COMN RTN
06AE 0 0202          DC     /0202      22 1443
06AF 0 0021          DC     /0021      INTRP AORS

      *
      *
      *
06B0 0 0000          DC      0
06B1 0 4002         BSI     CMTRP      COMN RTN
06B2 0 0203          DC     /0203      23 1443
06B3 0 0022          DC     /0022      INTRP AORS

      *
      *
      *
                                COMMON TRAP ROUTINE USED
                                BY TIINT

06B4 0 0000          CMTRP OC      0
06B5 00 0C000502    XIO  L  FI0CC      TURN TIMERS OFF

      *
06B7 0 080E          XIO      ILSW      RESET ILSW

      *
06B8 0 080F          XIO      OSW       RESET DSX

      *
06B9 00 C48006B4    LD   I  CMTRP      GET INTRP LEVEL NUMB
06BA 00 D4000777    STO  L  TMK07+1C  SET IN PRINT MESSAGE

      *
06BD 00 740106B4    MDX  L  CMTRP,1
06BE 00 C48006B4    LD   I  CMTRP      GET INTRP ADDRESS
06BF 0 0008          STO      INLVL     SAVE
06C0 00 740103E5    MDX  L  INTSW,1   SET INTERRUPT SWITCH
06C1 00 4C4004CB    BOSC L  TIINS     RETURN TO MAIN FLOW SX

```

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INTERVAL TIMER FUNCTION TEST

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Address	Hex	Assembly	Comments	Address
06C6	0000	BSS E 0		88218370
06C6 0	0000	ILSW DC /0000	SENSE ILSW IOCC	88218380
06C7 0	0300	DC /0300		88218390
06C8 0	0000	DSW DC /0000	SENSE/RESET DSW IOCC	88218400
06C9 0	0721	DC /0721		88218410
06CA 0	0000	INLVL OC 0	TIMER INTERRUPT ADRS	88218420
06CB 0	0000	ERINT OC 0		88218430
06CC 0	C0FE	LD ERINT	SET I CTR IN Q REG	88218440
06CD 0	1890	SRT 16		88218450
06CE 00	0C0006C6	XIO L ILSW		88218460
06D0 0	300E	WTE WAIT 14	ILSW IN A REG	88218470
06D1 00	4C40012D	BOSC L TISRT		88218480
06D3 0	0000	SPVTP OC 0	INTERRUPT ENTRY IE	88218490
06D4 0	C0FE	LO SPVTP	I COUNT TO Q REG	88218500
06D5 0	1890	SRT 16		88218510
06D6 0	08EF	XIO ILSW	SENSE ILSW	88218520
06D7 0	F006	EOR SPVCN	CHECK FOR SPV	88218530
06D8 00	4C5B0448	8OSC L RTN55,←	BRANCH IF SPV	88218540
06DA 0	F003	EOR SPVCN	RESTORE ILSW	88218550
06DB 0	300F	WTF WAIT 15	NOT SPV ERROR	88218560
06DC 00	4C40012D	BOSC L TISRT	RESTART PROGRAM IX	88218570
06DE 0	2000	SPVCN DC /2000	SPV ILSW	88218580
06DF 0	0000	SVINT DC 0		88218590
06E0 0	0020	STO SVIO	SAVE ACCUMULATOR	88218600
06E1 00	0C0006C6	XIO L ILSW	RESET ILSW	88218610
06E2 00	74020700	MOX L SV7,2	SET PASS SWITCH	88218620
06E3 0	1010	SLA 16		88218630
06E4 0	0023	STO SV4	CLEAR AFEA CODE CNTR	88218640
06E5 0	C020	LD SV2		88218650
06E6 0	0023	STO SV6	SET IOCC IN USE SW	88218660
06E7 0	C010	LD SV1		88218670
06E8 0	0020	STO SV5	SET MODIFIER COUNTER	88218680
06E9 0	C01E	LD SV4		88218690
06EA 0	1008	SLA 11		88218700
06EB 0	E810	OR SV5	*BUILO IOCC	88218710
06EC 0	E810	OR SV6		88218720
06ED 0	001F	STO SVIO+1		88218730
06F0 0	0810	XIO SVIO	SENSE DSW AND RESET	88218740
06F1 00	74FF0708	MOX L SV5,-1		88218750
06F2 0	70F7	MOX SVIN1	BRANCH IF NOT ALL MO	88218760
06F3 00	7401070A	MOX L SV4,1	INCREMENT AREA CODE	88218770
06F4 0	C013	LO SV4		88218780
06F5 0	900E	S SV0	CHECK IF ALL AC USED	88218790
06F6 0	4808	BSC +	SKIP IF ALL AC USED	88218800
06F7 0	70EF	MOX SVIN0	GO SENSE WITH NXT AC	88218810
06FA 00	74FF070D	MOX L SV7,-1	SKIP IF SECOND PASS	88218820
06FC 0	7001	MOX +1		88218830

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```

06FD 0 7005          MOX          SVEXT-1
06FE 0 C00A          LD           SV3
06FF 0 D00C          STO          SV6          SET IOCC FOR PI
0700 0 1010          SLA          16
0701 0 D008          STO          SV4          SET AC FOR NEXT
0702 0 70E6          MOX          SVINO      *PASS
0703 0 C00A          LO           SVIO       RESTORE ACCUMULATOR
0704 00 4CC0060F     SVEXT BOSC I  SVINT     EXIT
*
*
*          ** CONSTANTS **
0706 0 001F          SV0          DC          /001F      NUMBER OF AREA CODES
0707 0 00FF          SV1          OC          /00FF      NUMBER OF MODIFIERS
0708 0 0701          SV2          OC          /0701      SENSE/RESET DSW
0709 0 0700          SV3          DC          /0700      SENSE/RESET PISW
070A 0 0000          SV4          DC          0          AREA CODE INDICATOR
070B 0 0000          SV5          DC          0          MODIFIER INDICATOR
070C 0 0000          SV6          OC          0          IOCC IN USE
070D 0 0000          SV7          DC          0          PASS SWITCH
070E 0 0000          BSS          E          0
070F 0 0000          SVIO         DC          0          SENSE DSW IOCC
070F 0 0000          DC          0

```

Address	Hex	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op417	
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BB219050
BB219060
BB219070
BB219080
BB219090
BB219100
BB219110
BB219120
BB219130
BB219140
BB219150
BB219160
BB219170
BB219180
BB219190
BB219200
BB219210
BB219220
BB219230
BB219240
BB219250
BB219260
BB219270
BB219280
BB219290
BB219300
BB219310
BB219320
BB219330
BB219340
BB219350
BB219360
BB219370
BB219380
BB219390
BB219400
BB219410
BB219420
BB219430
BB219440
BB219450
BB219460
BB219470
BB219480
BB219490
BB219500
BB219510
BB219520
BB219530
BB219540
BB219550
BB219560
BB219570
BB219580
BB219590
BB219600
BB219610
BB219620
BB219630
BB219640
BB219650
BB219660
BB219670
BB219680
BB219690
BB219700
BB219710
BB219720

INTERVAL TIMER FUNCTION TEST

0735	0	3326	DC	/3326	CO
0736	0	2427	DC	/2427	NP
0737	0	2335	DC	/2335	LE
0738	0	1335	DC	/1335	TE
0739	0	FFFF	DC	/FFFF	TERM
* TM04					
073A	0	000B	DC	/000B	WORD COUNT
073B	0	350A	OC	/350A	EO
073C	0	0A01	DC	/0A01	O1
073D	0	0000	DC	/0000	SPACE
073E	0	0000	DC	/0000	SPACE
073F	0	1235	DC	/1235	SE
0740	0	2814	OC	/2814	QU
0741	0	3525	DC	/3525	EN
0742	0	3335	DC	/3335	CE
0743	0	0035	DC	/0035	E
0744	0	2929	DC	/2929	RR
0745	0	2629	DC	/2629	OR
0746	0	FFFF	OC	/FFFF	TERM
* TM05					
0747	0	000E	DC	/000E	WORD COUNT
0748	0	350A	OC	/350A	EO
0749	0	0A02	DC	/0A02	O2
074A	0	0000	DC	/0000	SPACE
074B	0	0000	OC	/0000	SPACE
074C	0	1339	OC	/1339	TI
074D	0	2435	OC	/2435	ME
074E	0	2912	OC	/2912	RS
074F	0	0036	DC	/0036	F
0750	0	3139	DC	/3139	AI
0751	0	2300	OC	/2300	L
0752	0	1326	OC	/1326	TO
0753	0	0012	DC	/0012	S
0754	0	1335	OC	/1335	TE
0755	0	2700	OC	/2700	P
0756	0	FFFF	OC	/FFFF	TERM
* TM06					
0757	0	000E	DC	/000E	WORD COUNT
0758	0	350A	OC	/350A	EO
0759	0	0A03	DC	/0A03	O3
075A	0	0000	DC	/0000	SPACE
075B	0	0000	DC	/0000	SPACE
075C	0	1339	DC	/1339	TI
075D	0	2435	OC	/2435	ME
075E	0	2912	OC	/2912	RS
075F	0	0036	OC	/0036	F
0760	0	3139	OC	/3139	AI
0761	0	2300	OC	/2300	L
0762	0	1326	DC	/1326	TO
0763	0	0039	DC	/0039	I
0764	0	2513	DC	/2513	NT
0765	0	2927	DC	/2927	RP
0766	0	FFFF	DC	/FFFF	TERM
* TM07					
0767	0	0010	DC	/0010	WORD COUNT
0768	0	340A	DC	/340A	DO
0769	0	0A01	DC	/0A01	O1
076A	0	0000	DC	/0000	SPACE
076B	0	0000	DC	/0000	SPACE
076C	0	1339	OC	/1339	TI
076D	0	2435	DC	/2435	ME
076E	0	2912	DC	/2912	RS
076F	0	0026	OC	/0026	O
0770	0	2500	OC	/2500	N
0771	0	3925	DC	/3925	IN
0772	0	1329	OC	/1329	TR
0773	0	2700	DC	/2700	P
0774	0	2335	DC	/2335	LE

88219730
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88219800
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88219990
88220000
88220010
88220020
88220030
88220040
88220050
88220060
88220070
88220080
88220090
88220100
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88220200
88220210
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88220400

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0775 0 1535	DC	/1535	VE	88220410
0776 0 2300	DC	/2300	L	88220420
0777 0 0000	DC	/0000	LEVEL NUMBER	88220430
0778 0 FFFF	DC	/FFFF	TERM	88220440
* TMK08				
0779 0 0014	DC	/0014	WORD COUNT	88220450
077A 0 350A	DC	/350A	EO	88220460
077B 0 0A04	DC	/0A04	04	88220470
077C 0 0000	DC	/0000	SPACE	88220480
077D 0 0000	DC	/0000	SPACE	88220490
077E 0 2913	DC	/2913	RT	88220500
077F 0 2500	DC	/2500	N	88220510
0780 0 0200	DC	/0200	2	88220520
0781 0 1339	DC	/1339	TI	88220530
0782 0 2435	DC	/2435	ME	88220540
0783 0 2900	DC	/2900	R	88220550
0784 0 0000	DC	/0000	TIMER NUMBER	88220560
0785 0 0036	DC	/0036	F	88220570
0786 0 3139	DC	/3139	AI	88220580
0787 0 2335	DC	/2335	LE	88220590
0788 0 3400	DC	/3400	D	88220600
0789 0 1326	DC	/1326	TO	88220610
078A 0 0013	DC	/0013	T	88220620
078B 0 1429	DC	/1429	UR	88220630
078C 0 2500	DC	/2500	N	88220640
078D 0 2625	DC	/2625	OF	88220650
078E 0 FFFF	DC	/FFFF	TERM	88220660
* TMK09				
078F 0 0015	DC	/0015	WORD COUNT	88220670
0790 0 350A	DC	/350A	EO	88220680
0791 0 0A05	DC	/0A05	05	88220690
0792 0 0000	DC	/0000	SPACE	88220700
0793 0 0000	DC	/0000	SPACE	88220710
0794 0 2913	DC	/2913	RT	88220720
0795 0 2500	DC	/2500	N	88220730
0796 0 0200	DC	/0200	2	88220740
0797 0 1339	DC	/1339	TI	88220750
0798 0 2435	DC	/2435	ME	88220760
0799 0 2900	DC	/2900	R	88220770
079A 0 0000	DC	/0000	TIMER NUMBER	88220780
079B 0 0036	DC	/0036	F	88220790
079C 0 3139	DC	/3139	AI	88220800
079D 0 2335	DC	/2335	LE	88220810
079E 0 3400	DC	/3400	D	88220820
079F 0 1326	DC	/1326	TO	88220830
07A0 0 0013	DC	/0013	T	88220840
07A1 0 1429	DC	/1429	UR	88220850
07A2 0 2500	DC	/2500	N	88220860
07A3 0 2636	DC	/2636	OF	88220870
07A4 0 3600	DC	/3600	F	88220880
07A5 0 FFFF	DC	/FFFF	TERM	88220890
* TMK10				
07A6 0 0013	DC	/0013	WORD COUNT	88220900
07A7 0 350A	DC	/350A	EO	88220910
07A8 0 0A06	DC	/0A06	06	88220920
07A9 0 0000	DC	/0000	SPACE	88220930
07AA 0 0000	DC	/0000	SPACE	88220940
07AB 0 2913	DC	/2913	RT	88220950
07AC 0 2500	DC	/2500	N	88220960
07AD 0 0300	DC	/0300	3	88220970
07AE 0 1339	DC	/1339	TI	88220980
07AF 0 2435	DC	/2435	ME	88220990
07B0 0 2900	DC	/2900	R	88221000
07B1 0 0000	DC	/0000	TIMER NUMBER	88221010
07B2 0 0036	DC	/0036	F	88221020
07B3 0 3139	DC	/3139	AI	88221030
07B4 0 2335	DC	/2335	LE	88221040
07B5 0 3400	DC	/3400	D	88221050
				88221060
				88221070
				88221080

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07B6 0 1326	DC	/1326	TO	88221090
07B7 0 0039	DC	/0039	I	88221100
07B8 0 2513	DC	/2513	NT	88221110
07B9 0 2927	DC	/2927	RP	88221120
07BA 0 FFFF	DC	/FFFF	TERM	88221130
* BSS E 0				
07BC 0 0000	DC	/0000	WORD COUNT	88221140
07BD 0 350A	DC	/350A	EO	88221150
07BE 0 0A07	DC	/0A07	07	88221160
07BF 0 0000	DC	/0000	SPACE	88221170
07C0 0 0000	DC	/0000	SPACE	88221180
07C1 0 2913	DC	/2913	RT	88221190
07C2 0 2500	DC	/2500	N	88221200
07C3 0 0300	DC	/0300	3	88221210
07C4 0 1339	DC	/1339	TI	88221220
07C5 0 2435	DC	/2435	ME	88221230
07C6 0 2900	DC	/2900	R	88221240
07C7 0 0000	DC	/0000	TIMER NUMBER	88221250
07C8 0 3412	DC	/3412	DS	88221260
07C9 0 1600	DC	/1600	W	88221270
07CA 0 0000	DC	/0000	OSW IN	88221280
07CB 0 0000	DC	/0000	*ERROR	88221290
07CC 0 FFFF	DC	/FFFF	TERM	88221300
* BSS E 0				
07CE 0 0000	DC	/0000	WORD COUNT	88221310
07CF 0 350A	DC	/350A	EO	88221320
07D0 0 0A08	DC	/0A08	08	88221330
07D1 0 0000	DC	/0000	SPACE	88221340
07D2 0 0000	DC	/0000	SPACE	88221350
07D3 0 2913	DC	/2913	RT	88221360
07D4 0 2500	DC	/2500	N	88221370
07D5 0 0300	DC	/0300	3	88221380
07D6 0 1339	DC	/1339	TI	88221390
07D7 0 2435	DC	/2435	ME	88221400
07D8 0 2900	DC	/2900	R	88221410
07D9 0 0000	DC	/0000	TIMER NUMBER	88221420
07DA 0 0039	DC	/0039	I	88221430
07DB 0 2312	DC	/2312	LS	88221440
07DC 0 1600	DC	/1600	W	88221450
07DD 0 0000	DC	/0000	BLANK	88221460
07DE 0 0000	DC	/0000	ILSW	88221470
07DF 0 0000	DC	/0000	*ON INTRP	88221480
07E0 0 FFFF	DC	/FFFF	TERM	88221490
* BSS C 0				
07E2 0 0000	DC	/0000	WORD COUNT	88221500
07E3 0 0017	DC	/0017	EO	88221510
07E4 0 350A	DC	/350A	09	88221520
07E5 0 0A09	DC	/0A09	SPACE	88221530
07E6 0 0000	DC	/0000	SPACE	88221540
07E7 0 2913	DC	/2913	RT	88221550
07E8 0 2500	DC	/2500	N	88221560
07E9 0 0400	DC	/0400	4	88221570
07EA 0 1339	DC	/1339	TI	88221580
07EB 0 2435	DC	/2435	ME	88221590
07EC 0 2900	DC	/2900	R	88221600
07ED 0 0000	DC	/0000	TIMER NUMBER	88221610
07EE 0 1631	DC	/1631	WA	88221620
07EF 0 1200	DC	/1200	S	88221630
07F0 0 0000	DC	/0000	TIMER	88221640
07F1 0 0000	DC	/0000	*CONTENTS	88221650
07F2 0 0012	DC	/0012	S	88221660
07F3 0 3826	DC	/3826	HO	88221670

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07F4 0 1423	DC	/1423	UL
07F5 0 3400	DC	/3400	D
07F6 0 3235	DC	/3235	BE
07F7 0 0000	DC	/0000	BLANK
07F8 0 0000	DC	/0000	EXPECTED
07F9 0 0000	DC	/0000	*COUNT
07FA 0 FFFF	DC	/FFFF	TERM
* BSS E 0			
07FC 0 0000	DC	/0000	
07FD 0 0017	DC	/0017	WORD COUNT
07FE 0 350A	DC	/350A	EO
07FF 0 0A31	DC	/0A31	OA
0800 0 0000	DC	/0000	SPACE
0801 0 2913	DC	/2913	RT
0802 0 2500	DC	/2500	N
0803 0 0500	DC	/0500	S
0804 0 1300	DC	/1300	T
0805 0 0000	DC	/0000	TIMER NUMBER
0806 0 1631	DC	/1631	WA
0807 0 1200	DC	/1200	S
0808 0 0000	DC	/0000	ACTUAL
0809 0 0000	DC	/0000	*COUNT
080A 0 0035	DC	/0035	E
080B 0 1727	DC	/1727	XP
080C 0 3313	DC	/3313	CT
080D 0 3400	DC	/3400	D
080E 0 0000	DC	/0000	EXPECTED
080F 0 0000	DC	/0000	*COUNT
0810 0 0027	DC	/0027	P
0811 0 3112	DC	/3112	AS
0812 0 1200	DC	/1200	S
0813 0 0000	DC	/0000	PASS NUMBER
0814 0 FFFF	DC	/FFFF	TERM
* TMM15 DC /000E WORD COUNT			
0815 0 000E	DC	/000E	WORD COUNT
0816 0 330A	DC	/330A	CO
0817 0 0A02	DC	/0A02	O2
0818 0 0000	DC	/0000	SPACE
0819 0 0000	DC	/0000	SPACE
081A 0 3525	DC	/3525	EN
081B 0 1335	DC	/1335	TE
081C 0 2900	DC	/2900	R
081D 0 1213	DC	/1213	ST
081E 0 3129	DC	/3129	AR
081F 0 1339	DC	/1339	TI
0820 0 2537	DC	/2537	NG
0821 0 0033	DC	/0033	C
0822 0 2614	DC	/2614	OU
0823 0 2513	DC	/2513	NT
0824 0 FFFF	DC	/FFFF	TERM
* TMM16 DC /000F WORD COUNT			
0825 0 000F	DC	/000F	WORD COUNT
0826 0 330A	DC	/330A	CO
0827 0 0A03	DC	/0A03	O3
0828 0 0000	DC	/0000	SPACE
0829 0 0000	DC	/0000	SPACE
082A 0 3525	DC	/3525	EN
082B 0 1335	DC	/1335	TE
082C 0 2900	DC	/2900	R
082D 0 2514	DC	/2514	NU
082E 0 2432	DC	/2432	NB
082F 0 3529	DC	/3529	ER
0830 0 0026	DC	/0026	O
0831 0 3600	DC	/3600	F
0832 0 1213	DC	/1213	ST
0833 0 3527	DC	/3527	EP

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0834 0 1200	DC	/1200	S
0835 0 FFFF	DC	/FFFF	TERM
* TMM17 DC /000E WORD COUNT			
0836 0 000E	DC	/000E	WORD COUNT
0837 0 330A	DC	/330A	CO
0838 0 0A04	DC	/0A04	O4
0839 0 0000	DC	/0000	SPACE
083A 0 0000	DC	/0000	SPACE
083B 0 3525	DC	/3525	EN
083C 0 1335	DC	/1335	TE
083D 0 2900	DC	/2900	R
083E 0 0013	DC	/0013	T
083F 0 3924	DC	/3924	IM
0840 0 3529	DC	/3529	ER
0841 0 0025	DC	/0025	N
0842 0 1424	DC	/1424	UM
0843 0 3235	DC	/3235	BE
0844 0 2900	DC	/2900	R
0845 0 FFFF	DC	/FFFF	TERM
* TMM18 DC /001C WORD COUNT			
0846 0 001C	DC	/001C	WORD COUNT
0847 0 350A	DC	/350A	EO
0848 0 0A32	DC	/0A32	OB
0849 0 0000	DC	/0000	SPACE
084A 0 0000	DC	/0000	SPACE
084B 0 2913	DC	/2913	RT
084C 0 2500	DC	/2500	N
084D 0 0100	DC	/0100	I
084E 0 3426	DC	/3426	DO
084F 0 1432	DC	/1432	UB
0850 0 2335	DC	/2335	LE
0851 0 0039	DC	/0039	I
0852 0 2533	DC	/2533	NC
0853 0 2900	DC	/2900	R
0854 0 2636	DC	/2636	OF
0855 0 0039	DC	/0039	I
0856 0 0033	DC	/0033	C
0857 0 1329	DC	/1329	TR
0858 0 0034	DC	/0034	D
0859 0 1429	DC	/1429	UR
085A 0 3925	DC	/3925	IV
085B 0 3700	DC	/3700	G
085C 0 1324	DC	/1324	TM
085D 0 2900	DC	/2900	R
085E 0 0000	DC	/0000	TIMER NUMBER
085F 0 3312	DC	/3312	CS
0860 0 0033	DC	/0033	C
0861 0 1833	DC	/1833	YC
0862 0 2335	DC	/2335	LE
0863 0 FFFF	DC	/FFFF	TERM
* TMM19 DC /0014 WORD COUNT			
0864 0 0014	DC	/0014	WORD COUNT
0865 0 330A	DC	/330A	CO
0866 0 0A05	DC	/0A05	O5
0867 0 0000	DC	/0000	SPACE
0868 0 0000	DC	/0000	SPACE
0869 0 2935	DC	/2935	RE
086A 0 2731	DC	/2731	PA
086B 0 3929	DC	/3929	IR
086C 0 0036	DC	/0036	F
086D 0 3139	DC	/3139	AI
086E 0 2314	DC	/2314	LU
086F 0 2935	DC	/2935	RE
0870 0 0032	DC	/0032	B
0871 0 3536	DC	/3536	EF
0872 0 2629	DC	/2629	OR
0873 0 3500	DC	/3500	E
0874 0 3326	DC	/3326	CO

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0875 0 2513	DC	/2513	NT	88223130
0876 0 3925	OC	/3925	IN	88223140
0877 0 1439	DC	/1439	UI	88223150
0878 0 2537	DC	/2537	NG	88223160
0879 0 FFFF	DC	/FFFF	TERM	88223170
* TMM20				
087A 0 0000	OC	/0000	WORD COUNT	88223180
087B 0 350A	OC	/350A	EO	88223190
087C 0 0A33	DC	/0A33	OC	88223200
087D 0 0000	DC	0	SPACE	88223210
087E 0 0000	DC	0	SPACE	88223220
087F 0 3923	DC	/3923	IL	88223230
0880 0 2335	DC	/2335	LE	88223240
0881 0 3731	DC	/3731	GA	88223250
0882 0 2300	DC	/2300	L	88223260
0883 0 2913	DC	/2913	RT	88223270
0884 0 2500	DC	/2500	N	88223280
0885 0 3525	DC	/3525	EN	88223290
0886 0 1329	DC	/1329	TR	88223300
0887 0 180D	DC	/180D	Y	88223310
0888 0 FFFF	DC	/FFFF	TERM	88223320
* TMM21				
0889 0 0012	OC	/0012	WORD COUNT	88223330
088A 0 350A	DC	/350A	ED	88223340
088B 0 0A34	DC	/0A34	OD	88223350
088C 0 0000	DC	0	SPACE	88223360
088D 0 0000	DC	0	SPACE	88223370
088E 0 2913	DC	/2913	RT	88223380
088F 0 2500	DC	/2500	N	88223390
0890 0 0300	DC	/0300	3	88223400
0891 0 1339	DC	/1339	TI	88223410
0892 0 2435	DC	/2435	ME	88223420
0893 0 2900	DC	/2900	R	88223430
0894 0 0000	DC	0	TIMER NUMBER	88223440
0895 0 3923	DC	/3923	IL	88223450
0896 0 1216	DC	/1216	SW	88223460
0897 0 0016	DC	/0016	W	88223470
0898 0 3112	DC	/3112	AS	88223480
0899 0 0019	DC	/0019	Z	88223490
089A 0 3529	DC	/3529	ER	88223500
089B 0 2600	DC	/2600	O	88223510
089C 0 FFFF	DC	/FFFF	TERM	88223520
* TMM22				
089D 0 0017	DC	/0017	WORD COUNT	88223530
089E 0 350A	DC	/350A	ED	88223540
089F 0 0A35	DC	/0A35	OE	88223550
08A0 0 0000	DC	0	SPACE	88223560
08A1 0 0000	DC	0	SPACE	88223570
08A2 0 2913	DC	/2913	RT	88223580
08A3 0 2500	DC	/2500	N	88223590
08A4 0 0100	DC	/0100	I	88223600
08A5 0 3100	DC	/3100	A	88223610
08A6 0 2935	DC	/2935	RE	88223620
08A7 0 3700	DC	/3700	G	88223630
08A8 0 3338	DC	/3338	CH	88223640
08A9 0 3125	DC	/3125	AN	88223650
08AA 0 3735	DC	/3735	GE	88223660
08AB 0 3400	DC	/3400	D	88223670
08AC 0 2625	DC	/2625	ON	88223680
08AD 0 0013	DC	/0013	T	88223690
08AE 0 3924	DC	/3924	IM	88223700
08AF 0 2900	DC	/2900	R	88223710
08B0 0 0000	DC	/0000	TIMER NUMBER	88223720
08B1 0 3312	DC	/3312	CS	88223730
08B2 0 0033	DC	/0033	C	88223740
08B3 0 1833	DC	/1833	YC	88223750
08B4 0 2335	DC	/2335	LE	88223760
08B5 0 FFFF	DC	/FFFF	TERM	88223770

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0886 0 000B	DC	/000B	WORD COUNT	88223810
0887 0 310A	DC	/310A	AO	88223820
0888 0 0A03	DC	/0A03	OS	88223830
0889 0 0000	DC	0	SPACE	88223840
088A 0 0000	DC	0	SPACE	88223850
088B 0 2731	DC	/2731	PA	88223860
088C 0 1212	DC	/1212	SS	88223870
088D 0 0033	DC	/0033	C	88223880
088E 0 2624	DC	/2624	OM	88223890
088F 0 2723	DC	/2723	PL	88223900
0890 0 3513	DC	/3513	ET	88223910
0891 0 3500	DC	/3500	E	88223920
0892 0 FFFF	DC	/FFFF	TERM	88223930
* TMM23				
0893 0 0015	DC	/0015	WORD COUNT	88223940
0894 0 350A	DC	/350A	EO	88223950
0895 0 0A36	DC	/0A36	OF	88223960
0896 0 0000	DC	0	SPACE	88223970
0897 0 0000	DC	0	SPACE	88223980
0898 0 2913	DC	/2913	RT	88223990
0899 0 2500	DC	/2500	N	88224000
089A 0 0600	DC	/0600	6	88224010
089B 0 1339	DC	/1339	TI	88224020
089C 0 2435	DC	/2435	ME	88224030
089D 0 2900	DC	/2900	R	88224040
089E 0 0000	DC	0	TIMER NUMBER	88224050
089F 0 3631	DC	/3631	FA	88224060
08A0 0 3923	DC	/3923	IL	88224070
08A1 0 3534	DC	/3534	ED	88224080
08A2 0 0013	DC	/0013	T	88224090
08A3 0 2600	DC	/2600	O	88224100
08A4 0 3925	DC	/3925	IN	88224110
08A5 0 3329	DC	/3329	CR	88224120
08A6 0 3524	DC	/3524	EN	88224130
08A7 0 3525	DC	/3525	EN	88224140
08A8 0 1300	DC	/1300	T	88224150
08A9 0 FFFF	DC	/FFFF	TERM	88224160
* TMM24				
08AA 0 0016	DC	/0016	WORD COUNT	88224170
08AB 0 350A	DC	/350A	ED	88224180
08AC 0 010A	DC	/010A	IO	88224190
08AD 0 0000	DC	0	SPACE	88224200
08AE 0 0000	DC	0	SPACE	88224210
08AF 0 2913	DC	/2913	RT	88224220
08B0 0 2500	DC	/2500	N	88224230
08B1 0 0600	DC	/0600	6	88224240
08B2 0 1227	DC	/1227	SP	88224250
08B3 0 1500	DC	/1500	V	88224260
08B4 0 3925	DC	/3925	IN	88224270
08B5 0 1329	DC	/1329	TR	88224280
08B6 0 2713	DC	/2713	PT	88224290
08B7 0 0026	DC	/0026	O	88224300
08B8 0 2500	DC	/2500	N	88224310
08B9 0 1339	DC	/1339	TI	88224320
08BA 0 2435	DC	/2435	ME	88224330
08BB 0 2900	DC	/2900	R	88224340
08BC 0 0000	DC	0	TIMER NUMBER	88224350
08BD 0 3312	DC	/3312	CS	88224360
08BE 0 0033	DC	/0033	C	88224370
08BF 0 1833	DC	/1833	YC	88224380
08C0 0 2335	DC	/2335	LE	88224390
08C1 0 FFFF	DC	/FFFF	TERM	88224400
* TMM25				
08C2 0 0015	DC	/0015	WORD COUNT	88224410
08C3 0 350A	DC	/350A	EO	88224420
08C4 0 0101	DC	/0101	II	88224430
08C5 0 0000	DC	0	SPACE	88224440
* TMM26				
08C6 0 0015	DC	/0015	WORD COUNT	88224450
08C7 0 350A	DC	/350A	EO	88224460
08C8 0 0101	DC	/0101	II	88224470
08C9 0 0000	DC	0	SPACE	88224480

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08F6 0	0000	DC	0	SPACE
08F7 0	2913	DC	/2913	RT
08F8 0	2500	DC	/2500	N
08F9 0	0600	DC	/0600	6
08FA 0	2526	DC	/2526	NO
08FB 0	0039	DC	/0039	I
08FC 0	2513	DC	/2513	NT
08FD 0	2927	DC	/2927	RP
08FE 0	0026	DC	/0026	Q
08FF 0	2500	DC	/2500	N
0900 0	1539	DC	/1539	VI
0901 0	2623	DC	/2623	OL
0902 0	3113	DC	/3113	AT
0903 0	3500	DC	/3500	E
0904 0	1339	DC	/1339	TI
0905 0	2435	DC	/2435	ME
0906 0	2900	DC	/2900	R
0907 0	0000	DC	0	TIMER NUMBER
0908 0	FFFF	DC	/FFFF	TERM
090A	012D	END	TISRT	

8822468 88224690

INTERVAL TIMER FUNCTION TEST

CROSS REFERENCE LISTING

SYMBOL	VALUE	REFERENCES
ACS	04AC	01F2, 04A5
BSW	01AC	0147, 017E, 0460, 0469, 0474, 0492, 051C, 052C
BSWA	0180	0148, 0156, 017F, 01AC, 0462, 046B, 0476, 0494, 051E, 0527, 052E
CMTRP	06B4	0655, 0659, 065D, 0661, 0665, 0669, 0660, 0671, 0675, 0679, 0670, 0681, 0685, 0689, 0680, 0691, 0695, 0699, 0690, 06A1, 06A5, 06A9, 06A0, 0681, 0689, 068D, 063F
C00CV	05A4	057F, 05D2
C0DC1	05AB	05C7
C0DC2	05BD	058C
C0DC3	05C8	05C2
C0DC4	05CC	05A5, 05A6, 05A7
CODEH	0630	0614
C0DW0	0504	057A, 0581, 05AB, 05C8
C0000	0506	05B2, 0583, 0587, 0588
C0D01	0507	058F, 05C8
C0D02	05D8	05CA
CONST	01A1	0143, 0161, 0169, 0170
CTL01	013A	013E, 04EE
CTL02	0173	016E, 0180
CTL03	0179	0158
CTL04	016F	016C
CTL05	017E	0172
CTTBL	0345	02F5
OEL20	0504	022F, 0238, 0288, 0417, 04C5, 040E, 050F
DSW	06C8	0249, 0306, 033A, 0387, 038A, 0303, 04AB, 0647, 064A, 0688
ERINT	06C8	01A3, 043A, 06CC
ERR10	0538	051A, 0526
ERRDR	0511	01EC, 0208, 0256, 0250, 028F, 02A4, 0205, 031C, 0390, 0420, 0434, 044E, 04E7, 04F0, 0512, 0515, 0517, 0533, 0535
ERRSW	0213	0138, 01BF, 01EA, 01FA, 020F
ERR01	0523	0514
ERR02	0525	0522
ERR03	052C	0538
F10LC	0502	01E8, 01F0, 0231, 028D, 0304, 0331, 0382, 0383, 0300, 0419, 0490, 04C6, 040F, 0641, 0685
HEXCD	062E	02A0, 02E0, 030E, 0318, 036F, 0399, 061E, 0622
HEXCV	0609	029E, 0288, 030C, 0316, 038D, 0397, 0627
HEXC1	0610	0611, 061A
HEXC2	0623	060A, 0608
HEXW0	0629	029C, 02B9, 030A, 0314, 038B, 0395, 060D
HEX00	062A	0616, 0618, 0610, 061F, 0621
ILSW	06C6	03E7, 04A6, 0643, 06B7, 06CE, 06D6, 06E1
INCCT	0455	0400, 041D, 0426
INLVL	06CA	01C1, 0271, 0355, 03F1, 06C1
INTRP	0654	04FA
INTSW	03E5	0130, 035C, 0384, 03A4, 0387, 03C0, 0400, 06C2
IOARA	0599	056E, 0583, 058C, 058E, 05A2
LHIND	05D5	05A9, 05A0, 05C1, 05C4
LOG	053C	0179, 0183, 018A, 0191, 019D, 01FD, 02C5, 044C, 0465, 0470, 04E8, 04F5, 0523, 0546, 0560, 0562, 0575
LOG01	053D	0551, 0553
LOG02	0549	054F
LOG05	0554	0530, 057D
LOG06	055A	0519, 0532
LPERR	0539	0182
LPPGM	0188	015C, 018A, 01E2, 01F4, 0217, 025A, 0261, 02E0, 0320, 0362, 03A7, 053E
MASK0	026A	015E, 018C, 0218, 0258, 0262, 02E2, 0322, 0364, 03A9, 0540
MASK1	026C	0102, 0105, 0108, 01F4, 0222, 022D, 0264, 0267, 0278, 0278, 027E, 0289, 02E7, 02EA, 02ED, 02FA, 0327, 0369, 0378, 03AB, 03C7, 03C0, 0404, 0407, 040A, 0415, 0467, 0489, 048F, 04CA, 04CB, 04C0, 04D3, 04DD
NIOCC	0500	014F, 0542
OPIND	0183	

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PRSN	059C	0555
PRSN5	059A	0549, 0559
PRWRT	059E	0548, 0554
RESRT	01A6	013F
RTN	0196	0177
RTNNO	0181	012E, 0160, 0167, 0173, 0175, 0189, 0202, 024F, 02CA, 0335, 03D7, 043E
RTNRT	018E	0206, 0254, 02CF, 0340, 03DC, 0449
RTN00	0105	0104, 01F9
RTN01	01DE	01EF, 020E
RTN02	01F9	01E7
RTN03	0202	01FB
RTN04	0207	01F7
RTN05	01F8	01E1, 0211
RTN10	0224	0269
RTN11	0220	0246, 0259, 0260
RTN12	0256	0236
RTN13	0250	0240
RTN14	0264	0248
RTN15	0245	025C, 0263
RTN20	0278	027A, 02AD
RTN21	0286	0292, 02A7, 0208
RTN22	0294	044E
RTN23	02A8	0299, 02D9
RTN24	0287	02C9
RTN25	02CA	0285
RTN26	0201	02A8
RTN27	02AC	0293
RTN30	02EA	02E9, 0334
RTN31	02F4	031F, 0330
RTN32	02FC	0300, 032E
RTN33	032A	0303, 0329
RTN40	0366	03D2
RTN41	036F	03A0, 0386
RTN42	037A	037E, 0382
RTN43	03AE	0381, 03AD
RTN44	03D3	0389
RTN50	0407	0406, 042A
RTN51	0415	0423, 0428
RTN52	0424	041F, 0452
RTN53	042E	0437, 0439, 0451
RTN54	0438	044D
RTN55	0448	06D8
RTRN	0168	0190
RT00	0212	0204, 04C7
RT100	026F	0251
RT200	020A	0786
RT201	0208	0270
RT202	020C	0296, 02CC
RT300	0342	02F9, 02FE, 0308, 0326, 032A
RT301	0343	0337
RT302	0344	0301, 0309
RT400	030E	0357
RT401	030F	035E
RT402	03E0	03C2
RT403	03E1	0372, 037C, 0389, 03A3, 03AE
RT404	03E2	0371
RT405	03E3	0309
RT406	03E4	037F, 038A
RT500	0453	0440
SEGCK	0182	018E, 0205, 0252, 02C0, 0338, 030A, 0441
SNSWS	01AE	0168
SPEED	0184	014C, 0505
SPVCK	0454	03F5, 0428, 0448
SPVCN	060E	06D7, 06DA
SPVTP	0603	03EB, 06D4
SVEXT	0704	06FD
SVINT	060F	01A4, 0704

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SVINO	06E9	06F9, 0702
SVINI	06EB	06F3
SVIQ	070E	06E0, 06EF, 06F0, 0703
SVO	0706	06F7
SV1	0707	06E9
SV2	0708	06E7
SV3	0709	06FE
SV4	070A	06E6, 06EB, 06F4, 06F6, 0701
SV5	070B	06EA, 06ED, 06F1
SV6	070C	06E8, 06EE, 06FF
SV7	070D	06E3, 06FA
TIBCN	01B5	01C0, 0101, 010A, 0207, 0221, 0227, 0276, 0280, 02C1, 02D1, 02E5, 02F0, 0368, 03C5, 0402, 040F, 042E, 015A, 0486, 04EA, 04F3, 04F8, 04CF
TIINT	04A0	
TIIN1	04C4	
TIIN2	0400	
TIIN3	04F0	04E3
TIIN4	04EB	04F4
TIIN5	04C8	06C4
TIIN6	04F5	04D2
TI100	04FA	0480
TI101	04F8	0483
TI102	04FC	048E
TI103	04F0	0367, 04D4
TI104	04FE	0488
TIMA	0214	0132, 0134, 0136, 010E, 04C9
TIMAA	0458	0458
TIMAB	0465	046F
TIMAC	047E	047D
TIMAO	0488	048F
TIMAI	04A4	01B5, 03EF, 049A, 04AA
TIMAL	047C	0498
TIMAN	0456	017C, 0499
TIMER	019D	019C
TIM00	018A	0196
TIM01	0217	0197
TIM02	0270	019A
TIM03	02E0	0199
TIM04	0355	019A
TIM05	03EB	0198
TISRT	0120	0187, 0195, 01A7, 0601, 06DC, 0909
TIX01	049A	0457
TIX02	0498	0464, 0480
TIX03	049C	046D, 0483
TIX04	049D	0478, 047C
TIX05	049E	0484, 048D
TIX06	049F	0485
TIX07	04A2	0478
TIX08	04A3	047A
TMCNT	026E	0225, 0235, 023A, 023F, 0244
TMM01	0710	0178
TMM02	071F	04E0
TMM03	072C	0185
TMM04	073A	0193
TMM05	0747	04E9
TMM06	0757	04F2
TMM07	0767	04F7, 0688
TMM08	0779	0229, 0258
TMM09	078F	0228, 025F
TMM10	07A6	0282, 0291
TMM11	07BC	0284, 02A2, 02A6
TMM12	07CE	028F, 02C3, 02C7
TMM13	07E2	02F2, 0310, 031A, 031E
TMM14	07FC	0360, 036D, 0391, 0398, 039F, 03C3
TMM15	0815	045E
TMM16	0825	0467
TMM17	0836	0472
TMM18	0846	01DC, 01EE

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TMM19	0864	01FF
TMM20	087A	019F
TMM21	0889	0203, 02D7
TMM22	089D	0209, 0200
TMM23	08B6	018C
TMM24	08C3	0411, 0422
TMM25	08DA	0413, 0450
TMM26	08F2	0430, 0436
TRAP2	0640	02DB
TRA4A	03E6	03DE, 03E9
TRP01	0650	02A8, 02AE, 02B0, 02B2, 02B7, 0645
TRP02	0653	0294, 029A, 0649
TWRTA	0564	0544, 056C
TWRT0	0596	056D
TWRT1	0597	C57C
TWR01	0560	0569
TWR02	0578	0577, 0595
TWR03	0592	058B
TWSNS	05A0	0566, 057D, 0585
TWVRT	05A2	056F, 0586
UMSK0	01A8	0166, 01ED, 0248, 033C, 038C, 04C0, 055C
UMSK1	01AA	0167, 01CF, 0240, 033E, 038E, 04C2, 055E
WRDSW	0598	0565, 0569, 058F, 0593
WTA	0550	300A, 054A
WTB	0552	3008, 054B
WTC	0568	300C
WTO	0640	300D
WTE	0600	300E
WTF	0608	300F
WT1	0146	3001, 01AC, 02C0
WT2	0186	3002
WT3	0194	3003
WT4	03CF	3004

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1. PURPOSE
- THE INTERRUPT FUNCTION TEST CHECKS THE OPERATING CONDITION OF THE INTERRUPT CIRCUITS IN THE PROCESSOR/CONTROLLER. AUTOMATIC AND MANUAL INTERRUPTS, INTERRUPT PRIORITY, MASK REGISTER, DISABLE INTERRUPT SWITCH, AND TRACE MODE ARE TESTED.
2. PREREQUISITES
- 2.1 PROGRAM PREREQUISITES
- THE 1800 BASIC DIAGNOSTIC LOADER PROGRAM IS REQUIRED TO LOAD THE INTERRUPT FUNCTION TEST PROGRAM.
- 2.2 EQUIPMENT PREREQUISITES
- THE FOLLOWING EQUIPMENT IS REQUIRED,
- A. 1800 PROCESSOR/CONTROLLER.
B. 1442 CARD READ/PUNCH OR 1054 PAPER TAPE READER.
C. EITHER A 1053/1816, OR 1443 PRINTER.
3. USE PROCEDURE
- 3.1 PROGRAM LOADING
- REFER TO 1800 BASIC DIAGNOSTIC LOADER DOCUMENTATION PARAGRAPH 3.1, FOR LOADING INSTRUCTIONS.
- 3.2 PROGRAM OPERATION
- AFTER LOADING PROCESSOR STOPS AT WAIT 1 (B REG = 3001) WITH PROCESSOR STOPPED AT WAIT 1, PROCEED AS FOLLOWS -
- A. SET DISABLE INTERRUPT SWITCH TO OFF.
B. SET CHECK STOP SWITCH TO OFF.
C. SET WRITE STORAGE PROTECT BITS SWITCH TO YES.
D. AT THE CE PANEL, INSURE THAT THE CE INTERRUPT SWITCH IS SET TO INTERRUPT TO MAIN STORAGE.
E. SELECT PROGRAM OPTIONS FROM TABLE 1.
F. IF LOOP ROUTINE IS DESIRED, REFER TO TABLE 2.
G. DEPRESS START PUSHBUTTON. PROGRAM SHOULD START EXECUTION.
1. OPERATOR SHOULD PERFORM THE ACTIONS REQUESTED BY THE PROGRAM. THE ACTIONS TO BE PERFORMED ARE INDICATED BY A PRINTOUT.
2. IF THE OPTION'S OF LOOP ROUTINE OR LOOP PROGRAM ARE NOT SELECTED, THEN ROUTINE 1 THROUGH 6 WILL BE EXECUTED ONCE, FOLLOWED BY PRINTOUT A001 PROGRAM COMPLETE. THE PROGRAM STOPS AT WAIT 2 B REG = 3002. PRESSING START RETURNS PROGRAM TO WAIT 1.
3. IF A ROUTINE WAS SELECTED FOR LOOPING, THEN THAT ROUTINE WILL LOOP UNTIL THE PROGRAM IS TERMINATED OR THE LOOP ROUTINE FUNCTION IS CHANGED OR CLEARED. IF THE LOOP ROUTINE FUNCTION IS CHANGED, THEN THE NEW ROUTINE SELECTED WILL BE LOOPEO. IF THE LOOP ROUTINE FUNCTION IS CLEARED, THE PROGRAM WILL CONTINUE FROM THE PRESENT ROUTINE TO COMPLETION. FOR ROUTINE LOOPING WITH BYPASS MANUAL CHECKS, REFER TO NOTE 1 TABLE 2.
4. IF LOOP PROGRAM WAS SELECTED, AND THE MANUAL CHECKS WERE NOT BYPASSED, THEN ROUTINES 1 THROUGH 6 WILL BE RUN IN SEQUENCE IN LOOP FASHION.
5. IF LOOP PROGRAM WAS SELECTED AND THE MANUAL CHECKS ARE BYPASSED, THEN THE PROGRAM WILL RUN ROUTINE 1, 1ST PASS OF ROUTINE 2 AND ALL OF ROUTINE 3 AND 4 IN SEQUENTIAL LOOP FASHION. ROUTINE 5 AND 6 ARE NOT RUN UNDER THIS SETUP.

INTERRUPT FUNCTION TEST

TABLE 1
PROGRAM OPTIONS - DATA ENTRY SWITCHES

NOTE

THE OPTIONS OF SELECT 1443 AS OUTPUT DEVICE, BYPASS MANUAL CHECKS, AND NUMBER OF INTERRUPT LEVELS SELECTED WILL BE HONORED ONLY IF THEY ARE ENTERED WHILE THE PROCESSOR IS STOPPED AT WAIT 1 (B REG = 3001).

DATA ENTRY SWITCHES																OPTION DESCRIPTION	
* 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 *																	

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	1	HALT ON ERROR
.	2	BYPASS ERROR PRINT
.	3	LOOP ON ERROR
.	4	LOOP PROGRAM
.	5	USE 1443 AS OUTPUT DEVICE
.	6	BYPASS MANUAL CHECKS - NOTE 1.
.	7	BYPASS ROUTINE 3 PRIORITY PRINTOUT
0	0	12	12 INTERRUPT LEVELS
1	0	18	18 INTERRUPT LEVELS
0	1	24	24 INTERRUPT LEVELS

NOTE 1 -- MANUAL CHECKS INCLUDE DISABLE SWITCH, CONSOLE INTERRUPT																	
PUSHBUTTON, CE INTERRUPT BUTTON AND TRACE MODE OPERATION.																	
SWITCH 8 BYPASSES THESE CHECKS BY PREVENTING PASS 2 OF																	
ROUTINE 2 AND ALL OF ROUTINES 5 AND 6 FROM OPERATING.																	
SWITCH 8 IN CONJUNCTION WITH OPTION SWITCHES 7 AND 11 WILL																	
RUN THE PROGRAM IN A MODE ADAPTABLE TO SCOPING.																	

TABLE 2

SENSE / PROGRAM								OPTION DESCRIPTION	
* 0 1 2 3 4 5 6 7 *									

.	1	ROUTINE NUMBER TO LOOP. NUMBER MUST
X	X	X	2	BE IN HEX AND MAY BE CHANGED AT ANY
.	3	TIME.

NOTE --									
IF ROUTINE ENTRY IS 5 OR 6 AND THE PROGRAM OPTION TO									
BYPASS MANUAL CHECKS HAS BEEN SELECTED, THEN THE PROGRAM									
WILL LOOP SELECTING THAT ROUTINE BUT NOT ALLOWING IT TO									
EXECUTE. IF ROUTINE ENTRY IS 2 AND BYPASS MANUAL CHECKS IS									
SELECTED, THEN ROUTINE 2 WILL LOOP WITHOUT CHECKING									
THE DISABLE INTERRUPT SWITCH.									

3.3 PROGRAM TERMINATION

A NORMAL PROGRAM RUN TERMINATES BY PROGRAM STOPPING AT WAIT 2 FOLLOWING 'PROGRAM COMPLETE' PRINTOUT. DEPRESSION OF THE START PUSHBUTTON WILL CAUSE PROGRAM TO BRANCH TO WAIT 1 TO PERMIT PROGRAM TO BE REPEATED, IF DESIRED.

THE PROGRAM MAY ALSO BE TERMINATED AT ANY TIME BY DEPRESSING THE IMMED STOP PUSHBUTTON. DEPRESSING RESET AND START PUSHBUTTONS WILL BRANCH PROGRAM TO WAIT 1 TO PERMIT PROGRAM TO BE REPEATED.

INTERRUPT FUNCTION TEST

3.4 RESTART PROCEDURE

PRESS THE STOP, RESET AND START BUTTONS. THE PROGRAM SHOULD GO TO WAIT 1. IF THIS DOES NOT OCCUR, THE PROGRAM MUST BE RELOADED.

3.5 PROGRAM HALTS (IN LISTING)

PROGRAM WAITS ARE USED IN THIS PROGRAM, AND ARE IDENTIFIED BY REFERENCING THE B REG AND I REG.

A PROGRAM WAIT IS OF THE FORM,

30XX, I B REG).

A DESCRIPTION OF THE INDIVIDUAL PROGRAM WAITS CAN BE FOUND AT THE BEGINNING OF THE PROGRAM LISTING. A TYPICAL WAIT DESCRIPTION FOLLOWS. IT IS INCLUDED TO SHOW THE FORMAT OF THE LISTING, AND IT IS NOT NECESSARILY A DESCRIPTION OF AN ACTUAL WAIT.

3001 D 01E0 DC WAIT1+1 WAIT 1
*
*
* ONE OF THE METERED I/O UNITS
* FAILED TO SEND A RESPONSE
* INTERRUPT TO THE PROGRAM. INDEX
* REGISTER 1 WILL HAVE THE ADDRESS
* OF THE IOCC. THE AREA CODE WILL
* INDICATE THE I/O UNIT NOT READY.
* IF A 2401/02 DRIVE IS NOT READY,
* PROGRAM WILL NOT STOP AT WAIT 1.
*

B REG, (FIRST 4 DIGIT GROUP) CORRESPONDS TO B REG READING.

I REG, (SECOND 4 DIGIT GROUP) CORRESPONDS TO I REG READING.

4. PRINTOUTS

THE VARIOUS PRINTOUTS THAT MAY OCCUR DURING EXECUTION OF THIS PROGRAM FOLLOW.

4.1 STATUS MESSAGES

ADD1 PROGRAM COMPLETE

ONE PASS THROUGH THE PROGRAM HAS BEEN COMPLETED. DEPRESS START TO RETURN TO WAIT 1.

4.2 COMMAND MESSAGES

COD1 TURN DISABLE SW ON PUSH START

COMMAND TO OPERATOR.

COD2 TURN DISABLE SW OFF

COMMAND TO OPERATOR. PROGRAM SHOULD START EXECUTIONG WHEN SWITCH IS TURNED OFF. IF IT DOES NOT, PUSH START TO CONTINUE.

INTERRUPT FUNCTION TEST

C003 PUSH CE INTERRUPT BUTTON

COMMAND TO OPERATOR.

C004 PUSH CONS INTRP BUTTON

COMMAND TO OPERATOR. THE PROGRAM DELAYS FOR 1 MINUTE WAITING FOR THE BUTTON TO BE PUSHED. IF THE BUTTON IS NOT PUSHED, OR IF IT FAILS TO INTERRUPT, AN ERROR PRINTOUT WILL OCCUR.

C005 SET TRACE MODE PUSH START

COMMAND TO OPERATOR.

C006 SET RUN MODE PUSH START

COMMAND TO OPERATOR.

C007 SET DISABLE ON HIT CE AND CONS BTNS SET TRACE AND START

COMMAND TO OPERATOR TO CHECK CE AND CONSOLE INTERRUPT BUTTONS FOR INTERRUPTS WITH DISABLE INTERRUPT SWITCH ON.

C008 REPAIR FAILURE BEFORE CONTINUING

THIS PRINTOUT WILL FOLLOW ERROR MESSAGE E008. THE FAILURE INDICATED BY MESSAGE E008 CAN CAUSE LOSS OF PROGRAM CONTROL IF THE PROGRAM IS CONTINUED AND SHOULD THEREFORE BE REPAIRED BEFORE THE REMAINDER OF THE PROGRAM IS RUN.

4.3 DATA MESSAGES

D001 RTN OX PRIORITY CHECK

PRIORITY CHECK HEADING PRINTOUT. ROUTINE NUMBER CAN BE 3 OR 4.

REQ SEQUENCE XX XX XX XX XX
SRVC SEQUENCE XX XX XX XX XX

THESE TWO PRINTOUTS WILL OCCUR FOLLOWING THE HEADING PRINTOUT. REQUEST SEQUENCE INDICATES THE ORDER (READING FROM LEFT TO RIGHT) IN WHICH THE INTERRUPTS WERE RECEIVED BY THE TRAP ROUTINES. SERVICED SEQUENCE INDICATES (READING FROM LEFT TO RIGHT) THE ORDER IN WHICH THE INTERRUPTS WERE SERVICED BY THE TRAP ROUTINES.

THE FIRST INTERRUPT IS ISSUED BY ROUTINE 3, OR WITH EITHER THE TRACE MODE OR CE INTERRUPT BUTTON BY ROUTINE 5. THE FIRST TRAP ROUTINE ENTERED, WILL ISSUE AN INTERRUPT TO THE NEXT HIGHER LEVEL ETC, UNTIL LEVEL 00 IS REACHED. LEVEL 00 WILL CAUSE AN OP CODE VIOLATE TO INTERRUPT TO LEVEL ERROR. LEVEL ERROR WILL BE SERVICED IMMEDIATELY AND CAUSE THE SERVICING OF ALL NESTED INTERRUPTS IN THE OPPOSITE ORDER THAN THEY WERE RECEIVED.

IF THE FIRST INTERRUPT REQUEST FAILS, BOTH THE REQUEST AND SERVICED SEQUENCE PRINTOUTS WILL BE BLANK. IF A REQUEST FAILS TO INTERRUPT FROM A TRAP ROUTINE, THEN, THE LEVEL FROM WHICH THE REQUEST WAS ISSUED WILL BE THE FIRST ONE SERVICED.

INTERRUPT FUNCTION TEST

TYPICAL CORRECT PRINTOUTS FOLLOW.

FOR ROUTINE 3 AND SYSTEM WITH 12 INTERRUPT LEVELS,

REQ SEQUENCE 11 10 09 08 02 01 00 ER
SRVC SEQUENCE ER 00 01 02 08 09 10 11

FOR ROUTINE 3 AND SYSTEM WITH 18 INTERRUPT LEVELS,

REQ SEQUENCE 17 16 15 14 02 01 00 ER
SRVC SEQUENCE ER 00 01 02 14 15 16 17

FOR ROUTINE 3 AND SYSTEM WITH 24 INTERRUPT LEVELS,

REQ SEQUENCE 23 22 21 20 02 01 00 ER
SRVC SEQUENCE ER 00 01 02 20 21 22 23

FOR ROUTINE 5 AND SYSTEM WITH 12 INTERRUPT LEVELS,

REQ SEQUENCE CE IOR TR) 11 10 09 02 01 00 ER
SRVC SEQUENCE ER 00 01 02 09 10 11 CE IOR TR)

FOR ROUTINE 5 AND SYSTEM WITH 18 INTERRUPT LEVELS,

REQ SEQUENCE CE IOR TR) 17 16 15 02 01 00 ER
SRVC SEQUENCE ER 00 01 02 15 16 17 CE IOR TR)

FOR ROUTINE 5 AND SYSTEM WITH 24 INTERRUPT LEVELS,

REQ SEQUENCE CE IOR TR) 23 22 21 02 01 00 ER
SRVC SEQUENCE ER 00 01 02 21 22 23 CE IOR TR)

D002 CONSOLE BUTTON ON LEVEL XX ILSW BIT X

THIS PRINTOUT IS GIVEN BY ROUTINE 4 TO INDICATE THE LEVEL TO WHICH THE CONSOLE INTERRUPT PUSHBUTTON INTERRUPTS. THE ILSW BIT IS IN DECIMAL. IF NO ILSW BIT WAS ON, IT WILL BE INDICATED BY AN 'N'.

4.4 ERROR MESSAGES

E001 RTN OX LEVEL XX FAILED TO INTRP

ROUTINE NUMBER (RTN) CAN BE 2, 4, OR 5.

THIS ERROR PRINTOUT INDICATES THAT,

- A. THE LEVEL SPECIFIED FAILED TO RESPOND TO A PROGRAMMED INTERRUPT.
- B. IF RTN 03, LEVEL ER, OP CODE VIOLATE FAILED TO INTERRUPT.
- C. IF RTN 05, LEVEL ER, VIOLATING A STORAGE PROTECTED LOCATION FAILED TO CAUSE AN INTERRUPT.
- D. IF RTN 04, THE LEVEL SPECIFIED FAILED TO INTERRUPT AFTER THE MASK REGISTER WAS RESET OFF.

E002 RTN OX REQ XX GIVEN LEVL XX SRVCD

ROUTINE NUMBER CAN BE 2, 4, OR 5. PRINTOUT OCCURS WHEN THE INTERRUPT GIVEN INTERRUPTS TO THE WRONG LEVEL.

INTERRUPT FUNCTION TEST

E003 RTN 0X REQ XX INTRPO WITH DISABLE SW ON

ROUTINE NUMBER CAN BE 2 OR 5. THE REQUEST NUMBER SPECIFIED RESULTED IN AN INTERRUPT WHILE THE DISABLE INTERRUPT SWITCH WAS ON.

E004 RTN 0X WRONG ILSW ON ERR INTRP

IF ROUTINE 2, THE ILSW WAS WRONG FOR AN OP CODE VIOLATE. IF ROUTINE 5, THE ILSW WAS WRONG FOR A STORAGE PROTECT VIOLATION.

E005 SEQUENCE ERROR RTN 0X

ROUTINE NUMBER SPECIFIED SHOULD HAVE BEEN RUN, BUT WAS NOT. PUSH START TO GO TO WAIT 1.

E006 RTN 4 LEVEL XX INTRPO WHILE MASKED

THE LEVEL SPECIFIED INTERRUPTED WHILE THE MASK REGISTER WAS SET ON.

E007 RTN 2 INT XX ILSW NOT ZERO

THE ILSW FOR THE LEVEL INDICATED WAS NOT ZERO AFTER A PROGRAMMED INTERRUPT. FOLLOWING THIS PRINTOUT, IF THE PROGRAM IS IN A NORMAL PROGRAM RUN, IT WILL STOP AT WAIT 15 WITH ILSW IN A REGISTER.

E008 RTN 6 TRACE DID NOT INTRPT ON PASS XX

TEN PASSES ARE MADE THROUGH THIS ROUTINE. IF ANY PASS IS MADE WITHOUT RECEIVING A TRACE INTERRUPT THIS PRINTOUT WILL OCCUR, INDICATING THE PASS NUMBER.

E009 RTN 6 EXPECTED INTRPT FROM INSTRN XX GOT XX

THE ROUTINE HAS 10 INSTRUCTIONS WHICH SHOULD INTERRUPT IN SEQUENCE. IF THE SEQUENCE IS DESTROYED DUE TO A SKIPPED INSTRUCTION OR BECAUSE TRACE FAILED TO INTERRUPT, THE PRINTOUT WILL OCCUR, INDICATING THE INSTRUCTION THAT INTERRUPTED, AND THE INSTRUCTION THAT SHOULD HAVE INTERRUPTED.

THE INSTRUCTIONS USED IN THE ROUTINE FOLLOW.

1	LO	6	M
2	RTE	7	A
3	STO	8	BSC
4	S	9	NOP
5	EOR	10	MDX

E00A CONSOLE BUTTON FAILED

IF THE CONSOLE BUTTON DOES NOT CAUSE AN INTERRUPT, OR IF THE CONSOLE BUTTON IS NOT DEPRESSED WITHIN 1 MINUTE AFTER THE OPERATOR WAS REQUESTED TO DO SO, THIS PRINTOUT WILL OCCUR.

E00B RTN1 INTRPT NOT INHIBITED AFTER XXX

XXX REPRESENTS EITHER XIO OR BSI. THIS PRINTOUT INDICATES THAT AN INTERRUPT WAS NOT INHIBITED FOR 1 INSTRUCTION FOLLOWING THE EXECUTION OF AN XIO OR BSI INSTRUCTION.

INTERRUPT FUNCTION TEST

E00C ILLEGAL RTN ENTRY

AN ENTRY OF 7 WAS ENTERED IN THE SENSE PROGRAM SWITCHES. THIS IS AN INVALID ENTRY. PROGRAM RETURNS TO WAIT 1, B REG = 3001.

5. COMMENTS

THE INTERRUPT FUNCTION TEST CONSISTS OF A CONTROL ROUTINE AND SIX TESTING ROUTINES. INTERRUPT LEVELS 0 THROUGH 23 ARE CHECKED USING THE PROGRAMMED INTERRUPT FEATURE. LEVEL INTERNAL IS CHECKED BY ISSUING OP CODE VIOLATES AND STORAGE PROTECT VIOLATES. LEVELS TR AND CE ARE CHECKED OUT THROUGH THEIR ASSOCIATED HARDWARE. THE CONSOLE INTR PUSHBUTTON IS ALSO CHECKED.

THE CONTROL ROUTINE SEQUENCES THE TEST ROUTINES AND ACCOMPLISHES THE PROGRAM OPTIONS SPECIFIED BY THE OPERATOR.

ROUTINE 1 CHECKS TO INSURE THAT THE INTERRUPT IS DELAYED FOR 1 INSTRUCTION FOLLOWING THE EXECUTION OF AN XIO AND BSI. THE XIO CHECK IS MADE FIRST. THE BSI CHECK IS THEN MADE BY EXECUTING THE BSI IMMEDIATELY AFTER AN XIO. IF AN ERROR IS DETECTED, THE CE WILL RECEIVE A FIX COMMAND AND THE PROGRAM WILL GO TO WAIT 1.

ROUTINE 2 CHECKS THE BASIC OPERATION OF INTERRUPT LEVELS 0 THROUGH 23 AND INTERRUPT LEVEL INTERNAL. PASS 1 CHECKS FOR PROPER INTERRUPTING AND PASS 2 CHECKS FOR NO INTERRUPTS WITH THE DISABLE INTERRUPT SWITCH SET TO ON. LEVEL INTERNAL IS CHECKED WITH AN OP CODE VIOLATE, AND IS ALSO CHECKED FOR PROPER ILSW BIT ON. LEVELS 0 THROUGH 23 ARE CHECKED FOR NO ILSW BIT BEING ON AFTER PROGRAM INTERRUPT. EACH PASS IS RUN 500 TIMES.

ROUTINE 3 CHECKS LEVELS 0 THROUGH 23 AND LEVEL INTERNAL FOR PROPER PRIORITY SEQUENCING. THE METHOD USED TO CHECK PRIORITY SEQUENCING IS EXPLAINED IN PARAGRAPH 4. PRINTOUTS, UNDER THE 'RTN 0X PRIORITY CHECK' PRINTOUT.

ROUTINE 4 CHECKS THE MASK REGISTER. THE FIRST PASS CHECKS THAT THE MASK REG. CAN BE SET ON, AND THE SECOND PASS CHECKS THAT IT CAN BE RESET OFF. THE CHECK IS MADE 500 TIMES.

ROUTINE 5 CHECKS THE HARDWARE INTERRUPT FEATURES, THAT IS THE CONSOLE INTR AND CE INTERRUPT PUSHBUTTONS, AND THE TRACE MODE CIRCUITRY. PROPER INTERRUPTING, PRIORITY, AND INTERRUPT DISABLE ARE CHECKED. LEVEL INTERNAL ILSW IS CHECKED FOR PROPER BIT ON AFTER A STORAGE PROTECT VIOLATION.

ROUTINE 6 IS A CHECK OF TRACE MODE WHILE RUNNING A 10 INSTRUCTION ROUTINE. TEN PASSES ARE MADE THROUGH THIS ROUTINE. THE 10 INSTRUCTIONS USED ARE LISTED IN PARAGRAPH 4. PRINTOUTS, UNDER 'RTN05 EXPECTED INTRPT FROM INSTRN XX GOT XX' PRINTOUT.

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INTERRUPT FUNCTION TEST

028C	ABS ORG	/3001	88300010
			88300020
		** PROGRAM WAITS **	88300030
			88300040
			88300050
3001 0 0130	DC	WT1+1 WAIT 1	88300060
			88300070
		WAIT OCCURS AFTER PROGRAM	88300080
		HAS LOADED. PERFORM SETUP,	88300090
		ENTER DESIRED OPTIONS IN	88300100
		DATA ENTRY SWITCHES AND	88300110
		DEPRESS START.	88300120
			88300130
3002 0 0175	DC	WT2+1 WAIT 2	88300140
			88300150
		PROGRAM RAN TO COMPLETION.	88300160
		DEPRESSING START RETURNS	88300170
		PROGRAM TO WAIT 1.	88300180
			88300190
3003 0 0184	DC	WT3+1 WAIT 3	88300200
			88300210
		PROGRAM SEQUENCE ERROR.	88300220
		SUPERVISOR SECTION OF	88300230
		PROGRAM DETECTED AN ERROR	88300240
		IN ROUTINE SEQUENCING.	88300250
			88300260
3004 0 027F	DC	WT4+1 WAIT 4	88300270
			88300280
		ROUTINE 2 WAIT. TURN THE	88300290
		DISABLE INTERRUPT SWITCH	88300300
		ON AND DEPRESS THE START	88300310
		PUSHBUTTON.	88300320
			88300330
3005 0 0250	DC	WT5+1 WAIT 5	88300340
			88300350
		ROUTINE 2 WAIT. TURN THE	88300360
		DISABLE INTERRUPT SWITCH	88300370
		OFF. PROGRAM SHOULD START	88300380
		EXECUTION. IF IT DOES NOT	88300390
		(DUE TO INTERNAL INTERRUPT	88300400
		FAILURE)PRESS START BUTTON	88300410
		TO CONTINUE.	88300420
			88300430
3006 0 035C	DC	WT6+1 WAIT 6	88300440
			88300450
		ROUTINE 5 WAIT. WRONG ILSW	88300460
		WAS SENSED ON STORAGE	88300470
		PROTECT VIOLATE INTERRUPT.	88300480
		THE ILSW IS IN THE A REG.	88300490
		PUSH START TO CONTINUE.	88300500
			88300510
3007 0 0386	DC	WT7+1 WAIT 7	88300520
			88300530
		ROUTINE 5 WAIT. SET MODE	88300540
		SWITCH TO TRACE AND PRESS	88300550
		START. PROGRAM WILL CHECK	88300560
		TRACE INTERRUPT.	88300570
			88300580
3008 0 038E	DC	WT8+1 WAIT 8	88300590
			88300600
		ROUTINE 5 WAIT. SET MODE	88300610
		SWITCH TO RUN AND DEPRESS	88300620
		START BUTTON.	88300630
			88300640
3009 0 03CF	DC	WT9+1 WAIT 9	88300650
			88300660
		ROUTINE 5 WAIT. SET MODE	88300670
		SWITCH TO TRACE AND PRESS	88300680

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			* START. PROGRAM WILL MAKE	88300690
			* AN INTERRUPT PRIORITY	88300700
			* CHECK IN TRACE MODE	88300710
			* OPERATION.	88300720
				88300730
300A 0 0306	OC	WTA+1 WAIT A		88300740
				88300750
		ROUTINE 5 WAIT. SET MODE		88300760
		SWITCH TO RUN AND DEPRESS		88300770
		START BUTTON.		88300780
				88300790
300B 0 03E6	DC	WTB+1 WAIT B		88300800
				88300810
		ROUTINE 5 WAIT. DEPRESS		88300820
		C.E. INTERRUPT BUTTON.		88300830
		PROGRAM WILL MAKE AN		88300840
		INTERRUPT PRIORITY CHECK		88300850
		WITH THE C.E. INTERRUPT		88300860
		LEVEL.		88300870
				88300880
300C 0 03F7	OC	WTC+1 WAIT C		88300890
				88300900
		ROUTINE 5 WAIT. SET THE		88300910
		DISABLE INTERRUPT SWITCH		88300920
		ON, THEN DEPRESS THE C.E.		88300930
		INTERRUPT AND CONSOLE		88300940
		INTERRUPT BUTTONS. NO		88300950
		INTERRUPT SHOULD OCCUR.		88300960
		THEN SET TRACE MODE AND		88300970
		DEPRESS START.		88300980
				88300990
300D 0 040A	OC	WTD+1 WAIT D		88301000
				88301010
		ROUTINE 5 WAIT. SET THE		88301020
		MODE SWITCH TO RUN AND		88301030
		DEPRESS START.		88301040
				88301050
300E 0 040E	DC	WTE+1 WAIT E		88301060
				88301070
		ROUTINE 5 WAIT. TURN THE		88301080
		DISABLE INTERRUPT SWITCH		88301090
		OFF. PROGRAM SHOULD CONTI-		88301100
		NUE. IF IT DOES NOT(DUE		88301110
		TO DISABLE INTERRUPT IN-		88301120
		OPERATIVE* ,MEN PUSH START		88301130
		TO CONTINUE.		88301140
				88301150
300F 0 0450	OC	WTF+1 WAIT F		88301160
				88301170
		ROUTINE 6 WAIT. SET MODE		88301180
		SWITCH TO TRACE AND PRESS		88301190
		START BUTTON. PROGRAM WILL		88301200
		CHECK TRACE MODE OPERATION		88301210
				88301220
3010 0 0475	DC	WT10+1 WAIT 10		88301230
				88301240
		ROUTINE 6 WAIT. SET MODE		88301250
		SWITCH TO RUN AND DEPRESS		88301260
		START BUTTON.		88301270
				88301280
3011 0 051F	OC	WT11+1 WAIT 11		88301290
				88301300
		WAIT ON ERROR OPTION		88301310
		REQUESTED. DEPRESS START		88301320
		BUTTON TO CONTINUE.		88301330
				88301340
3012 0 0538	DC	WT12+1 WAIT 12		88301350
				88301360

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INTERRUPT FUNCTION TEST

```
*          1443 NOT READY. MAKE 1443
*          READY AND DEPRESS START.
*
3013 0 053A      DC      WT13+1      WAIT 13
*
*          1443 BUSY. THIS IS AN
*          ERROR CONDITION. REMEDY
*          CAUSE, THEN PUSH START TO
*          CONTINUE.
*
3014 0 0553      DC      WT14+1      WAIT 14
*
*          I816/I053 NUMBER 1 NOT
*          READY. MAKE READY AND PUSH
*          START TO CONTINUE.
*
3015 0 06C8      DC      WT15+1      WAIT 15
*
*          THIS WAIT WILL OCCUR
*          DURING ROUTINE 2 IF OPTION
*          BIT SWITCH 8 IS NOT ON, AND
*          AN ILSW ERROR IS DETECTED
*          ON A PROG. GENERATED INTER-
*          RUPT. THE ILSW IS IN THE
*          A REG. DEPRESS START TO
*          CONTINUE.
*
3016
012C 0 8300      DRG      300
                  DC      /8300      PID
*
*          * INTERRUPT FUNCT TEST *
*          * ** INTRP **          *
*
*          *****
*
*          CONTROL ROUTINE
*
0120 00 440004BE  START BSI L INTST      SET SPURIOUS INT ADR
*
012F 0 C864      LDD      CNCO2      SET RESTART ADDRESS
0130 00 0C000026  STD L /0026      IN LOCATION 26
0132 0 C863      LDD      CNCO3
0133 00 0C000000  STD L /0000
*
0135 0 1010      SLA      16          CLEAR BIT SWITCH
0136 0 0058      STO      BSW00      *READ IN AREA AND BY
0137 0 0061      STO      RUNSW      *PASS MAN CKS SWITCH
0138 00 04000428  STD L CN400      CLEAR TRACE INDICATO
013A 0 2C40      OC       /2C40      INSURE SP AREA IS
013B 0 0429      DC       CN401      *CLEAR
*
013C 0 3001      WT1      WAIT      1      ENTER PROG. OPTIONS
*
0130 0 084C      *        XIO      8SW0      READ BIT SWITCHES
*
013E 0 10A0      CON01  SLT      32      CLEAR A AND Q
013F 0 C052      LD       BSW00      GET OPTION ENTRY
0140 0 18C8      RTE      8
0141 0 1010      SLA      16
0142 0 1081      SLT      1
0143 0 0055      STO      RUNSW      SET RUN SWITCH
0144 0 1010      SLA      16
0145 0 1081      SLT      1
0146 0 D053      STD      OP1ND      SET OUTPUT DEVICE 10
*
*
*          DETERMINE NUMBER OF LEVELS
*
```

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INTEPRUPT FUNCTION TEST

```
D147 0 C04A      LO       8SW00      GET OPTION ENTRIES      88302050
D148 0 4810      BSC      -          SKIP IF BIT 0 ON      88302060
D149 0 7004      MOX      CTRL1      BRANCH ON NOT BIT 0 88302070
D14A 0 6311      LDX      3 17      SET LEVEL INDICATOR      88302080
D14B 0 684C      STX      3 LVSAV     *FOR 18 LEVELS      88302090
D14C 0 6302      LDX      3 2       SET INOEX FOR 18 LVL 88302100
D14D 0 700A      MOX      CTRL3      CONTINUE      88302110
D14E 0 1001      CTRL1  SLA      1    CHECK FOR BIT 1      88302120
D14F 0 4810      BSC      -          SKIP IF BIT 1 ON      88302130
D150 0 7004      MOX      CTRL2      BRANCH ON NOT BIT 1 88302140
D151 0 6317      LDX      3 23      SET LEVEL INDICATOR      88302150
D152 0 6845      STX      3 LVSAV     *FOR 24 LEVELS      88302160
D153 0 6304      LOX      3 4       SET INOEX FOR 24 LVL 88302170
D154 0 7003      MOX      CTRL3      CONTINUE      88302180
D155 0 6308      CTRL2  LOX      3 11 SET LEVEL INDICATOR      88302190
D156 0 6841      STX      3 LVSAV     *FOR 12 LEVELS      88302200
D157 0 6300      LOX      3 0       SET INOEX FOR 12 LVL 88302210
D158 0 6835      CTRL3  STX      3 LVLI X SAVE INDEX SETTING 88302220
*
0159 0 1010      CON06  SLA      16    CLEAR ROUTN. NUMBER 88302230
015A 0 0034      SIO      RTNNO      88302240
*
015B 0 082C      CNTRL  XIO      SNSWS READ SENSE SWITCHES 88302250
015C 0 1005      SLA      5          CHECK FOR LOOP RTN 88302260
015D 0 1800      SRA      13         88302270
015E 0 4808      BSC      +          SKIP IF LOOP ROUTINE 88302280
015F 0 7002      MOX      **2        88302290
0160 0 002E      STO      RTNNO      GO EXECUTE ROUTINE 88302300
0161 0 7006      MDX      CON05+2    88302310
0162 0 C02C      LD       RTNNO      88302320
0163 0 902C      S        SIX        CK IF ALL RTNS RUN 88302330
0164 0 4818      BSC      +-        88302340
0165 0 7006      MDX      CON03      ALL ROUTINES HAVE RN 88302350
0166 00 7401018F CON05  MDX L RTNNO,1 ADD 1 TO RTN.NO. 88302360
0168 00 6580018F LOX      11 RTNNO 88302370
016A 00 4080019A BSC      11 RTN-1 EXIT TO ROUTINE 88302380
*
*          ALL ROUTINES HAVE RUN 88302390
*
016C 0 0810      CON03  XIO      BSW0 READ BIT SWITCHES 88302400
016D 0 C024      LD       BSW00      GET BIT SWITCHES 88302410
016E 0 1804      SRA      4          CK LOOP PROGRAM 88302420
016F 0 4804      BSC      E          88302430
0170 0 70E8      MDX      CON06      LOOP PROGRAM 88302440
*
0171 00 44000523 BSI L LOG      PRINT PROGRAM      SRC 88302450
0173 0 09C9      DC       1NM07     IS COMPLETE      88302460
*
*          *****
*
0174 0 3002      WT2      WAIT      2      PROGRAM COMPLETE 88302470
0175 00 4C00012D BSC L START      PROGRAM RESTART 88302480
*
*          *** ROUTINE RETURN *** 88302490
*
0177 0 C019      RTNRT  LO      SEQCK SEQUENCE CHECK 88302500
0178 0 4818      BSC      +-        88302510
0179 0 70E1      MDX      CNTRL     CHECK OK 88302520
*
017A 00 678D018F LOX      13 RTNNO 88302530
017C 00 C700092F LD       L3 INLV+1 GET HEX.VALUE OF RTN 88302540
017E 00 D4000A2C STO L INM12+15 SET IN MESSAGE 88302550
*
0180 00 44000523 BSI L LOG      PRINT SEQUENCE      SRC 88302560
0182 0 0A1D      DC       INM12     ERROR 88302570
*
*          *****
*
```

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INTERRUPT FUNCTION TEST

```
0183 0 3003      WT3  WAIT  3      SEQUENCE ERROR      88302730
0184 00 4C000120  BSC  L  START                          88302740
*                                          88302750
*          CONTROL ROUTINE CONSTANTS      88302760
*                                          88302770
0186 00 00000000  OEC    0                          88302780
0188 0 0000      SNSWS OC /0000      REAO SENSE SW IOCC  88302790
0189 0 0760      OC /0760                          88302800
018A 0 0192      BSWO  OC BSWO0      REAO BIT SWITCH IOCC 88302810
0188 0 0240      OC /0240                          88302820
*                                          88302830
018C 0 0193      BSW1  OC BSWO1      REAO BIT SWITCH IOCC 88302840
018D 0 0240      OC /0240                          88302850
*                                          88302860
018E 0 0000      LVLIX OC 0          NO.OF LEVELS INOEX  88302870
018F 0 0000      RTNNO OC 0          ROUTINE NUMBER      88302880
0190 0 0006      SIX  OC 6          CONSTANT 6           88302890
0191 0 0000      SEQCK OC 0          SEQUENCE CHECK SAVE  88302900
*                                          88302910
0192 0 0000      BSWO0 DC 0          BIT SW. CONTROL DATA 88302920
0193 0 0000      BSWO1 OC 0          NO INTERRUPT LEVELS  88302930
*                                          88302940
0194 0 4C00      CNC02 OC /4C00      RESTART INSTRUCTIONS 88302950
0195 0 0120      OC START                          88302960
0196 0 7025      CNC03 OC /7025                          88302970
0197 0 4400      OC /4400                          88302980
0198 0 0000      LVSAV DC 0          NO-INTR.LVLS SAVE    88302990
0199 0 0000      RUNSW OC 0                          88303000
019A 0 0000      OPINO OC 0          OUTPUT DEVICE INOCTR 88303010
*                                          88303020
*          ROUTINE ADDRESSES              88303030
*                                          88303040
019B 0 01A6      RTN  OC INT00      ROUTINE 1             88303050
019C 0 01FE      OC INT01      ROUTINE 2             88303060
019D 0 0292      OC INT02      ROUTINE 3             88303070
019E 0 0285      OC INT03      ROUTINE 4             88303080
019F 0 0328      OC INT04      ROUTINE 5             88303090
01A0 0 0446      OC INT05      ROUTINE 6             88303100
01A1 0 01A2      OC INTER      INVALID ENTRY           88303110
*                                          88303120
*****                                88303130
01A2 00 44000523 INTER 8SI L LOG      PRINT INVALID ENTRY SRC 88303140
01A4 0 089F      OC INM25                                88303150
*****                                88303160
*                                          88303170
01A5 0 7096      MOX  WT1      RETURN TO WAIT 1          88303180
*                                          88303190
*****                                88303200
*          ROUTINE NUMBER ONE            88303210
*****                                88303220
*                                          88303230
01A6 00 0C000320 INT00 XIO L MASK0      MASK INTERRUPTS  88303240
01A8 00 0C000322  XIO L MASK1                                88303250
*                                          88303260
01AA 0 6318      LOX  3 27      SET INTERRUPT           88303270
01AB 00 C40001F5 LO  L VCTOR      *TRANSFER VECTOR      88303280
01AD 00 07000007 STO  L3 7                                88303290
01AF 0 73FF      MOX  3 -1                                88303300
01B0 0 70FC      MOX  *-4                                88303310
*                                          88303320
01B1 0 C848      LOO  XIO      SET UP MESSAGE           88303330
01B2 00 0C000886 STO  L INM23+22                                88303340
01B4 00 0C00028C LOO  L XIOCC      SET UP IOCC           88303350
01B6 00 0C00028A STO  L ISINT                                88303360
01B8 0 630C      LOX  3 12      SET INTRP INOEX         88303370
01B9 00 650001C5 LOX  L1 PL1+1      SET UP TRAP ROUTINEF  88303380
01BB 00 60000608 STX  L1 PLEXT+1      RETURN             88303390
01B0 00 0C000324 XIO  L UMSK0      UNMASK INTERRUPTS    88303400
```

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INTERRUPT FUNCTION TEST

```
018F 00 0C000326 XIO L UMSK1      88303410
01C1 00 0C00028A INO01 XIO L ISINT      ISSUE INTRP CHECK 88303420
01C3 0 1000      NOP      *POLL ON XIO 88303430
01C4 0 701C      PL1  MOX  FAIL      INTERRUPT FAILED 88303440
01C5 0 C030      LO  ICTR      CHECK FOR PROPER I 88303450
01C6 0 F030      EOR  XIOCK      *COUNT ON INTERRUPT 88303460
01C7 00 4C2001E9  8SC  L POLER,Z      BRANCH ON WRONG I CT 88303470
*                                          88303480
01C9 0 C832      LOO  BSI      SET UP MESSAGE 88303490
01CA 00 0C000886 STO  L INM23+22                                88303500
01CC 00 65000106 LOX  L1 PL2+1      SET UP TRAP ROUTINE 88303510
01CE 00 60000608 STX  L1 PLEXT+1      *RETURN           88303520
*                                          88303530
01D0 00 0C00028A XIO L ISINT      ISSUE INTRP CHECK 88303540
01D2 0 4000      BSI *      *FOLL ON BSI 88303550
01D3 0 1000      NOP                                88303560
01D4 0 1000      NOP                                88303570
01D5 0 7004      PL2  MOX  **4      BRNCH IF INTRP FAILO 88303580
01D6 0 C01F      LO  ICTR      CHECK FOR PROPER I 88303590
01D7 0 F020      EOR  BSICK      *COUNT ON INTERRUPT 88303600
01D8 00 4C2001E9  8SC  L POLER,Z      BRANCH ON WRONG I CT 88303610
*                                          88303620
01DA 0 C0B4      INO02 LO RTNNO      PREPARE SEQUENCE CK 88303630
01DB 0 F01D      EOR  CN001                                88303640
01DC 0 00B4      STO  SEQCK      88303650
01DD 00 440004BE  BSI  L INTST      SETUP XFER VECTORS SRC 88303660
*                                          88303670
01DF 00 4C000177  8SC  L RTNRT      RETURN TO CONTROL 88303680
01E1 00 C400028A FAIL  LO L ISINT      MODIFY IOCC FOR 88303690
01E3 0 1001      SLA  1      *NEXT INTERRUPT 88303700
01E4 00 0400028A STO  L ISINT                                88303710
01E6 0 73FF      MDX  3 -1                                88303720
01E7 0 7009      MOX  INO01      CONTINUE 88303730
01E8 0 70F1      MDX  INO02      END ROUTINE 88303740
*                                          88303750
01E9 00 650001A6 POLER LOX L1 INT00      SET LOOP ERROR 88303760
01EB 00 60000521 STX  L1 LPERR+1      *RETURN           88303770
*                                          88303780
*****                                88303790
01ED 00 440004F5  BSI  L ERROR      PRINT POLL ERROR SRC 88303800
01EF 0 0B70      OC INM23      MESSAGE TAG 88303810
01F0 00 44000523  BSI  L LOG      PRINT FIX COMMAND 88303820
01F2 0 0B89      OC INM24                                88303830
*****                                88303840
*                                          88303850
01F3 00 4C00013C  BSC  L WT1      GO TO WAIT 1 88303860
*                                          88303870
*          ROUTINE 1 CONSTANTS            88303880
*                                          88303890
01F5 0 0601      VCTOR OC POLL      TRANSFER VECTOR 88303900
01F6 0 0000      ICTR OC 0          I COUNT ON INTERRUPT 88303910
01F7 0 0104      XIOCK OC PL1      XIO CHECK CONSTANT 88303920
01F8 0 0105      BSICK OC PL2      8SI CHECK CONSTANT 88303930
01F9 0 0001      CN001 OC 1          CONSTANT 1 88303940
01FA 0 0000      BSS  E                                88303950
01FB 0 0017      XIO  DC /0017      X 88303960
01FC 0 3926      OC /3926      10 88303970
01FD 0 0032      BSI  OC /0032      B 88303980
01FE 0 1239      OC /1239      SI 88303990
*                                          88304000
*****                                88304010
*          ROUTINE NUMBER TWO            88304020
*****                                88304030
01FE 00 C4000931 INT01 LO L INLV+3      GET HEX 2 88304050
0200 00 04000975 STO  L INM03+7      SET ROUTINE NUMBER 88304060
0202 00 0400098A STO  L INM04+7      IN ERROR MESSAGES 88304070
0204 00 040009A1 STO  L INM05+7                                88304080
```

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```

0006 00 0400098B      *      STO L INM06+7
0209 00 440004AA      *      BSI L LVLST          GO SET INTERPT ADRSS
                                *
020A 00 C4000280      LD L CN101
020C 00 D400C281      STO L CN102          SET 1ST PASS SWITCH
020E 00 C4000284      LD L CN105          SET PASS SWITCH
0210 00 D4000285      STO L PSSW
                                *
0212 00 C4000282      RT100 LO L CN103
0214 00 D4000286      STD L ECKSW          SE LVL ER CHECK SW.
0216 00 65800198      LOX I1 LVSAV
0218 0 7101           MOX I1 I          IX 1 = NO.LEVELS +1
0219 00 66800198      LOX I2 LVSAV
021B 0 7201           MOX I2 I          IX 2 = NO.LEVELS +1
021C 00 67000226      LOX I3 RT101
021E 00 6F000521      STX I3 LPERR+1        SET LODP ERR RETURN
                                *
0220 00 6780018E      LDX I3 LVLIX          SET UP INTERRUPT
0222 00 CF00028C      LOO I3 XIOCC          IOCC
0224 00 0C00028A      STD L ISINT
                                *
0226 00 C600092E      RT101 LD L2 INLVT          SET REQUEST NUMBRER
0228 00 04000979      STO L INM03+11        IN FRRDR MESSAGES
022A 00 D400098E      STO L INM04+11
022C 00 040009A5      STO L INM05+11
022E 00 04000A4F      STO L INM14+10
                                *
0230 0 0859           XIO          ISINT          ISSUE PROGMO INTRPT
0231 0 1000           NOP
0232 00 440006D7      BSI L SERVC          PRGM OPERATION PROT SRC
                                *
0234 00 C4000281      RT109 LD L CN102          REQUEST OIO NOT INRP
0236 0 4B18           BSC ←
0237 0 7003           MDX          RT104          NDT 1ST PASS OK
                                *
*****
023B 00 440004F5      BSI L ERROR          PRINT REQUEST FAILED SRC
023A 0 096E           DC          INM03          TO INTERRUPT
*****
                                *
                                *      RETURN FROM TRAP ROUTINES
                                *
023B 0 71FF           PT104 MOX I -1          CK IF ALL LVLS DONE
023C 0 701C           MOX          RT105          NO
0230 00 74FF0286      MOX L ECKSW,-1        LEVEL ERROR CHECKED
023F 0 7027           MOX          RT106          NO
0240 00 74FF0285      MOX L PSSW,-1        SKIP IF 500 PASSES
0242 0 70CF           MOX          RT100
                                *
                                *      1ST PASS COMPLETE CHECK IF
                                *      MODE IS RUN WITHOUT STOPS
                                *
0243 00 C4000199      LJ L RUNSW          GET RUN SWITCH
0245 00 4C200250      BSC L RT11J,Z        RUN WITH OUT STOPS
                                *
                                *      RUN NORMAL PROGRAM MODE
                                *
0247 0 C039           LD          CN102          GET 1ST PASS SWITCH
0248 0 4820           BSC          Z
0249 0 702F           MOX          RT107          1ST PASS
                                *
                                *      ROUTINE ONE COMPLETE
                                *
024A 00 440004A0      BSI L NESTI          SET NEST ADDRESSES
                                *
*****
024C 00 44000523      BSI L LOG          PRINT TURN DISABLE SRC

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ADDRESS	DATA	OPERATION	STATUS	DESCRIPTION	ADDRESS
024E 0 095E	*****				8B304770
024F 0 3005	WT5 WAIT 5			TURN DISABLE SW OFF	8B304780
0250 00 C400018F	RT110 LD L RTNND			PREPARE SEQUENCE CK	8B304790
0252 0 902F	S CN103				8B304800
0253 00 04000191	STO L SEQCK				8B304810
0255 00 4400048E	BS1 L INTST			SET SPURIOUS INT ADR	8B304820
0257 00 4C000177	BSC L RTNRT			RETURN TO CONTROL	8B304830
0259 0 C030	RT105 LD 1SINT			GET IOCC ADDR5 WO	8B304840
025A 0 4808	BSC +			CK BIT 0 = 1	8B304850
025B 0 7004	MDX RT108			BIT 0 = 1	8B304860
025C 0 1001	SLA 1				8B304870
025D 0 D02C	STD 1SINT				8B304880
025E 0 72FF	MDX 2 -1			SET FOR NEXT LVL. NO	8B304890
025F 0 70C6	MDX RT101			CONTINUE	8B304900
0260 0 C02A	RT108 LO 1SINT+1			CLEAR BIT 15 FROM	8B304910
0261 0 901E	S CN101			IOCC CONTROL WORD	8B304920
0262 0 0028	STD 1SINT+1				8B304930
0263 0 C01C	LD CN101			SET BIT 13 IN IOCC	8B304940
0264 0 1022	SLA 2			ADDRESS WORD	8B304950
0265 0 0024	STO 1SINT				8B304960
0266 0 70F7	NOX RT105+5				8B304970
0267 0 72FF	RT106 MOX 2 -1				8B304980
0268 0 1000	NDP				8B304990
0269 00 C600092E	LD L2 INLVT			LEVEL ERROR	8B305000
026B 00 04000979	STO L INM03+11			SET IN ERROR	8B305010
026C 00 0400098E	STD L INM04+11			*MESSAGES	8B305020
026F 00 040009A5	STO L INM05+11				8B305030
0271 00 04000A4F	STO L INM14+10				8B305040
0273 00 67000277	LOX L3 RT106+16				8B305050
0275 00 6F000521	STX L3 LPERR+1			LOOP ERROR RETURN	8B305060
0277 0 0100	DC /0100			ILLEGAL OP CODE	8B305070
	*****				8B305080
	*****			ILLEGAL OP CODE OIO	8B305090
	*****			NOT INTERRUPT	8B305100
0278 0 70BB	MDX RT109				8B305110
0279 0 1010	RT107 SLA 16				8B305120
027A 0 0006	STO CN102			CLEAR 1ST PASS SW.	8B305130
027B 00 44000523	BS1 L LOG			PRINT TURN OISABLE	8B305140
0270 0 0949	OC INM01			SWITCH ON	8B305150
	*****				8B305160
027E 0 3004	WT4 WAIT 4			SET DISABLE SW ON	8B305170
027F 0 708E	MDX RT100-4			GO MAKE 2ND PASS	8B305180
	*****				8B305190
	*****			ROUTINE TWO CONSTANTS	8B305200
0280 0 0001	CN101 DC 1				8B305210
0281 0 0000	CN102 OC 0			1ST PASS SWITCH	8B305220
0282 0 0002	CN103 DC 2				8B305230
0283 0 0607	CN104 OC SERVC				8B305240
0284 0 01F4	CN105 DC /01F4			PASS CONSTANT	8B305250
0285 0 0000	PSSW OC 0			PASS SWITCH	8B305260
	*****				8B305270
	*****				8B305280
	*****				8B305290
	*****				8B305300
	*****				8B305310
	*****				8B305320
	*****				8B305330
	*****				8B305340
	*****				8B305350
	*****				8B305360
	*****				8B305370
	*****				8B305380
	*****				8B305390

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```
0286 0 0000      ECKSW DC      0      LEVEL ERR CHECK SW.      88305450
*
0288 00 00000000      DEC      0
028A 0 0000      ISINT DC      0      PRDGRAMED INTERRUPT      88305470
0288 0 0000      OC      0      IOCC      88305480
*
028C 0 0010      XIOCC OC      /0010      12 LEVELS OF INTRPT      88305490
0280 0 04A0      DC      /04A0      88305500
028E 0 1000      OC      /1000      18 LEVELS OF INTRP      88305510
028F 0 04A1      DC      /04A1      88305520
0290 0 0040      DC      /0040      24 LEVELS OF INTRPT      88305530
0291 0 04A1      OC      /04A1      88305540
*
*
*****
*
ROUTINE NUMBER THREE
*****
*
0292 00 C4000932      INT02 LD      L      INIVT+4      GET HEX 3      88305550
0294 00 D4000A5F      STO      L      INM15+7      SET IN LOG MESSAGE      88305560
*
0296 00 440004B8      *      BSI      L      PRIST      GO SET TRAP ADDRESS SRC      88305570
*
0298 00 6580018E      LDX      L1      LVLIX      SET IOCC FOR LOWEST      88305580
029A 00 C000028C      LDO      L1      XIOCC      INTERRUPT LEVEL      88305590
029C 0 0815      STO      CN200      88305600
*
029D 00 67800198      LDX      L3      LVSAY      NUMBER OF INTERRUPTS      88305610
029F 0 7302      MOX      3      2      TO BE GENERATED      88305620
*
02A0 0 6100      LOX      1      0      PRINT TABLE INDEX      88305630
02A1 0 6200      LOX      2      0      88305640
*
02A2 0 080F      XIO      CN200      ISSUE INTERRUPT      88305650
02A3 0 1000      NOP      88305660
*
*
RETURN FROM TRAP ROUTINES
*
02A4 00 440004E2      *      BSI      L      PRIPT      GO OUTPUT PRIO. SEQ      88305670
*
02A6 00 4400048E      *      BSI      L      INTST      SET SPURIOUS INT ADR      88305680
*
02A8 00 C400018F      *      LO      L      RTNNO      PREPARE SEQUENCE CK      88305690
02AA 0 9009      S      CN201      88305700
02AB 00 04000191      *      STO      L      SEQCK      88305710
*
02AD 00 4C000177      *      BSC      L      RTNRT      RETURN TO CONTROL      88305720
*
*
ROUTINE THREE CONSTANTS
*
02B0 00 00000000      *      DEC      0      88305730
02B2 0 0000      CN200 OC      0      INTERRUPT IOCC      88305740
02B3 0 0000      OC      0      88305750
*
02B4 0 0003      CN201 OC      3      CONSTANT 3      88305760
*
*****
*
ROUTINE NUMBER FOUR
*****
*
02B5 00 C4000933      INT03 LO      L      INLVT+5      GET HEX 4      88305770
02B7 00 D4000975      STO      L      INM03+7      SET RTN NO. IN ERROR      88305780
02B9 00 D400098A      STO      L      INM04+7      *MESSAGES      88305790
*
02B8 00 440004AA      *      BSI      L      LVLST      GO SET UP TRAP ADOORS      88305800
02B0 0 C0C6      LO      CN105      SET PASS SWITCH      88305810
02BC 0 00C6      STO      PSSW      88305820
*
```

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```
028F 00 C4000280      RT300 LD      L      CN101      88306130
02C1 0 0057      STO      CN300      SET 1ST PASS SW.      88306140
*
02C2 00 650002D0      LDX      L1      RT301      SET UP LOOP ON      88306150
02C4 00 60000521      STX      L1      LPERR+1      ERRDR RETURN      88306160
*
02C6 0 0859      XIO      MASK0      MASK UPPER LEVELS      88306170
02C7 0 085A      XIO      MASK1      MASK LOWER LEVELS      88306180
*
02C8 00 6780018E      RT306 LDX      L3      LVLIX      SET UP INITIAL IOCC      88306190
02CA 00 CF00028C      LOD      L3      XIOCC      FOR LOWEST LEVEL      88306200
02CC 0 0851      STD      CN301      88306210
*
02C0 00 67800198      LDX      L3      LVSAY      SET IX FOR NO.OF      88306220
02CF 0 7301      MOX      3      1      INTERRUPTS      88306230
*
02D0 00 C700092E      RT301 LO      L3      INLVT      GET REQUEST NUMBER      88306240
02D2 00 D4000979      STO      L      INM03+11      SET IN ERROR      88306250
02D4 00 0400098E      STO      L      INM04+11      *MESSAGES      88306260
02D6 00 04000A39      STO      L      INM13+11      88306270
*
02D8 0 0845      RT302 XIO      CN301      ISSUE INTERRUPT      88306280
02D9 0 1000      NOP      88306290
*
02DA 0 C03E      LD      CN300      GET 1ST PASS SWITCH      88306300
02DB 0 4808      BSC      +      88306310
02DC 0 7011      MDX      RT303      NOT 1ST PASS INTR ER      88306320
*
02DE 0 73FF      RT305 MDX      3      -1      CHECK IF ALL LVLS      88306330
02DE 0 701A      MOX      RT304      NO      88306340
02DF 0 C039      LO      CN300      YES      88306350
02E0 0 4820      BSC      2      88306360
02E1 0 7012      MDX      RT311      1ST PASS COMPL.CNTNU      88306370
02E2 00 74FF0285      MOX      L      PSSW,-1      SKIP IF 500 PASSES      88306380
02E4 0 70DA      MOX      RT300      88306390
*
*
ROUTINE COMPLETED
*
02E5 00 4400048E      *      BSI      L      INTST      SET SPURIOUS INT ADR      88306400
*
02E7 00 C400018F      *      LO      L      RTNNO      PREPARE SEQUENCE CK      88306410
02E9 0 9031      S      CN303      88306420
02EA 00 04000191      STO      L      SEQCK      88306430
*
02EC 00 4C000177      *      BSC      L      RTNRT      RETURN TO CONTROL      88306440
*
*
REQ OID NOT INTRP MASK OFF
*
02EE 00 440004F5      RT303 BSI      L      ERRDR      LOG REQUEST FAILED SRC      88306450
02F0 0 096E      DC      INM03      TO INTERRUPT      88306460
*****
*
02F1 00 44000609      *      BSI      L      SVINT      88306470
02F3 0 70E9      MDX      RT305      88306480
*
*
SET UP FOR 2ND PASS
*
02F4 0 1010      RT311 SLA      16      88306490
02F5 0 D023      STO      CN300      CLEAR 1ST PASS SWITC      88306500
*
02F6 0 0820      XIO      UMSK0      UNMASK UPPER LEVELS      88306510
02F7 0 082E      XIO      UMSK1      UNMASK LOWER LEVELS      88306520
*
02F8 0 70CF      MDX      RT306      GO MAKE SECONO PASS      88306530
*
02F9 0 C024      RT304 LD      CN301      MODIFY IOCC FOR      88306540
88306550
88306560
88306570
88306580
88306590
88306600
88306610
88306620
88306630
88306640
88306650
88306660
88306670
88306680
88306690
88306700
88306710
88306720
88306730
88306740
88306750
88306760
88306770
88306780
88306790
88306800
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INTERRUPT FUNCTION TEST

028C	ABS ORG	/3001	88300010 88300020 88300030 88300040 88300050 88300060 88300070 88300080 88300090 88300100 88300110 88300120 88300130 88300140 88300150 88300160 88300170 88300180 88300190 88300200 88300210 88300220 88300230 88300240 88300250 88300260 88300270 88300280 88300290 88300300 88300310 88300320 88300330 88300340 88300350 88300360 88300370 88300380 88300390 88300400 88300410 88300420 88300430 88300440 88300450 88300460 88300470 88300480 88300490 88300500 88300510 88300520 88300530 88300540 88300550 88300560 88300570 88300580 88300590 88300600 88300610 88300620 88300630 88300640 88300650 88300660 88300670 88300680
		** PROGRAM WAITS **	
3001 0 0130	DC	WT1+1 WAIT 1	
		WAIT OCCURS AFTER PROGRAM HAS LOADED. PERFORM SETUP, ENTER DESIRED OPTIONS IN DATA ENTRY SWITCHES AND DEPRESS START.	
3002 0 0175	DC	WT2+1 WAIT 2	
		PROGRAM RAN TO COMPLETION. DEPRESSING START RETURNS PROGRAM TO WAIT 1.	
3003 0 0184	DC	WT3+1 WAIT 3	
		PROGRAM SEQUENCE ERROR. SUPERVISOR SECTION OF PROGRAM DETECTED AN ERROR IN ROUTINE SEQUENCING.	
3004 0 027F	DC	WT4+1 WAIT 4	
		ROUTINE 2 WAIT. TURN THE DISABLE INTERRUPT SWITCH ON AND DEPRESS THE START PUSHBUTTON.	
3005 0 0250	DC	WT5+1 WAIT 5	
		ROUTINE 2 WAIT. TURN THE DISABLE INTERRUPT SWITCH OFF. PROGRAM SHOULD START EXECUTION. IF IT DOES NOT (DUE TO INTERNAL INTERRUPT FAILURE) PRESS START BUTTON TO CONTINUE.	
3006 0 035C	DC	WT6+1 WAIT 6	
		ROUTINE 5 WAIT. WRONG ILSW WAS SENSED ON STORAGE PROTECT VIOLATE INTERRUPT. THE ILSW IS IN THE A REG. PUSH START TO CONTINUE.	
3007 0 0386	DC	WT7+1 WAIT 7	
		ROUTINE 5 WAIT. SET MODE SWITCH TO TRACE AND PRESS START. PROGRAM WILL CHECK TRACE INTERRUPT.	
3008 0 038E	DC	WT8+1 WAIT 8	
		ROUTINE 5 WAIT. SET MODE SWITCH TO RUN AND DEPRESS START BUTTON.	
3009 0 03CF	DC	WT9+1 WAIT 9	
		ROUTINE 5 WAIT. SET MODE SWITCH TO TRACE AND PRESS	

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300A 0 03D6	OC	WTA+1 WAIT A	88300690 88300700 88300710 88300720 88300730 88300740 88300750 88300760 88300770 88300780 88300790 88300800 88300810 88300820 88300830 88300840 88300850 88300860 88300870 88300880 88300890 88300900 88300910 88300920 88300930 88300940 88300950 88300960 88300970 88300980 88300990 88301000 88301010 88301020 88301030 88301040 88301050 88301060 88301070 88301080 88301090 88301100 88301110 88301120 88301130 88301140 88301150 88301160 88301170 88301180 88301190 88301200 88301210 88301220 88301230 88301240 88301250 88301260 88301270 88301280 88301290 88301300 88301310 88301320 88301330 88301340 88301350 88301360
		START. PROGRAM WILL MAKE AN INTERRUPT PRIORITY CHECK IN TRACE MODE OPERATION.	
300B 0 03E6	OC	WTB+1 WAIT B	
		ROUTINE 5 WAIT. SET MODE SWITCH TO RUN AND DEPRESS START BUTTON.	
300C 0 03F7	OC	WTC+1 WAIT C	
		ROUTINE 5 WAIT. DEPRESS C.E. INTERRUPT BUTTON. PROGRAM WILL MAKE AN INTERRUPT PRIORITY CHECK WITH THE C.E. INTERRUPT LEVEL.	
300D 0 040A	DC	WTD+1 WAIT D	
		ROUTINE 5 WAIT. SET THE MODE SWITCH TO RUN AND DEPRESS START.	
300E 0 040E	DC	WTE+1 WAIT E	
		ROUTINE 5 WAIT. TURN THE DISABLE INTERRUPT SWITCH OFF. PROGRAM SHOULD CONTI- NUE. IF IT DOES NOT (DUE TO DISABLE INTERRUPT IN- OPERATIVE* WHEN PUSH START TO CONTINUE.	
300F 0 045D	OC	WTF+1 WAIT F	
		ROUTINE 6 WAIT. SET MODE SWITCH TO TRACE AND PRESS START BUTTON. PROGRAM WILL CHECK TRACE MODE OPERATION	
3010 0 0475	DC	WT10+1 WAIT 10	
		ROUTINE 6 WAIT. SET MODE SWITCH TO RUN AND DEPRESS START BUTTON.	
3011 0 051F	OC	WT11+1 WAIT 11	
		HALT ON ERROR OPTION REQUESTED. DEPRESS START BUTTON TO CONTINUE.	
3012 0 0538	DC	WT12+1 WAIT 12	

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INTERRUPT FUNCTION TEST

```
*          1443 NOT READY. MAKE 1443
*          READY AND DEPRESS START.
*
3013 0 053A          DC      WT13+1      WAIT 13
*
*          1443 BUSY. THIS IS AN
*          ERROR CONDITION. REMEY
*          CAUSE, THEN PUSH START TO
*          CONTINUE.
*
3014 0 0553          DC      WT14+1      WAIT 14
*
*          1816/1053 NUMBER 1 NOT
*          READY. MAKE READY AND PUSH
*          START TO CONTINUE.
*
3015 0 06C8          DC      WT15+1      WAIT 15
*
*          THIS WAIT WILL OCCUR
*          DURING ROUTINE 2 IF OPTION
*          BIT SWITCH 8 IS NOT ON, AND
*          AN ILSW ERROR IS DETECTED
*          ON A PROG. GENERATED INTER-
*          RUPT. THE ILSW IS IN THE
*          A REG. DEPRESS START TO
*          CONTINUE.
*
3016          ORG      300
012C 0 8300          OC      /8300      PID
*
*          * INTERRUPT FUNCT TEST *
*          * ** INTRP **          *
*          *
*          *****
*
*          CONTROL ROUTINE
*
0120 00 4400048E    START BSI L INTST      SET SPURIOUS INT ADR
*
012F 0 C864          LDD      CNC02      SET RESTART ADDRESS
0130 00 0C000026    STD      L /0026      IN LOCATION 26
0132 0 C863          LDD      CNC03
0133 00 0C000000    STD      L /0000
*
0135 0 1010          SLA      16          CLEAR BIT SWITCH
0136 0 0058          STO      BSW00      *READ IN AREA AND BY
0137 0 0061          STO      RUNSW     *PASS MAN CKS SWITCH
0138 00 04000428    STD      L CN400     CLEAR TRACE INDICATO
013A 0 2C40          DC       /2C40      INSURE SP AREA IS
013B 0 0429          DC       CN401      *CLEAR
*
013C 0 3001          WT1      WAIT      1      ENTER PROG.OPTIONS
*
013D 0 084C          *        XIO      BSW0      READ BIT SWITCHES
*
013E 0 10A0          CON01 SLT      32      CLEAR A AND Q
013F 0 C052          LD       BSW00      GET OPTION ENTRY
0140 0 18C8          RTE      8
0141 0 1010          SLA      16
0142 0 1081          SLT      1
0143 0 0055          STO      RUNSW     SET RUN SWITCH
0144 0 1010          SLA      16
0145 0 1081          SLT      1
0146 0 0053          STO      OPINO     SET OUTPUT DEVICE IO
*
*
*          DETERMINE NUMBER OF LEVELS
*

```

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INTERUPT FUNCTION TEST

```
0147 0 C04A          LO       BSW00      GET OPTION ENTRIES
0148 0 4810          BSC      -          SKIP IF BIT 0 ON
0149 0 7004          MOX      CTRL1      BRANCH ON NOT BIT 0
014A 0 6311          LOX      3 17      SET LEVEL INDICATOR
014B 0 684C          STX      3 LVSAV     *FOR 18 LEVELS
014C 0 6302          LDX      3 2        SET INDEX FOR 18 LVL
014D 0 700A          MOX      CTRL3      CONTINUE
014E 0 1001          CTRL1 SLA      1      CHECK FOR BIT 1
014F 0 4810          BSC      -          SKIP IF BIT 1 ON
0150 0 7004          MOX      CTRL2      BRANCH ON NOT BIT 1
0151 0 6317          LOX      3 23      SET LEVEL INDICATOR
0152 0 6845          STX      3 LVSAV     *FOR 24 LEVELS
0153 0 6304          LOX      3 4        SET INDEX FOR 24 LVL
0154 0 7003          MOX      CTRL3      CONTINUE
0155 0 6308          CTRL2 LOX      3 11      SET LEVEL INDICATOR
0156 0 6841          STX      3 LVSAV     *FOR 12 LEVELS
0157 0 6300          LOX      3 0        SET INDEX FOR 12 LVL
0158 0 6835          CTRL3 STX      3 LVLIX  SAVE INDEX SETTING
*
0159 0 1010          CON06 SLA      16
015A 0 0034          SFO      RTNNO      CLEAR ROUTN. NUMBER
*
015B 0 082C          CNTRL XIO      SNSWS  READ SENSE SWITCHES
015C 0 1005          SLA      5          CHECK FOR LOOP RTN
015D 0 1800          SRA      13
015E 0 4808          BSC      4          SKIP IF LOOP ROUTINE
015F 0 7002          MOX      *+2
0160 0 002E          STO      RTNNO
0161 0 7006          MOX      CON05+2     GO EXECUTE ROUTINE
0162 0 C02C          LO       RTNNO
0163 0 902C          S        SIX
0164 0 4818          BSC      +-
0165 0 7006          MOX      CON03      ALL ROUTINES HAVE RN
0166 00 7401018F    CON05 MOX      L RTNNO,1  A00 1 TO RTN.NO.
0168 00 6580018F    LOX      11 RTNNO
016A 00 4080019A    BSC      11 RTN-1     EXIT TO ROUTINE
*
*          ALL ROUTINES HAVE RUN
*
016C 0 0810          CON03 XIO      BSW0      READ BIT SWITCHES
016D 0 C024          LD       BSW00      GET BIT SWITCHES
016E 0 1804          SRA      4          CK LOOP PROGRAM
016F 0 4804          BSC      E
0170 0 70E8          MOX      CON06      LOOP PROGRAM
*
*****
0171 00 44000523    BSI      L LOG      PRINT PROGRAM          SRC
0173 0 09C9          OC       INM07      IS COMPLETE
*****
0174 0 3002          WT2      WAIT      2      PROGRAM COMPLETE
0175 00 4C000120    BSC      L START      PROGRAM RESTART
*
*          *** ROUTINE RETURN ***
*
0177 0 C019          RTNRT LO       SEQCK  SEQUENCE CHECK
0178 0 4818          BSC      +-
0179 0 70E1          MOX      CNTRL      CHECK OK
*
017A 00 6780018F    LOX      13 RTNNO
017C 00 C700092F    LD       L3 INLVT+1  GET HEX.VALUE OF RTN
017E 00 D4000A2C    STO      L INM12+15  SET IN MESSAGE
*
*****
0180 00 44000523    BSI      L LOG      PRINT SEQUENCE          SRC
0182 0 0A10          DC       INM12      ERROR
*****
*

```

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INTERRUPT FUNCTION TEST

```
0183 0 3003      WT3  WAIT  3      SEQUENCE ERROR
0184 00 4C000120 BSC  L  START
*
*          CONTROL ROUTINE CONSTANTS
*
0186 00 00000000      OEC      0
0188 0 0000      SNSWS OC      /0000      READ SENSE SW IOCC
0189 0 0760      OC      /0760
018A 0 0192      BSW0 OC      BSW00      READ BIT SWITCH IOCC
018B 0 0240      OC      /0240
*
018C 0 0193      BSW1 OC      BSW01      READ BIT SWITCH IOCC
018D 0 0240      OC      /0240
*
018E 0 0000      LVLIX OC      0      NO.OF LEVELS INOEX
018F 0 0000      RTNNO OC      0      ROUTINE NUMBER
0190 0 0006      SIX  OC      6      CONSTANT 6
0191 0 0000      SEQCK OC      0      SEQUENCE CHECK SAVE
*
0192 0 0000      BSW00 OC      0      BIT SW. CONTROL DATA
0193 0 0000      BSW01 OC      0      NO INTERRUPT LEVELS
*
0194 0 4C00      CNC02 OC      /4C00      RESTART INSTRUCTIONS
0195 0 0120      OC      START
0196 0 7025      CNC03 OC      /7025
0197 0 4400      OC      /4400
0198 0 0000      LVSAV OC      0      NO.INTR.LVLS SAVE
0199 0 0000      RUNSW OC      0
019A 0 0000      OPINO OC      0      OUTPUT DEVICE INOCTR
*
*          ROUTINE ADDRESSES
*
019B 0 01A6      RTN  OC      INT00      ROUTINE 1
019C 0 01FE      OC      INT01      ROUTINE 2
019D 0 0292      OC      INT02      ROUTINE 3
019E 0 0285      OC      INT03      ROUTINE 4
019F 0 0328      OC      INT04      ROUTINE 5
01A0 0 0446      OC      INT05      ROUTINE 6
01A1 0 01A2      OC      INTER      INVALID ENTRY
*
*****
01A2 00 44000523 INTER BSI L LOG      PRINT INVALID ENTRY SRC
01A4 C 0B9F      OC      INM25
*****
*
01A5 0 7096      MOX  WT1      RETURN TO WAIT 1
*
*****
*          ROUTINE NUMBER ONE
*****
01A6 00 0C000320 INT00 XIO L MASK0      MASK INTERRUPTS
01A8 00 0C000322      XIO L MASK1
*
01AA 0 631B      LOX  3 27      SET INTERRUPT
01AB 00 C40001F5 LO  L VCTOR      *TRANSFER VECTOR
01AD 00 07000007 STO L3 7
01AF 0 73FF      MOX  3 -1
01B0 0 70FC      MOX  *-4
*
01B1 0 C848      LOO  XIO      SET UP MESSAGE
01B2 00 0C000B86 STO L INM23+22
01B4 00 0C00028C LOO L XIOCC      SET UP IOCC
01B6 00 0C00028A STO L ISINT
01B8 0 630C      LOX  3 12      SET INTRP INOEX
01B9 00 650001C5 LOX L1 PL1+1      SET UP TRAP ROUTINE
01BB 00 60000608 STX L1 PLEXT+1      RETURN
01B0 00 0C000324 XIO L UMSK0      UNMASK INTERRUPTS
```

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INTERRUPT FUNCTION TEST

```
01BF 00 0C000326      XIO L UMSK1
01C1 00 0C00028A IN001 XIO L ISINT      ISSUE INTRP CHECK
01C3 0 1000      NOP      *POLL ON XIO
01C4 0 701C      PL1  MOX  FAIL      INTERRUPT FAILED
01C5 0 C030      LO  ICTR      CHECK FOR PROPER I
01C6 0 F030      EOR  XIOCK      *COUNT ON INTERRUPT
01C7 00 4C2001E9      BSC L POLER,Z      BRANCH ON WRONG I CT
*
01C9 0 C832      LOO  BSI      SET UP MESSAGE
01CA 00 0C000B86 STO L INM23+22
01CC 00 65000106 LDX L1 PL2+1      SET UP TRAP ROUTINE
01CE 00 60000608 STX L1 PLEXT+1      *RETURN
*
0100 00 0C00028A      XIO L ISINT      ISSUE INTRP CHECK
0102 0 4000      BSI  *      *FOLL ON BSI
0103 0 1000      NOP
0104 0 1000      NOP
0105 0 7004      PL2  MOX  **4      BRNCH IF INTRP FAILO
0106 0 C01F      LO  ICTR      CHECK FOR PROPER I
0107 0 F020      EOR  BSICK      *COUNT ON INTERRUPT
0108 00 4C2001E9      BSC L POLER,Z      BRANCH ON WRONG I CT
*
010A 0 C0B4      IN002 LO  RTNNO      PREPARE SEQUENCE CK
010B 0 F010      EOR  CN001
010C 0 00B4      STO  SEQCK
010D 00 4400048E      BSI L INTST      SETUP XFER VECTORS SRC
*
010F 00 4C000177      BSC L RTNRT      RETURN TO CONTROL
01E1 00 C400028A FAIL LO L ISINT      MODIFY IOCC FOR
01E3 0 1001      SLA  1      *NEXT INTERRUPT
01E4 00 0400028A STO L ISINT
01E6 0 73FF      MOX  3 -1
01E7 0 7009      MOX  IN001      CONTINUE
01E8 0 70F1      MOX  IN002      ENO ROUTINE
*
01E9 00 650001A6      POLER LOX L1 INT00      SET LOOP ERROR
01EB 00 60000521      STX L1 LPERR+1      *RETURN
*
*****
01ED 00 440004F5      BSI L ERROR      PRINT POLL ERROR SRC
01EF 0 CB70      OC  INM23      MESSAGE TAG
01F0 00 44000523      BSI L LOG      PRINT FIX COMMAND
01F2 0 0B89      OC  INM24
*****
*
01F3 00 4C00013C      BSC L WT1      GO TO WAIT 1
*
*          ROUTINE 1 CONSTANTS
*
01F5 0 0601      VCTOR OC      POLL      TRANSFER VECTOR
01F6 0 0000      ICTR OC      0      I COUNT ON INTERRUPT
01F7 0 0104      XIOCK OC      PL1      XIO CHECK CONSTANT
01F8 0 0105      BSICK OC      PL2      BSI CHECK CONSTANT
01F9 0 0001      CN001 OC      1      CONSTANT 1
01FA 0 0000      BSS  E
01FA 0 0017      XIO  OC      /0017      X
01FB 0 3926      OC      /3926      IO
01FC 0 0032      BSI  OC      /0032      B
01FD 0 1239      OC      /1239      SI
*
*****
*          ROUTINE NUMBER TWO
*****
01FE 00 C4000931 INT01 LO L INLV+3      GET HEX 2
0200 00 04000975 STO L INM03+7      SET ROUTINE NUMBER
0202 00 0400098A STO L INM04+7      IN ERROR MESSAGES
0204 00 040009A1 STO L INM05+7
```

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INTERRUPT FUNCTION TEST

```
0286 0 0000      ECKSW OC      0      LEVEL ERR CHECK SW.
*
0288 00 00000000      OEC      0
028A 0 0000      ISINT OC      0      PROGRAMED INTERRUPT
028B 0 0000      OC      0      IOCC
*
028C 0 0010      XIOCC OC      /0010      12 LEVELS OF INTRPT
028D 0 04A0      OC      /04A0
028E 0 1000      OC      /1000      18 LEVELS OF INTRP
028F 0 04A1      OC      /04A1
0290 0 0040      OC      /0040      24 LEVELS OF INTRPT
0291 0 04A1      OC      /04A1
*
*****
***** ROUTINE NUMBER THREE *****
*****
0292 00 C4000932      INT02 LO      L      INIVT+4      GET HEX 3
0294 00 04000A5F      STO      L      INM15+7      SET IN LOG MESSAGE
*
0296 00 4400048B      BSI      L      PRIST      GO SET TRAP ADDRESS SRC
*
0298 00 6580018E      LOX      11      LVLIX      SET IOCC FOR LOWEST
029A 00 C000028C      LOO      L1      XIOCC      INTERRUPT LEVEL
029C 0 0B15      STD      CN200
*
0290 00 67800198      LOX      13      LVSAY      NUMBER OF INTERRUPTS
029F 0 7302      MOX      3      2      TO BE GENERATEO
*
02A0 0 6100      LOX      1      0      PRINT TABLE INOEX
02A1 0 6200      LOX      2      0
*
02A2 0 080F      XIO      CN200      ISSUE INTERRUPT
02A3 0 1000      NOP
*
*****
***** RETURN FROM TRAP ROUTINES *****
*****
02A4 00 440004E2      BSI      L      PRIPT      GO OUTPUT PRIO. SEQ
*
02A6 00 4400048E      BSI      L      INTST      SET SPURIOUS INT AOR
*
02A8 00 C400018F      LO      L      RTI:NO      PREPARE SEQUENCE CK
02AA 0 9009      S      CN201
02AB 00 04000191      STO      L      SEQCK
*
02A0 00 4C000177      BSC      L      RTNRT      RETURN TO CONTROL
*
*****
***** ROUTINE THREE CONSTANTS *****
*****
02B0 00 00000000      OEC      0
02B2 0 0000      CN200 OC      0      INTERRUPT IOCC
02B3 0 0000      OC      0
*
02B4 0 0003      CN201 OC      3      CONSTANT 3
*
*****
***** ROUTINE NUMBER FOUR *****
*****
02B5 00 C4000933      INT03 LO      L      INLVT+5      GET HEX 4
02B7 00 04000975      STO      L      INM03+7      SET RTN NO. IN ERROR
02B9 00 0400098A      STO      L      INM04+7      *MESSAGES
*
02B8 00 440004AA      BSI      L      LVLST      GO SET UP TRAP ADOORS
02B0 0 C0C6      LO      CN105      SET PASS SWITCH
02BE 0 00C6      STO      PSSW
*
```

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INTERRUPT FUNCTION TEST

```
028F 00 C40002B0      RT300 LO      L      CN101
02C1 0 0057      STO      CN300      SET 1ST PASS SW.
*
02C2 00 65000200      LOX      L1      RT301      SET UP LOOP ON
02C4 00 60000521      STX      L1      LPERR+1      ERROR RETURN
*
02C6 0 0B59      XIO      MASKO      MASK UPPEP LEVELS
02C7 0 0B5A      XIO      MASK1      MASK LOWER LEVELS
*
02C8 00 6780018E      RT306 LOX      13      LVLIX      SET UP INITIAL IOCC
02CA 00 CF00028C      LOO      L3      XIOCC      FOR LOWEST LEVEL
02CC 0 0B51      STO      CN301
*
02C0 00 67800198      LOX      13      LVSAY      SET IX FOR NO.OF
02CF 0 7301      MOX      3      1      INTERRUPTS
*
0200 00 C700092E      RT301 LO      L3      INLVT      GET REQUEST NUMBER
0202 00 04000979      STO      L      INM03+11      SET IN ERROR
0204 00 0400098E      STO      L      INM04+11      *MESSAGES
0206 00 04000A39      STO      L      INM13+11
*
0208 0 0B45      RT302 XIO      CN301      ISSUE INTERRUPT
0209 0 1000      NOP
*
020A 0 C03E      LO      CN300      GET 1ST PASS SWITCH
020B 0 4B0B      BSC      +
020C 0 7011      MOX      RT303      NOT 1ST PASS INTR ER
*
0200 0 73FF      RT305 MOX      3      -1      CHECK IF ALL LVLS
020E 0 701A      MOX      RT304      NO
020F 0 C039      LO      CN300      YES
02E0 0 4B20      BSC      Z
02E1 0 7012      MOX      RT311      1ST PASS COMPL.CNTNU
02E2 00 74FF02B5      MDX      L      PSSW,-1      SKIP IF 500 PASSES
02E4 0 700A      MOX      RT300
*
*****
***** ROUTINE COMPLETED *****
*****
02E5 00 4400048E      BSI      L      INTST      SET SPURIOUS INT AOR
*
02E7 00 C400018F      LO      L      RTNNO      PREPARE SEQUENCE CK
02E9 0 9031      S      CN303
02EA 00 04000191      STO      L      SEQCK
*
02EC 00 4C000177      BSC      L      RTNRT      RETURN TO CONTROL
*
*****
***** REQ OIO NOT INTRP MASK OFF *****
*****
02EE 00 440004F5      RT303 BSI      L      ERROR      LOG REQUEST FAILED SRC
02F0 0 096E      OC      INM03      TO INTERRUPT
*****
02F1 00 44000609      BSI      L      SVINT
02F3 0 70E9      MOX      RT305
*
*****
***** SET UP FOR 2ND PASS *****
*****
02F4 0 1010      RT311 SLA      16
02F5 0 0023      STO      CN300      CLEAR 1ST PASS SWITC
*
02F6 0 0B20      XIO      UMSK0      UNMASK UPPER LEVELS
02F7 0 0B2E      XIO      UMSK1      UNMASK LOWER LEVELS
*
02F8 0 70CF      MOX      RT306      GO MAKE SECONO PASS
*
02F9 0 C024      RT304 LO      CN301      MODIFY IOCC FOR
```

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INTERRUPT FUNCTION TEST

```
02FA 0 480B      BSC      *      NEXT INTERRUPT      88306810
02FB 0 7003      MOX      RT307      88306820
02FC 0 1001      SLA      1      88306830
02FD 0 0020      STO      CN301      88306840
02FE 0 7001      MOX      RT301      GO ISSUE NEXT INTRPT 88306850
*      88306860
02FF 0 C01F      RT307 LD      CN301+1  CLEAR BIT 1K FROM 88306870
0300 00 94000280 S      L      CN101      CCMANO WORD      88306880
0302 0 001C      STO      CN301+1  88306890
0303 0 C016      LD      CN302      SET BIT 13 IN IOCC 88306900
0304 0 0019      STO      CN301      ADDRESS WORD      88306910
0305 0 70CA      MOX      RT301      GO ISSUE NEXT INTRPT 88306920
*      88306930
*      RETURN FROM TRAP ROUTINES      88306940
*      88306950
0306 0 C012      RT308 LD      CN300      GET PASS SWITCH 88306960
0307 0 4820      BSC      Z      SKIP IF 2ND PASS      88306970
0308 0 7006      MOX      RT309      88306980
0309 00 C40006CA LD      L      CNM00      88306990
030B 0 4820      BSC      Z      88307000
030C 0 7008      MOX      RT310      WRONG LEVEL SERVICED 88307010
0300 00 4C4002D0 BOSC L RT305 OK GO ON      88307020
*      88307030
*****      88307040
030F 00 440004F5 RT309 BSI L ERROR REQ INTRPT WHILE SRC 88307050
0311 0 0A2E      OC      INM13      MASKED      88307060
*****      88307070
*      88307080
0312 0 0800      XIO      MASK0      88307090
0313 0 080E      XIO      MASK1      88307100
0314 0 70CB      MOX      RT305      88307110
*      88307120
*****      88307130
0315 00 440004F5 RT310 BSI L ERROR WRONG LEVEL SERVICED 88307140
0317 0 0983      OC      INM04      88307150
*****      88307160
*      88307170
0318 0 70C4      MOX      RT305      88307180
*      88307190
*      ROUTINE FOUR CONSTANTS      88307200
*      88307210
0319 0 0000      CN300 OC      0      1ST PASS SWITCH 88307220
031A 0 0004      CN302 OC      /0004      88307230
031B 0 0004      CN303 DC      4      88307240
*      88307250
031C 00 00000J00 DEC      0      88307260
031E 0 0000      CN301 OC      0      INTERRUPT IOCC 88307270
031F 0 0000      DC      0      88307280
*      88307290
0320 0 FFFF      MASK0 OC      /FFFF      MASK UPPER IOCC 88307300
0321 0 0480      DC      /0480      88307310
0322 0 FFFF      MASK1 DC      /FFFF      MASK LOWER IOCC 88307320
0323 0 0481      DC      /0481      88307330
*      88307340
0324 0 0000      UNSK0 OC      /0000      UNMASK UPPER IOCC 88307350
0325 0 0480      DC      /0480      88307360
0326 0 0000      UNSK1 DC      /0000      UNMASK LOWER IOCC 88307370
0327 0 0481      DC      /0481      88307380
*      88307390
*      88307400
*****      88307410
*      ROUTINE NUMBER FIVE      88307420
*****      88307430
*      88307440
032B 00 C4000199 INTO4 LD      L      RUNSW      CK IF RUN NO STOPS 88307450
032A 00 4C20040E BSC      L      RT414,Z      BRNCH IF RUN NO STOP 88307460
*      88307470
032C 00 C4000934 LD      L      INLVT+6      GET HEX 5      88307480
```

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INTERRUPT FUNCTION TEST

```
032E 00 04000975 STO L INM03+7 SET IN ERROR MESSAGE 88307490
0330 00 0400098A STO L INM04+7 88307500
0332 00 040009A1 STO L INM05+7 88307510
0334 00 04000A5F STO L INM15+7 88307520
0336 0J 0400098B STO L INM06+7 88307530
*      88307540
0338 00 440004AA *      BSI L LVLST GO SET UP INT.AOORSS 88307550
*      88307560
033A 00 66000438 LOX L2 RT401 SET TRAP RTNS RETURN 88307570
033C 00 6700034A LOX L3 RT400 SET LOOP ON ERROR 88307580
033E 0C 6F000521 STX L3 LPERR+1 RETURN 88307590
0340 00 67000350 LOX L3 RT402 SET COMN RTN RETURN 88307600
0342 00 C400097E LO L INLVT SET REQUEST NO IN 88307610
0344 00 04000979 STO L INM03+11 *ERROR MESSAGES 88307620
0346 00 0400098E STO L INM04+11 88307630
*      88307640
0348 0 2C41      OC      /2C41 WRITE STORAGE PROTCT 88307650
0349 0 0429      OC      CN401 88307660
*      88307670
034A 00 04000429 RT400 STO L CN401 VIOLATE PROT.STORAGE 88307680
*      88307690
*****      88307700
034C 00 440004F5 BSI L ERROR REQUEST FAILED TO SRC 88307710
034E 0 096E      DC      INM03 INTERRUPT 88307720
*****      88307730
*      88307740
034F 0 700C      MOX      RT413 88307750
*      88307760
0350 00 C40006D6 RT402 LO L ILSAV CHECK IF PROPER ILSW 88307770
0352 00 F400042A EOR L CN402 *BIT FOR SPV 88307780
0354 0 4818      BSC      +-      SKIP ON WRONG BIT 88307790
0355 0 7006      MOX      RT413 BRANCH IF ILSW OK 88307800
*      88307810
*****      88307820
0356 00 44000523 BSI L LOG WRONG ILSW BIT 88307830
0358 0 0984      OC      INM06 88307840
*****      88307850
*      88307860
0359 00 C4000606 LO L ILSAV ILSW TO A 88307870
*      88307880
035B 0 3006      WT6 WAIT 6 ILSW ERROR 88307890
*      88307900
035C 00 C4000948 RT413 LO L INLVT+26 SET CE REQ IN ERROR 88307910
035E 00 0400098E STO L INM04+11 MESSAGES 88307920
0360 00 04000979 STO L INM03+11 88307930
0362 00 6700036C LOX L3 RT404 SET LOOP ERROR 88307940
0364 00 6F000521 STX L3 LPERR+1 RETURN 88307950
*      88307960
0366 00 67000374 LOX L3 RT403 SET COMN RTN RETURN 88307970
0368 00 C400042C LO L CN404 CE INTERRUPT BRANCH 88307980
036A 00 04000002 STO L /0002 *TO ADDRESS 88307990
*      88308000
*****      88308010
036C 00 44000523 RT404 BSI L LOG PUSH CE INTERRUPT SRC 88308020
036E 0 0907      OC      INM08 BUTTON 88308030
*****      88308040
*      88308050
036F 00 440005F3 BSI L DELAY GO WAIT FOR INTRPT 88308060
*      88308070
*****      88308080
0371 00 440004F5 BSI L ERROR LOG CE REQ FAILED SRC 88308090
0373 0 096E      DC      INM03 88308100
*****      88308110
*      88308120
0374 00 C4000947 RT403 LO L INLVT+25 SET TRACE REQUEST 88308130
0376 00 0400098E STO L INM04+11 IN ERROR MESSAGES 88308140
0378 00 04000979 STO L INM03+11 88308150
*      88308160
```

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INTERRUPT FUNCTION TEST

```
0465 0 1000      NOP
0466 0 7000      MDX   RT501
*
0467 0 C025      RT501 LD   TRIND   GET TRACED INDICATOR
0468 00 4C180477 BSC L  TRAER,+  BRANCH NO INSTR TRCD
046A 00 74010478 MDX L  TRAER+1,1
046C 0 6200      LDX   2 0
046D 0 1010      SLA   16
046E 0 D01E      STO   TRIND   CLEAR TRACED INOICTR
*
046F 0 73FF      MDX   3 -1      CHECK IF 10 PASSES
0470 0 700E      MOX   RT502     NO
*
*****
0471 00 44000523 BSI L  LOG      LOG SET RUN MOOE   SRC
0473 0 0A0B      DC    INM11
*****
0474 0 3010      WT10 WAIT  16      SET RUN MOOE
*
0475 00 4C400481 BOSC L  RT504     RESET BRANCH
*
0477 00 4C000000 TRAER LO  L  0      SET PASS NUMBER IN
0479 00 04000803 STO L  INM18+23    *ERROR MESSAGE
*
*****
047B 00 440004F5 BSI L  ERROR    LOG TRACE DIO NOT   SRC
047D 0 0AEC      DC    INM18     INTERRUPT
*****
047E 0 70EB      MOX   RT501+3
*
047F 00 4C40045D RT502 BOSC L  RT500     MAKE ANOTHER PASS
*
0481 00 4C00018F RT504 LD   L  RTNND   PREPARE SEQUENCE CK
0483 0 F008      EOR   CN502
0484 00 04000191 STO L  SEQCK
*
0486 00 4400048E BSI L  INTST    SET SPURIOUS INT AOR
*
0488 00 4C000177 BSC L  RTNRT
*
*****
ROUTINE SIX CONSTANTS
*****
048A 0 0001      CN500 DC    1
048B 0 0000      CN501 DC    0
048C 0 C006      CN502 DC    6
048D 0 0000      TRIND DC    0
*
*****
ROUTINE TO LOAD SPURIOUS
INTERRUPT TRAP ADDRESSES
*****
048E 0 0000      INTST OC    0
048F 00 0C000320 XIO L  MASK0     MASK INTERRUPTS   SE
0491 00 0C000322 XIO L  MASK1
0493 0 631B      LDX   3 27
0494 0 C00A      LD    INCN     ADDRESS SVINT
0495 00 07000007 STO L3 /0007    SET ADDRESS SVINT
0497 0 73FF      MDX   3 -1     *INTO ALL INTERRUPT
0498 0 70FC      MDX   *-4      *LOCATIONS
0499 00 0C000324 XIO L  UMSK0     UNMASK INTERRUPTS
049B 00 0C000326 XIO L  UMSK1
049D 00 4C80048E BSC I  INTST    RETURN TO USER   SX
*
049F 0 0609      INCN DC    SVINT   TRAP ROUTINE ADDRES
*
*****
SET TRAP ADDRESSES TO
```

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INTERRUPT FUNCTION TEST

```
*
*
* SERVICE NESTED INTERRUPTS
* WHILE DISABLED
*
04A0 0 0000      NEST1 DC    0
04A1 0 621B      LDX   2 27      SET INOEX
04A2 0 C006      LO    NSTCN     ADDRESS SERVC
04A3 00 06000007 STO L2 7      SET ADDR IN XFER LC
04A5 0 72FF      MOX   2 -1      SKIP IF DONE
04A6 0 70FC      MDX   *-4      OR TO DO NEXT VECTOR
04A7 00 4C8004A0 BSC I  NEST1    EXIT SUBROUTINE   SX
*
04A9 0 06D7      NSTCN DC    SERVC
*
*****
TRAP ADDRESS SETUP
*****
04AA 0 0000      LVLST DC    0
04AB 0 6300      LDX   3 0
04AC 0 621B      LOX   2 27
04AD 0 C008      LO    LVLST1    = LVL01
04AE 00 07000008 LVST1 STO L3 /0008
04B0 0 8006      A      LVLST2    ADD 3 FOR NEXT ADDR
04B1 0 7301      MDX   3 1
04B2 0 72FF      MDX   2 -1
04B3 0 70FA      MDX   LVST1
04B4 00 4C8004AA BSC I  LVLST    RETURN           SX
*
04B6 0 063A      LVLS1 OC    LVL01  1ST TRAP RTN ADDRESS
04B7 0 0003      LVLS2 OC    3
*
*****
PRIORITY TRAP ADDRESS AND
PRINT TABLE SETUP
*****
04B8 0 0000      PRIST DC    0
04B9 0 6300      LOX   3 0
04BA 0 621A      LOX   2 26
04BB 0 C020      LD    CNST0
04BC 00 07000009 SET01 STO L3 9
04BE 0 7301      MOX   3 1
04BF 0 8010      A      CNST1
04C0 0 72FF      MDX   2 -1
04C1 0 70FA      MOX   SET01
04C2 00 670006E0 LOX L3 PRI01  LOAD INTERNAL INTRPT
04C4 00 6F000008 STX L3 8      *XFER VECTOR
*
*****
SET UP PRIORITY SEQUENCE
PRINT TABLE
*****
04C6 0 6334      LOX   3 52
04C7 0 C818      LDD    CNST2
04C8 00 0F000A76 SET02 STO L3 IN16V-2  REQUEST SEQUENCE MSG
04CA 00 0F000A86 STO L3 IN17V-2  SERVICE SEQUENCE MSG
04CC 0 73FE      MDX   3 -2
04CD 0 70FA      MDX   SET02
04CE 00 04000AAB STO L  IN16V+51  SET TERMINATOR AT
04D0 00 04000AEB STO L  IN17V+51  END OF MESSAGE TABLE
*
*****
SET IOCC FOR LOWEST LEVEL
*****
04D2 00 6580018E LDX I1 LVLIX
04D4 00 0D00028C LDD L1 XIOCC  GET COMMAND FROM TBL
04D6 00 0C000700 STO L  PR262   SET IN TR AND CE
04D8 00 0C000714 STO L  PR272   TRAP ROUTINES
04DA 00 4C800488 BSC I  PRIST    EXIT           SX
*
*****
SETUP CONSTANTS
*****
88311570
88311580
88311590
88311600
88311610
88311620
88311630
88311640
88311650
88311660
88311670
88311680
88311690
88311700
88311710
88311720
88311730
88311740
88311750
88311760
88311770
88311780
88311790
88311800
88311810
88311820
88311830
88311840
88311850
88311860
88311870
88311880
88311890
88311900
88311910
88311920
88311930
88311940
88311950
88311960
88311970
88311980
88311990
88312000
88312010
88312020
88312030
88312040
88312050
88312060
88312070
88312080
88312090
88312100
88312110
88312120
88312130
88312140
88312150
88312160
88312170
88312180
88312190
88312200
88312210
88312220
88312230
88312240
```

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INTERRUPT FUNCTION TEST

```
040C 0 06EE      CNST0 OC      PRI26      LOWEST LEVEL ADDRESS      88312250
0400 0 0014      CNST1 OC      20                                     88312260
*                                                         88312270
040E 00 00000000      OEC      0                                     88312280
04E0 0 FFFF      CNST2 OC      /FFFF      TERMINATOR      88312290
04E1 0 0000      OC      /0000      BLANK      88312300
*                                                         88312310
*                                                         88312320
*          PRIORITY SEQUENCE LOG      88312330
*                                                         88312340
04E2 0 0000      PRIPT OC      0          SE      88312350
*                                                         88312360
04E3 00 0C00018A      XIO L BSWO      CHECK IF BYPASS      88312370
04E5 00 C4000192      LO L BSWOO      *PRIORITY PRINTOUT      88312380
04E7 0 1007      SLA      7                                     88312390
04E8 0 4828      BSC      +Z                                     88312400
04E9 0 7009      MOX      PRIXT      88312410
*                                                         88312420
*          LOG PRIORITY HEADING      88312430
*****      88312440
04EA 00 44000523      BSI L LOG      SRC      88312450
04EC 0 0A58      OC      INM15      88312460
*                                                         88312470
*          OUTPUT REQUEST SEQUENCE      88312480
*                                                         88312490
*****      88312500
04EO 00 44000523      BSI L LOG      SRC      88312510
04EF 0 0A6C      OC      INM16      88312520
*                                                         88312530
*          OUTPUT SERVICED SEQUENCE      88312540
*                                                         88312550
*****      88312560
04FO 00 44000523      BSI L LOG      SRC      88312570
04F2 0 0AAC      OC      INM17      88312580
*                                                         88312590
*                                                         88312600
04F3 00 4C8004E2      PRIXT BSC I PRIPT      EXIT      88312610
*****      88312620
*          ERROR ROUTINE      88312630
*****      88312640
04F5 0 0000      ERROR OC      0          SE      88312650
04F6 00 44000609      BSI L SVINT      RESET POSSIBLE OSW      SRC      88312660
04F8 0 7003      MOX      ERALT+3      SKIP ALTERNATE ENTRY      88312670
04F9 0 0000      ERALT DC      0          ALTERNATE TRACE ENTR      88312680
04FA 0 COFE      LD      ERALT      STORE ALTERNATE ENTR      88312690
04FB 0 00F9      STO      ERROR      *I CTR IN NORMAL ENT      88312700
04FC 00 C48004F5      LO I ERROR      SET MESSAGE ADDRESS      88312710
04FE 0 000A      STO      ERRO1+1      IN LOG CALL      88312720
*                                                         88312730
*                                                         88312740
04FF 00 74010522      MOX L ERRI0,1      SET ERR CALL INOCTOR      88312750
*                                                         88312760
0501 00 0C00018A      XIO L BSWO      CHECK IF BYPASS      88312770
0503 00 C4000192      LO L BSWOO      *ERROR PRINT REQSTD      88312780
0505 0 1802      SRA      2                                     88312790
0506 0 4804      BSC      E                                     88312800
0507 0 7002      MOX      ERRO2      88312810
*                                                         88312820
*****      88312830
0508 0 401A      ERRO1 BSI LOG      GO PRINT ERROR      SRC      88312840
0509 0 0000      OC      0          88312850
*                                                         88312860
*****      88312870
050A 0 1010      ERRO2 SLA      16      CLEAR ERROR CALL      88312880
050B 0 0016      STO      ERRI0      INOICATOR      88312890
*                                                         88312900
050C 00 0C00018A      XIO L BSWO      CHECK IF HALT ON      88312910
050E 00 C4000192      LD L BSWOO      *ERROR REQUESTED      88312920
```

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```
0510 0 1801      SRA      1      88312930
0511 0 4804      BSC      E      88312940
0512 0 7008      MOX      WT11      HALT ON ERROR BRANCH      88312950
*                                                         88312960
0513 00 0C00018A      ERR03 XIO L BSWO      CHECK IF LOOP ON      88312970
0515 00 C4000192      LO L BSWOO      *ERROR REQUESTED      88312980
0517 0 1803      SRA      3      88312990
0518 0 4804      BSC      E      88313000
0519 0 7006      MOX      LPERR      LOOP ERROR      88313010
*                                                         88313020
051A 00 740104F5      MOX L ERROR,1      A00 1 TO RETURN      88313030
051C 00 4C8004F5      BSC I ERROR      RETURN TO USER      SX      88313040
*                                                         88313050
*          ERROR HALT REQUESTED      88313060
*                                                         88313070
051E 0 3011      WT11 WAIT      17      HALT ON ERROR REQ.      88313080
051F 0 70F3      MOX      ERRO3      88313090
*                                                         88313100
*          LOOP ERROR REQUESTED      88313110
*                                                         88313120
0520 00 4C000000      LPERR BSC L 0      88313130
*                                                         88313140
0522 0 0000      ERRIO OC      0          ERROR CALL INDICATOR      88313150
*****      88313160
*          LOG ROUTINE      88313170
*****      88313180
*                                                         88313190
0523 0 0000      LOG OC      0          SE      88313200
*                                                         88313210
0524 0 6810      LOG01 STX      3 LOG06+1      SAVE IX 3      88313220
0525 00 0C000320      XIO L MASK0      MASK INTERRUPTS      88313230
0527 00 0C000322      XIO L MASK1      88313240
*                                                         88313250
0529 00 C400019A      LO L OPIND      CK OUTPUT DEVICE      88313260
0528 00 4C180548      BSC L TWRTR,+      BRANCH IF TYPEWRITER      88313270
*                                                         88313280
0520 00 C4800523      LD I LOG      GET MESSAGE ADDRESS      88313290
052F 0 005B      STO      PRWRT      SET IN IOCC      88313300
*                                                         88313310
0530 0 0853      LOG02 XIO      PRSNS      CHECK PRINTER READY      88313320
0531 00 4C040537      BSC L WT12,E      BRANCH IF NOT READY      88313330
0533 0 1801      SRA      1      88313340
0534 00 4C040539      BSC L WT13,E      BRANCH IF BUSY      88313350
0536 0 7004      MOX      LOG05      READY AND NOT BUSY      88313360
*                                                         88313370
0537 0 3012      WT12 WAIT      18      1443 NOT READY      88313380
0538 0 70F7      MOX      LOG02      CHECK AGAIN      88313390
*                                                         88313400
0539 0 3013      WT13 WAIT      19      1443 BUSY      88313410
053A 0 70F5      MOX      LOG02      CHECK AGAIN      88313420
*                                                         88313430
053B 0 044C      LOG05 XIO      PRWRT      OUTPUT MESSAGE      88313440
*                                                         88313450
053C 0 0849      XIO      PRSN      CHECK FOR OP COMPLT      88313460
0530 0 1002      SLA      2      88313470
053E 0 4810      BSC      -      88313480
053F 0 70FC      MOX      *-4      88313490
0540 0 0843      XIO      PRSNS      RESET DSW      88313500
*                                                         88313510
*          PRINTING COMPLETE      88313520
*                                                         88313530
0541 00 67000300      LOG06 LDX      L3 0      RESTORE IX 3      88313540
0543 00 0C000324      XIO L UMASK0      UNMASK INTERRUPTS      88313550
0545 00 0C000326      XIO L UMASK1      88313560
0547 00 74010523      MOX L LOG,1      BUMP RETURN      88313570
*                                                         88313580
0549 00 4C800523      BSC I LOG      RETURN TO USER      SX      88313590
*                                                         88313600
```

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INTERRUPT FUNCTION TEST

```
0548 0 1010      TWRTR SLA 16
054C 0 0032      STO  WROSW
054D 0 083C      XIO  TWSNS      CHECK IF TYPEWRITER
054E 0 1005      SLA  5          READY
054F 0 180F      SRA  15
0550 00 4C180554 BSC  L  TWR01,+-
*
0552 0 3014      WT14 WAIT 20      1816/1053 NOT READY
0553 0 70F9      MOX
*
0554 0 0028      TWR01 LO  TWRTO  CARRAIGE RETURN AND
0555 0 002A      STO  IOARA  LINE SPACE TO IO ARA
*
0556 0 0835      XIO  TWRTR  CARG RETURN/LINE SP
*
0557 0 0332      XIO  TWSNS  HANG TILL NOT BUSY
0558 0 180B      SRA  11
0559 0 4804      BSC  E
055A 0 70FC      MOX  *-4
*
0558 0 6301      LOX  3 1      8YPASS 1443 WORD COUNT
055C 00 C4800523 LO  I  LOG      GET MESSAGE ADDRESS
055E 0 0001      STO  TWR02+1
*
055F 00 C7000000 TWR02 LO  L3 0      GET WORD TO PRINT
0561 00 0400058E STO  L  CODWO  SET IN CONVERSION RT
0563 0 001A      EOR  TWRTR  CHECK IF TERMINATOR
0564 00 4C180541 BSC  L  LOG06,+-  BRANCH IF TERMINATOR
*
*****
0566 00 4400058E 8SI  L  COOCV  GO CONVERT 43 TO TW SRC
*****
*
0568 00 C400058E LO  L  CODWO
056A 0 0015      STO  IOARA
*
*
056B 0 0820      XIOWR XIO  TWRTR  WRITE CHARACTER
*
056C 0 0810      XIOSN XIO  TWSNS  HANG ON BUSY
056D 0 1808      SRA  11
056E 0 4804      BSC  E
056F 0 70FC      MOX  XIOSN  BUSY
*
*
*
*
0570 0 000E      LO  WROSW  GET 1/2 WORD SWITCH
0571 0 4804      BSC  E
0572 0 7006      MOX  TWR03  GO SET UP NEXT WORD
*
*
*
*
0573 0 000C      LO  IOARA
0574 0 1008      SLA  8          POSITION 2ND 1/2 WO
0575 0 000A      STO  IOARA
0576 00 7401057F MOX  L  WROSW,1  BUMP WORD SWITCH
0578 0 70F2      MOX  XIOWR  GO WRITC 2ND 1/2 WO
*
*
*
*
0579 0 7301      TWR03 MOX  3 1      NEXT WORD INDEX
057A 00 7401057F MOX  L  WROSW,1  BUMP WORD SWITCH
057C 0 70E2      MDX  TWR02  GO GET NEXT WORD
*
*
*
*
057D 0 8103      TWRTO DC  /8103  LINE SP/CARRAIGE RTN
```

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INTERRUPT FUNCTION TEST

```
057E 0 FFFF      TWRTR OC  /FFFF  TERMINATOR
057F 0 0000      WROSW OC  0        1/2 WORD SWITCH
0580 0 0000      IOARA DC  0        OUTPUT AREA
*
0582 00 00000000 * OEC 0
*
0584 0 0000      PRSNS OC  /0000  PRINTER SENSE IOCC
0585 0 3701      OC  /3701
0586 0 0000      PRSN OC  0        NON RESET SENSE
0587 0 3700      OC  /3700
0588 0 0000      PRWRT OC  /0000  PRINTER WRITE IOCC
0589 0 3500      OC  /3500
058A 0 0000      TWSNS OC  /0000  TYPEWTR SENSE IOCC
058B 0 0F03      OC  /0F03
058C 0 0580      TWRTR OC  IOARA  TYPEWTR WRITE IOCC
058D 0 0902      OC  /0902
*
*****
*
*
*
*
058E 0 0000      COOCV OC  0
058F 0 6927      STX  1 COOC4+1  SAVE INOEX REGS SE
0590 0 6A28      STX  2 COOC4+3
0591 0 6B29      STX  3 COOC4+5
*
0592 0 1010      SLA  16
0593 0 0028      STO  LHINO  CLEAR LEFT HALF WORD
0594 0 6300      LOX  3 0      *INDICATOR
*
0595 0 0028      CODC1 LO  CODWO  GET WORD TO CONVERT
0596 0 1890      SRT  16        SET IN Q
0597 0 0027      LO  LHINO
0598 0 4820      BSC  Z
0599 0 1088      SLT  8        SKIP IF LEFT HALF
                                POSITION RIGHT HALF
*
059A 0 1010      SLA  16
059B 0 1084      SLT  4
059C 0 0023      STO  COO00  ZONE TO ACCUM
059D 00 658005C0 LOX  11 COO00  IX 1 = ZONE
*
059F 0 1010      SLA  16
05A0 0 1084      SLT  4
05A1 0 001E      STO  COO00  DIGIT TO ACCUM
05A2 00 668005C0 LOX  12 COO00  IX 2 = DIGIT
*
05A4 00 C50005C3 LO  L1 ZONE  GET ZONE TABLE ADORS
05A6 0 0001      STO  COOC2+1  SET IN CONVERSION WO
*
05A7 00 C6000000 COOC2 LO  L2 0      GET CONVERTED CODE
05A9 00 070005C1 STO  L3 COO01
*
05A8 0 0013      LO  LHINO
05AC 00 4C2005B2 BSC  L  COOC3,Z  BRNCH IF RIGHT HALF
05AE 00 740105BF MOX  L  LHINO,1
05B0 0 7301      MOX  3 1
05B1 0 70E3      MOX  CODC1  GO CONVERT RIGHT HLF
*
05B2 0 000E      COOC3 LO  COO01  PACK CONVERTED CODES
05B3 0 1008      SLA  8
05B4 0 0800      OR  COO02
05B5 0 0008      STO  COOWD
*
05B6 00 65000000 COOC4 LOX  L1 0      RESTORE INOEX REGS
05B8 00 66000000 LOX  L2 0
05BA 00 67000000 LOX  L3 0
*
```

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INTEPRUPT FUNCTION TEST

```
*
*      INTERRUPT LEVEL 13
*      PRIORITY 15
066A 0 0000      LVL15 DC      0
066B 0 401F      BSI      CMTRP      GO TO COMN TRAP RTN. SRC
066C 0 0103      DC      /0103      13
*
*
*      INTERRUPT LEVEL 14
*      PRIORITY 16
066D 0 0000      LVL16 DC      0
066E 0 401C      BSI      CMTRP      GO TO COMN TRAP RTN. SRC
066F 0 0104      DC      /0104      14
*
*
*      INTERRUPT LEVEL 15
*      PRIORITY 17
0670 0 0000      LVL17 DC      0
0671 0 4019      BSI      CMTRP      GO TO COMN TRAP RTN. SRC
0672 0 0105      DC      /0105      15
*
*
*      INTERRUPT LEVEL 16
*      PRIORITY 18
0673 0 0000      LVL18 DC      0
0674 0 4016      BSI      CMTRP      GO TO COMN TRAP RTN. SRC
0675 0 0106      DC      /0106      16
*
*
*      INTERRUPT LEVEL 17
*      PRIORITY 19
0676 0 0000      LVL19 DC      0
0677 0 4013      BSI      CMTRP      GO TO COMN TRAP RTN. SRC
0678 0 0107      DC      /0107      17
*
*
*      INTERRUPT LEVEL 18
*      PRIORITY 20
0679 0 0000      LVL20 DC      0
067A 0 4010      BSI      CMTRP      GO TO COMN TRAP RTN. SRC
067B 0 0108      DC      /0108      18
*
*
*      INTERRUPT LEVEL 19
*      PRIORITY 21
067C 0 0000      LVL21 DC      0
067D 0 400D      BSI      CMTRP      GO TO COMN TRAP RTN. SRC
067E 0 0109      DC      /0109      19
*
*
*      INTERRUPT LEVEL 20
*      PRIORITY 22
067F 0 0000      LVL22 DC      0
0680 0 400A      BSI      CMTRP      GO TO COMN TRAP RTN. SRC
0681 0 020A      DC      /020A      20
*
*
*      INTERRUPT LEVEL 21
*      PRIORITY 23
0682 0 0000      LVL23 DC      0
```

```
88317690
88317700
88317710
88317720
88317730
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88317750
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88317800
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88317990
88318000
88318010
88318020
88318030
88318040
88318050
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88318080
88318090
88318100
88318110
88318120
88318130
88318140
88318150
88318160
88318170
88318180
88318190
88318200
88318210
88318220
88318230
88318240
88318250
88318260
88318270
88318280
88318290
88318300
88318310
88318320
88318330
88318340
88318350
88318360
```

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INTEPRUPT FUNCTION TEST

```
0683 0 4007      BSI      CMTRP      GO TO COMN TPAP RTN SPC
0684 0 0201      OC      /0201      21
*
*
*      INTERRUPT LEVEL 22
*      PRIORITY 24
0685 0 0000      LVL24 DC      0
0686 0 4004      BSI      CMTRP      GO TO COMN TRAP RTN SRC
0687 0 0202      DC      /0202      22
*
*
*      INTERRUPT LEVEL 23
*      PRIOTITY 25
0688 0 0000      LVL25 DC      0
0689 0 4001      BSI      CMTRP      GO TO COMN TRAP RTN SRC
068A 0 0203      OC      /0203      23
*
*
*      COMMON TRAP ROUTINE
CMTRP DC      0
LD      I      CMTRP      NO.OF LVL. SERVICED
STO      L      INMJ4+18    SET IN MESSAGE
S      L      INM04+11    SUB REQUEST NUMBER
STO      L      CNM00      SAVE
XIO      ILSW      SENSE AND SAVE ILSW
STO      ILSAV
*
*
*      EXIT IF ROUTINES 4 OR 5
LD      L      RTNNO
EOR      L      CN403
BSC      L2      0,+--      ROUTINE 5 EXIT
LD      L      RTNNO
EOR      L      CN303
BSC      L      RT308,+--    ROUTINE 4 EXIT
*
*
*      CHECK WHICH PASS OF RTN.1
LD      L      CN102
BSC      L      CMT02,Z
BSI      L      SERVC      SERVICE DISABLED SRC
MOX      CMT00      *INTERRUPT
*
*
*      CHECK IF PROPER LEVEL SRVC
CMT02 LD      CMT00
BSC      L      CMT03,+--    BRANCH ON PROPR LVL
*
*      MDX      CMT01      WRONG LEVEL SERVICE0
*
*****
CMT00 BSI      L      ERROR      PRINT REQ INTERRUPTD SRC
DC      INH05      WITH DISABLE SW ON
*****
BSC      L      RT104
*****
CMT01 BSI      L      ERROR      PRINT WRONG LEVEL SRC
DC      INH04      SERVICED
*****
CMT03 LD      L      INM03+11    CK IF LVL INTERNAL
S      L      INLVT
BOSC      L      CMT06,+--    BRANCH IF INTERNAL
*
0684 00 C4000979
0686 00 9400092E
0688 00 4C5806CB
```

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88318370
8831838C
88318390
88318400
88318410
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88318440
88318450
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88318470
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88318490
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88319000
88319010
88319020
88319030
88319040
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INTERRUPT FUNCTION TEST

```
06BA 0 C01B      LD      ILSAV      CHECK ILSW
06BB 00 4C6006BF  80SC L  CMT05,Z  BRANCH IF ILSW NOT D
*
06B0 00 4C400238  *      B0SC L  RT104      RETURN TO ROUTINE 1
*
*****
068F 00 440004F5  CMT05 BSI L  ERROR      PRINT ILSW NOT ZERO SRC
06C1 D 0A45      DC      INM14
*****
*
06C2 00 C4000199  CMT04 LD  L  RUNSW      BYPASS WAIT IF RUN
06C4 00 4C2006C8  8SC  L  CNM00-2,Z  MOOE WITH OUT STOPS
06C6 0 C00F      LD      ILSAV      ILSW TO A
*
06C7 D 3015      WT15 WAIT  21      ILSW NOT 0  PROG INT
*
06C8 D0 4C000238  *      8SC  L  RT104      ERROR ILSW
06CA 0 000D      CNM00 DC  0      CONTINUE
*
06CB 0 C00A      CMTD6 LO  ILSAV
06CC 00 4C2806C8  8SC  L  CNM00-2,+Z
*****
06CE 00 440004F5  *      BSI L  ERROR      WRONG ILSW ON OP  SRC
06D0 D 0984      OC      INM06      CDOE VIOLATE
*****
06D1 D 70FD      MOX      CMT04      CONTINUE
06D2 00 00000000  DEC      0
06D4 0 0000      ILSW DC  /0000      SENSE ILSW IOCC
06D5 0 0300      DC      /0300
*
06D6 0 DD00      ILSAV DC  0      SAVE FOR ILSW
*
*
06D7 D 0000      *      SERVC DC  0
06D8 0D 0C000436  XIO L  CNSNS
06DA D 08F9      XIO  ILSW      SENSE ILSW
06DB 0D 4CC006D7  B0SC I  SERVC      EXIT      ESET  SX
*
*
*      PRIORITY TRAP ROUTINES
*
*      INTERRUPT ROUTINE LEVEL ER
*
06DE 00 0D000000  DEC      0
06E0 J 0000      PRID1 DC  0
06E1 D C00B      LD      REGER      Set LEVEL ER REQUEST
06E2 00 D5000A78  STO L1 IN16V      NUMBER IN REQ SEQ MG
06E4 00 D6000A88  STO L2 IN17V      SET ER IN SER SEQ MG
06E6 0 7202      MDX  2 2
06E7 00 0C000436  XIO L  CNSNS      WRONG BTTN PROTECT
06E9 00 DC0006D4  XIO L  ILSW      SENSE RESET
06E8 00 4CC006E0  B0SC I  PRI01
06ED 0 3529      REGER DC  /3529      ER
*
*      INTERRUPT ROUTINE LEVEL TR
*
*
06EE 0 0000      PRI26 DC  0
06EF 0 C00E      LO      REQTR      LEVEL TR REQUEST TO
06F0 00 D5000A78  STO L1 IN16V      REQUEST SEQUENCE MSG
06F2 0 7102      MDX  1 2
06F3 0 73FF      MOX  3 -1
06F4 0 7006      MDX  PR260      NOT LAST INTERRUPT
*
*
*      SERVICE THIS LEVEL
*
06F5 0 C008      *      PR261 LO  REQTR      LEVEL TR TO LEVEL
06F6 00 D6000A88  STO L2 IN17V      SRVCO SEQUENCE MSG
06F8 0 7202      MDX  2 2
```

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INTERRUPT FUNCTION TEST

```
06F9 00 4C8006EE  *      BSC  I  PRI26      NON RESET BRANCH
*      PR260 XIO  PR262      ISSUE INTERRUPT
06FB 0 0804      *      NOP
06FC 0 1000      *      MOX  PR261
06FD 0 70F7
*
06FE C 1329      REQTR DC  /1329      TR
06FF 0 0000      *      OC  0
*
0700 0 0000      PR262 OC  0      LOWEST LEVEL IOCC
0701 0 0000      *      OC  0
*
*      INTERRUPT ROUTINE LEVEL CE
*
0702 0 0000      *      PRI27 DC  0
0703 0 C00E      *      LO  REQCE      LEVEL CE REQUEST TO
0704 00 05000A78  *      STO L1 IN16V      REQUEST SEQUENCE MSG
0706 0 7102      *      MOX  1 2
0707 0 73FF      *      MDX  3 -1
0708 0 7006      *      MOX  PR270
*
*      SERVICE THIS LEVEL
*
0709 0 C008      *      PR271 LO  REQCE      LEVEL CE TO LEVEL
070A 00 06000A88  *      STO L2 IN17V      SRVCD SEQUENCE MSG
070C 0 7202      *      MOX  2 2
070D 00 4CC0C00A  *      B0SC I  /000A      BRANCH RESET
*
070F 0 0804      *      PR270 XIO  PR272      ISSUE INTERRUPT
0710 0 1000      *      NOP
0711 0 70F7      *      MOX  PR271
*
0712 0 3335      *      REQCE DC  /3335      CE
0713 0 0000      *      DC  0
*
0714 0 0000      *      PR272 OC  0      LOWEST LEVEL IOCC
0715 0 0000      *      DC  0
*
*      INTERRUPT ROUTINE LEVEL 00
*
0716 0 0000      *      PRI02 OC  0
0717 0 C00E      *      LO  REQ00      LEVEL 00 REQUEST TO
0718 00 D5000A78  *      STO L1 IN16V      REQUEST SEQUENCE MSG
071A 0 7102      *      MOX  1 2
071B 0 73FF      *      MOX  3 -1
071C 0 7006      *      MDX  PR020      NOT LAST INTERRUPT
*
*      SERVICE THIS INTERRUPT
*
071D 0 C008      *      PRO21 LO  REQ00      LEVEL 00 TO LEVEL
071E 00 D6000A88  *      STO L2 IN17V      SRVCO SEQUENCE MSG
0720 0 7202      *      MOX  2 2
0721 00 4CC00716  *      B0SC I  PRI02      BRANCH RESET
*
0723 0 0100      *      PRO20 DC  /D100      ILLEGAL OP INTRP ER
0724 0 1000      *      NOP
0725 0 70F7      *      MOX  PRO21
*
0726 0 0A0A      *      REQ00 OC  /0A0A      00
0727 0 0000      *      DC  0
*
0728 0 0000      *      OC  0
0729 0 0000      *      OC  0
*
*      INTERRUPT ROUTINE LEVEL 01
*
072A 0 0000      *      PRI03 OC  0
072B 0 C00E      *      LO  REQ01      LEVEL 01 REQUEST TO
```

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```
072C 00 05000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
072E 0 7102            MOX 1 2
072F 0 73FF            MOX 3 -1
0730 0 7006            MOX      PRO30      NOT LAST INTERRUPT
*
*          SERVICE THIS INTERRUPT
*
0731 0 C008            PR031 LO      REQ01      LEVEL 01 TO LEVEL
0732 00 06000A88      STO L2 IN17V      SRVCO SEQUENCE MSG
0734 0 7202            MOX 2 2
0735 00 4CC0072A      BOSC 1 PRI03      BRANCH RESET
*
0737 0 0804            PR030 XIO      PR032      INTERRUPT FOR LVL
0738 0 1000            NOP
0739 0 70F7            MOX      PRO31
*
073A 0 0A01            REQ01 OC      /0A01      01
0738 0 0000            OC
*
073C 0 8000            PR032 OC      /8000      INTRP 00 IOCC
0730 0 04A0            OC      /04A0
*
*          INTERRUPT ROUTINE LEVEL 02
*
073E 0 0000            PRI04 OC      0
073F 0 C00E            LO      REQ02      LEVEL 01 REQUEST TO
0740 00 05000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
0742 0 7102            MOX 1 2
0743 0 73FF            MOX 3 -1
0744 0 7006            MOX      PRO40      NOT LAST INTERRUPT
*
*          SERVICE THIS INTERRUPT
*
0745 0 C008            PR041 LO      REQ02      LEVEL 02 TO LEVEL
0746 00 06000A88      STO L2 IN17V      SRVCO SEQUENCE MSG
0748 0 7202            MOX 2 2
0749 00 4CC0073E      BOSC 1 PRI04      BRANCH RESET
*
0748 0 0804            PR040 XIO      PR042      INTERRUPT FOR LVL 02
074C 0 1000            NOP
0740 0 70F7            MOX      PRO41
*
074E 0 0A02            REQ02 OC      /0A02      02
074F 0 0000            OC
*
0750 0 4000            PR042 OC      /4000      INTRP 01 IOCC
0751 0 04A0            OC      /04A0
*
*          INTERRUPT ROUTINE LEVEL 03
*
0752 0 0000            PRI05 OC      0
0753 0 C00E            LO      REQ03      LEVEL 03 REQUEST TO
0754 00 05000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
0756 0 7102            MOX 1 2
0757 0 73FF            MOX 3 -1
0758 0 7006            MOX      PRO50      NOT LAST INTERRUPT
*
*          SERVICE THIS INTERRUPT
*
0759 0 C008            PR051 LO      REQ03      LEVEL 03 TO LEVEL
075A 00 06000A88      STO L2 IN17V      SRVCO SEQUENCE MSG
075C 0 7202            MOX 2 2
0750 00 4CC00752      BOSC 1 PRI05      BRANCH RESET
*
075F 0 0804            PR050 XIO      PR052      INTERRUPT FOR LVL 01
0760 0 1000            NOP
0761 0 70F7            MOX      PRO51
*
```

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```
0762 0 0A03            REQ03 OC      /0A03      03
0763 0 0000            OC
*
0764 0 2000            PR052 OC      /2000      INTRP 01 IOCC
0765 0 04A0            OC      /04A0
*
*          INTERRUPT ROUTINE LEVEL 04
*
0766 0 0000            PRI06 OC      0
0767 0 C00E            LO      REQ04      LEVEL 04 REQUEST TO
0768 00 05000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
076A 0 7102            MOX 1 2
0768 0 73FF            MOX 3 -1
076C 0 7006            MOX      PRO60      NOT LAST INTERRUPT
*
*          SERVICE THIS INTERRUPT
*
0760 0 C008            PR061 LO      REQ04      LEVEL 04 TO LEVEL
076E 00 06000A88      STO L2 IN17V      SRVCO SEQUENCE MSG
0770 0 7202            MOX 2 2
0771 00 4CC00766      BOSC 1 PRI06      BRANCH RESET
*
0773 0 0804            PR060 XIO      PR062      INTERRUPT FOR LVL 03
0774 0 1000            NOP
0775 0 70F7            MOX      PRO61
*
0776 0 0A04            REQ04 OC      /0A04      04
0777 0 000C            OC
0778 0 1000            PR062 OC      /1000      INTRP 03 IOCC
0779 0 04A0            OC      /04A0
*
*          INTERRUPT ROUTINE LEVEL 05
*
077A 0 0000            PRI07 OC      0
0778 0 C00E            LO      REQ05      LEVEL 05 REQUEST TO
077C 00 05000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
077E 0 7102            MOX 1 2
077F 0 73FF            MOX 3 -1
0780 0 7006            MOX      PRO70      NOT LAST INTERRUPT
*
*          SERVICE THIS INTERRUPT
*
0781 0 C008            PR071 LO      REQ05      LEVEL 05 TO LEVEL
0782 00 06000A88      STO L2 IN17V      SRVCO SEQUENCE MSG
0784 0 7202            MOX 2 2
0785 00 4CC0077A      BOSC 1 PRI07      BRANCH RESET
*
0787 0 0804            PR070 XIO      PR072      INTERRUPT FOR LVL 04
0788 0 1000            NOP
0789 0 70F7            MOX      PRO71
*
078A 0 0A05            REQ05 OC      /0A05      05
0788 0 0000            OC
*
078C 0 0800            PR072 OC      /0800      INTRP 04 IOCC
0780 0 04A0            OC      /04A0
*
*          INTERRUPT ROUTINE LEVEL 06
*
078E 0 0000            PRI08 OC      0
078F 0 C00E            LO      REQ06      LEVEL 06 REQUEST TO
0790 00 05000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
0792 0 7102            MOX 1 2
0793 0 73FF            MOX 3 -1
0794 0 7006            MOX      PRO80
*
*          SERVICE THIS INTERRUPT
*
```

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INTERRUPT FUNCTION TEST

Address	Operation	Source	Destination	Condition	Priority	Interrupt
0795 0	C008	PR081	LO	REQ06		LEVEL 06 TO LEVEL
0796 00	D6000A88		STO	L2 IN17V		SRVCO SEQUENCE MSG
0798 0	7202		MOX	2 2		
0799 00	4CC0078E		8OSC	1 PRI08		BRANCH RESET
079B 0	0804					
079C 0	1000	PR080	XIO	PR082		INTERRUPT FOR LVL 05
079D 0	70F7		NOP			
			MOX	PR061		
079E 0	0A06					
079F 0	0000		REQ06	0C	/0A06	06
				0C	0	
07A0 0	0400					
07A1 0	04A0		PR082	0C	/0400	INTRP 05 IOCC
				0C	/G4A0	
						INTERRUPT ROUTINE LEVEL 07
07A2 0	0000					
07A3 0	C00E		PRI09	0C	0	
07A4 00	05000A78		LO	REQ07		LEVEL 07 REQUEST TO
07A6 0	7102		STO	L1 IN16V		REQUEST SEQUENCE MSG
07A7 0	73FF		MOX	1 2		
07A8 0	7006		MOX	3 -1		
			MOX	PR090		NOT LAST INTERRUPT
						SERVICE THIS INTERRUPT
07A9 0	C008					
07AA 00	D6000A88		PR091	LO	REQ07	LEVEL 07 TO LEVEL
07AC 0	7202		STO	L2 IN17V		SRVCO SEQUENCE MSG
07AD 00	4CC007A2		MOX	2 2		
			8OSC	1 PRI09		BRANCH RESET
07AF 0	0804					
07B0 0	1000		PR090	XIO	PR092	INTERRUPT FOR LVL 06
07B1 0	70F7			NOP		
				MOX	PR091	
07B2 0	0A07					
07B3 0	0000		REQ07	0C	/0A07	07
				0C	0	
07B4 0	0200					
07B5 0	04A0		PR092	0C	/0200	INTRP 06 IOCC
				0C	/04A0	
						INTERRUPT ROUTINE LEVEL 08
07B6 0	0000					
07B7 0	C00E		PRI10	0C	0	
07B8 00	D5000A78		LO	REQ08		LEVEL 08 REQUEST TO
07BA 0	7102		STO	L1 IN16V		REQUEST SEQUENCE MSG
07BB 0	73FF		MOX	1 2		
07BC 0	7006		MOX	3 -1		
			MOX	PR100		NOT LAST INTERRUPT
						SERVICE THIS INTERRUPT
07B0 0	C008					
07BE 00	06000A88		PR101	LO	REQ08	LEVEL 08 TO LEVEL
07C0 0	7202		STO	L2 IN17V		SRVCO SEQUENCE MSG
07C1 00	4CC007B6		MOX	2 2		
			8OSC	1 PRI10		BRANCH RESET
07C3 0	0804					
07C4 0	1000		PRI00	XIO	PRI02	INTERRUPT FOR LVL 07
07C5 0	70F7			NOP		
				MOX	PRI01	
07C6 0	0A08					
07C7 0	0000		REQ08	0C	/0A08	08
				0C	0	
07C8 0	0100					
07C9 0	04A0		PR102	0C	/C100	INTRP 07 IOCC
				0C	/04A0	
						INTERRUPT ROUTINE LEVEL 09

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07CA 0	0000	PP111	OC	0		88322450
07C8 0	C09E		LO	REQ09	LEVEL 09 REQUEST TO	88322460
07CC 00	05000A78		STO	L1 IN16V	REQUEST SEQUENCE MSG	88322470
07CE 0	7102		MOX	1 2		88322480
07CF 0	73FF		MDX	3 -1		88322490
0700 0	7006		MOX	PR110	NOT LAST INTERRUPT	88322500
		*				88322510
		*				88322520
		*			SERVICE THIS INTERRUPT	88322530
		*				88322540
0701 0	C008	PR111	LO	REQ09	LEVEL 09 TO LEVEL	88322550
0702 00	06000A89		STO	L2 IN17V	SRVCD SEQUENCE MSG	88322560
0704 0	7202		MOX	2 2		88322570
0705 00	4CC007CA		80SC	1 PR111	BRANCH RESET	88322580
		*				88322590
0707 0	0804	PR110	X10	PR112	INTERRUPT FOR LVL 08	88322600
0708 0	1000		NOP			88322610
0709 0	70F7		MOX	PR111		88322620
		*				88322630
070A 0	0A09	REQ09	DC	/0A09	09	88322640
070B 0	0000		OC	0		88322650
		*				88322660
070C 0	0080	PR112	DC	/0080	INTRP 08 IOCC	88322670
0700 0	04A0		OC	/04A0		88322680
		*				88322690
		*			INTERRUPT ROUTINE LEVEL 10	88322700
		*				88322710
		*				88322720
070E 0	0000	PR112	OC	0		88322730
070F 0	C00E		LO	REQ10	LEVEL 10 REQUEST TO	88322740
07E0 00	05000A78		STO	L1 IN16V	REQUEST SEQUENCE MSG	88322750
07E2 0	7102		MOX	1 2		88322760
07E3 0	73FF		MOX	3 -1		88322770
07E4 0	7006		MOX	PR120	NOT LAST INTERRUPT	88322780
		*				88322790
		*				88322800
		*			SERVICE THIS INTERRUPT	88322810
		*				88322820
07E5 0	C008	PR121	LO	REQ10	LEVEL TO LEVEL	88322830
07E6 00	06000A88		STO	L2 IN17V	SRVCO SEQUENCE MSG	88322840
07E8 0	7202		MOX	2 2		88322850
07E9 00	4CC007DE		80SC	1 PR112	BRANCH RESET	88322860
		*				88322870
07EB 0	0804	PR120	X10	PR122	INTERRUPT FOR LVL 09	88322880
07EC 0	1000		NOP			88322890
07ED 0	70F7		MOX	PR121		88322900
		*				88322910
07EE 0	010A	REQ10	OC	/010A	10	88322920
07EF 0	0000		OC	0		88322930
		*				88322940
07F0 0	0040	PR122	OC	/0040	INTRP 09 IOCC	88322950
07F1 0	04A0		OC	/04A0		88322960
		*				88322970
		*			INTERRUPT ROUTINE LEVEL 11	88322980
		*				88322990
		*				88323000
07F2 0	0000	PR113	OC	0		88323010
07F3 0	C00E		LO	REQ11	LEVEL 11 REQUEST TO	88323020
07F4 00	05000A78		STO	L1 IN16V	REQUEST SEQUENCE MSG	88323030
07F6 0	7102		MOX	1 2		88323040
07F7 0	73FF		MOX	3 -1		88323050
07F8 0	7006		MOX	PR130	NOT LAST INTERRUPT	88323060
		*				88323070
		*				88323080
		*			SERVICE THIS INTERRUPT	88323090
		*				88323100
07F9 0	C008	PR131	LO	REQ11	LEVEL 11 TO LEVEL	88323110
07FA 00	06000A88		STO	L2 IN17V	SRVCO SEQUENCE MSG	88323120
07FC 0	7202		MOX	2 2		88323130
07F0 00	4CC007F2		80SC	1 PR113	BRANCH RESET	88323

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INTERRUPT FUNCTION TEST

```
0800 0 1000      NOP
0801 0 70F7      MOX      PR131

*
0802 0 0101      REQ11 DC   /0101    11
0803 0 0000      OC       0

*
0804 0 0020      PR132 DC   /0020    INTRP 10 IOCC
0805 0 04A0      DC       /04A0

*
*
*
*
*
0806 0 0000      PR114 DC   0
0807 0 C00E      LD       REQ12
0808 00 05000A78 ST0 L1 IN16V    LEVEL 12 REQUEST TO
080A 0 7102      MOX      1 2      REQUEST SEQUENCE MSG
080B 0 73FF      MOX      3 -1
080C 0 7006      MDX      PR140    NOT LAST INTERRUPT

*
*
*
*
*
0800 0 C008      PR141 LO     REQ12    LEVEL 12 TO LEVEL
080E 00 06000A88 ST0 L2 IN17V    SRVCO SEQUENCE MSG
0810 0 7202      MDX      2 2
0811 00 4CC00806 BOSC I PR114    BRANCH RESET

*
0813 0 0804      PR140 XIO    PR142    INTERRUPT FOR LVL 11
0814 0 1000      NOP
0815 0 70F7      MDX      PR141

*
0816 0 0102      REQ12 OC   /0102    12
0817 0 0000      OC       0

*
0818 0 0010      PR142 DC   /0010    INTRP 11 IOCC
0819 0 04A0      DC       /04A0

*
*
*
*
*
081A 0 0000      PR115 DC   0
081B 0 C00E      LD       REQ13    LEVEL 13 REQUEST TO
081C 00 05000A78 ST0 L1 IN16V    REQUEST SEQUENCE MSG
081E 0 7102      MDX      1 2
081F 0 73FF      MDX      3 -1
0820 0 7006      MDX      PR150    NOT LAST INTERRUPT

*
*
*
*
*
0821 0 C008      PR151 LO     REQ13    LEVEL 13 TO LEVEL
0822 00 06000A88 ST0 L2 IN17V    SRVCO SEQUENCE MSG
0824 0 7202      MDX      2 2
0825 00 4CC0081A BOSC I PR115    BRANCH RESET

*
0827 0 0804      PR150 XIO    PR152    INTERRUPT FOR LVL 12
0828 0 1000      NOP
0829 0 70F7      MDX      PR151

*
082A 0 0103      REQ13 OC   /0103    13
082B 0 0000      OC       0

*
082C 0 C008      PR152 DC   /0008    INTRP 12 IOCC
082D 0 04A0      DC       /04A0

*
*
*
*
*
082E 0 0000      PR116 DC   0
082F 0 C00E      LD       REQ14    LEVEL 14 REQUEST TO
0830 00 05000A78 ST0 L1 IN16V    REQUEST SEQUENCE MSG
0832 0 7102      MDX      1 2
0833 0 73FF      MDX      3 -1
```

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INTERRUPT FUNCTION TEST

```
0834 0 7006      MOX      PR160    NOT LAST INTERRUPT

*
*
*
*
*
0835 0 C008      PR161 LD     REQ14    LEVEL 14 TO LEVEL
0836 00 06000A88 ST0 L2 IN17V    SRVCO SEQUENCE MSG
0838 0 7202      MOX      2 2
0839 00 4CC0082E BOSC I PR116    BRANCH RESET

*
083B 0 0804      PR160 XIO    PR162    INTERRUPT FOR LVL 13
083C 0 1000      NOP
083D 0 70F7      MDX      PR161

*
083E 0 0104      REQ14 DC   /0104    14
083F 0 0000      DC       0

*
0840 0 0004      PR162 DC   /0004    INTRP 13 IOCC
0841 0 04A0      DC       /04A0

*
*
*
*
*
0842 0 0000      PR117 DC   0
0843 0 C00E      LD       REQ15    LEVEL 15 REQUEST TO
0844 00 05000A78 ST0 L1 IN16V    REQUEST SEQUENCE MSG
0846 0 7102      MOX      1 2
0847 0 73FF      MOX      3 -1
0848 0 7006      MDX      PR170    NOT LAST INTERRUPT

*
*
*
*
*
0849 0 C008      PR171 LD     REQ15    LEVEL 15 TO LEVEL
084A 00 06000A88 ST0 L2 IN17V    SRVCO SEQUENCE MSG
084C 0 7202      MOX      2 2
084D 00 4CC00842 BOSC I PR117    BRANCH RESET

*
084F 0 0804      PR170 XIO    PR172    INTERRUPT FOR LVL 14
0850 0 1000      NOP
0851 0 70F7      MDX      PR171

*
0852 0 0105      REQ15 OC   /0105    15
0853 0 0000      DC       0

*
0854 0 8000      PR172 DC   /8000    INTERP 14 IOCC
0855 0 04A1      DC       /04A1

*
*
*
*
*
0856 0 0000      PR118 DC   0
0857 0 C00E      LD       REQ16    LEVEL 16 REQUEST TO
0858 00 05000A78 ST0 L1 IN16V    REQUEST SEQUENCE MSG
085A 0 7102      MDX      1 2
085B 0 73FF      MDX      3 -1
085C 0 7006      MDX      PR180    NOT LAST INTERRUPT

*
*
*
*
*
085D 0 C008      PR181 LO     REQ16    LEVEL 16 TO LEVEL
085E 00 06000A88 ST0 L2 IN17V    SRVCO SEQUENCE MSG
0860 0 7202      MDX      2 2
0861 00 4CC00856 BOSC I PR118    BRANCH RESET

*
0863 0 0804      PR180 XIO    PR182    INTERRUPT FOR LVL 15
0864 0 1000      NOP
0865 0 70F7      MDX      PR181

*
0866 0 0106      REQ16 OC   /0106    16
0867 0 0000      DC       0

*
*
*
*
*
0868 0 0000      PR119 DC   0
0869 0 C00E      LD       REQ17    LEVEL 17 REQUEST TO
0870 00 05000A78 ST0 L1 IN16V    REQUEST SEQUENCE MSG
0872 0 7102      MDX      1 2
0873 0 73FF      MDX      3 -1
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INTERRUPT FUNCTION TEST

```
0868 0 4003    PR182 OC    /4000    INTRP 15 IOCC
0869 0 04A1    OC        /04A1

*
*           INTERRUPT ROUTINE LEVEL 17
*
086A 0 0000    PR119 OC    0
086B 0 C00E    LO        REQ17    LEVEL 17 REQUEST TO
086C 00 05000A7B ST0 L1 IN16V    REQUEST SEQUENCE MSG
086E 0 7102    MOX      1 2
086F 0 73FF    MOX      3 -1
0870 0 7006    MOX      PR190    NOT LAST INTERRUPT

*
*           SERVICE THIS INTERRUPT
*
0871 0 C008    PR191 LO    REQ17    LEVEL 17 TO LEVEL
0872 00 06000A88 ST0 L2 IN17V    SRVCD SEQUENCE MSG
0874 0 7202    MOX      2 2
0875 00 4CC0086A BOSC I PR119    BRANCH RESET

*
0877 0 0804    PR190 XIO    PR192    INTERRUPT FOR LVL 16
0878 0 1000    NOP
0879 0 70F7    MOX      PR191

*
087A 0 0107    REQ17 OC    /0107    17
087B 0 0000    OC        0

*
087C 0 2000    PR192 OC    /2000    INTRP 16 IOCC
087D 0 04A1    OC        /04A1

*
*           INTERRUPT ROUTINE LEVEL 18
*
087E 0 0000    PR120 OC    0
087F 0 C00E    LO        REQ18    LEVEL 18 REQUEST TO
0880 00 05000A78 ST0 L1 IN16V    REQUEST SEQUENCE MSG
0882 0 7102    MOX      1 2
0883 0 73FF    MOX      3 -1
0884 0 7006    MOX      PR200    NOT LAST INTERRUPT

*
*           SERVICE THIS INTERRUPT
*
0885 0 C008    PR201 LO    REQ18    LEVEL 18 TO LEVEL
0886 00 06000A88 ST0 L2 IN17V    SRVCD SEQUENCE MSG
0888 0 7202    MOX      2 2
0889 00 4CC0087E BOSC I PR120    BRANCH RESET

*
088B 0 0804    PR200 XIO    PR202    INTERRUPT FOR LVL 17
088C 0 1000    NOP
088D 0 70F7    MOX      PR201

*
088E 0 0108    REQ18 OC    /0108    18
088F 0 0000    OC        0

*
0890 0 1000    PR202 OC    /1000    INTRP 17 IOCC
0891 0 04A1    OC        /04A1

*
*           INTERRUPT ROUTINE LEVEL 19
*
0892 0 0000    PR121 OC    0
0893 0 C00E    LO        REQ19    LEVEL 19 REQUEST TO
0894 00 05000A78 ST0 L1 IN16V    REQUEST SEQUENCE MSG
0896 0 7102    MOX      1 2
0897 0 73FF    MOX      3 -1
0898 0 7006    MOX      PR210    NOT LAST INTERRUPT

*
*           SERVICE THIS INTERRUPT
*
0899 0 C008    PR211 LO    REQ19    LEVEL 19 TO LEVEL
089A 00 06000A88 ST0 L2 IN17V    SRVCD SEQUENCE MSG
```

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```
089C 0 7202    MOX      2 2
089D 00 4CC00892 BOSC I PR121    BRANCH RESET

*
*           INTERRUPT ROUTINE LEVEL 18
*
089F 0 0804    PR210 XIO    PR212    INTERRUPT FOR LVL 18
08A0 0 1000    NOP
08A1 0 70F7    MOX      PR211

*
08A2 0 0109    REQ19 OC    /0109    19
08A3 0 0000    OC        0

*
08A4 0 0800    PR212 OC    /0800    INTRP 18 IOCC
08A5 0 04A1    OC        /04A1

*
*           INTERRUPT ROUTINE LEVEL 20
*
08A6 0 0000    PR122 OC    0
08A7 0 C00E    LO        REQ20    LEVEL 20 REQUEST TO
08A8 00 05000A78 ST0 L1 IN16V    REQUEST SEQUENCE MSG
08AA 0 7102    MOX      1 2
08AB 0 73FF    MOX      3 -1
08AC 0 7006    MOX      PR220    NOT LAST INTERRUPT

*
*           SERVICE THIS INTERRUPT
*
08AD 0 C008    PR221 LO    REQ20    LEVEL 20 TO LEVEL
08AE 00 06000A88 ST0 L2 IN17V    SRVCD SEQUENCE MSG
08B0 0 7202    MOX      2 2
08B1 00 4CC008A6 BOSC I PR122    BRANCH RESET

*
08B3 0 0804    PR220 XIO    PR222    INTERRUPT FOR LVL 19
08B4 0 1000    NOP
08B5 0 70F7    MOX      PR221

*
08B6 0 020A    REQ20 OC    /020A    20
08B7 0 0000    OC        0

*
08B8 0 0400    PR222 OC    /0400    INTRP 19 IOCC
08B9 0 04A1    OC        /04A1

*
*           INTERRUPT ROUTINE LEVEL 21
*
08BA 0 0000    PR123 OC    0
08BB 0 C00E    LO        REQ21    LEVEL 21 REQUEST TO
08BC 00 05000A78 ST0 L1 IN16V    REQUEST SEQUENCE MSG
08BE 0 7102    MOX      1 2
08BF 0 73FF    MOX      3 -1
08C0 0 7006    MOX      PR230    NOT LAST INTERRUPT

*
*           SERVICE THIS INTERRUPT
*
08C1 0 C008    PR231 LO    REQ21    LEVEL 21 TO LEVEL
08C2 00 06000A88 ST0 L2 IN17V    SRVCD SEQUENCE MSG
08C4 0 7202    MOX      2 2
08C5 00 4CC0088A BOSC I PR123    BRANCH RESET

*
08C7 0 0804    PR230 XIO    PR232    INTERRUPT FOR LVL 20
08C8 0 1000    NOP
08C9 0 70F7    MOX      PR231

*
08CA 0 0201    REQ21 OC    /0201    21
08CB 0 0000    OC        0

*
08CC 0 0200    PR232 OC    /0200    INTRP 20 IOCC
08CD 0 04A1    OC        /04A1

*
*           INTERRUPT ROUTINE LEVEL 22
*
08CE 0 0000    PR124 OC    0
```

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INTERRUPT FUNCTION TEST

```
08CF 0 C00E      LD      REQ22      LEVEL 22 REQUEST TO
0800 00 05000A78  STO L1 IN16V  REQUEST SEQUENCE MSG
0802 0 7102      MOX      1 2
0803 0 73FF      MOX      3 -1
0804 0 7006      MOX      PR240      NOT LAST INTERRUPT
*
*          SERVICE THIS INTERRUPT
*
0805 0 C008      PR241 LO      REQ22      LEVEL 22 TO LEVEL
0806 00 06000A8B  STO L2 IN17V  SRVCO SEQUENCE MSG
0808 0 7202      MOX      2 2
0809 00 4CC008CE  BOSC I PR124  BRANCH RESET
*
080B 0 0804      PR240 XIO PR242      INTERRUPT FOR LVL 21
080C 0 1000      NOP
080D 0 70F7      MOX      PR241
*
080E 0 0202      REQ22 OC      /0202      22
080F 0 0000      DC      0
*
08E0 0 D100      PR242 OC      /01C0      INTRP 21 IOCC
08E1 0 04A1      OC      /04A1
*
*          INTERRUPT ROUTINE LEVEL 23
*
08E2 0 0000      PR125 DC      0
08E3 0 C00E      LD      REQ23      LEVEL 23 REQUEST TO
08E4 00 05000A78  STO L1 IN16V  REQUEST SEQUENCE MSG
08E6 0 7102      MOX      1 2
08E7 0 73FF      MOX      3 -1
08E8 0 7006      MOX      PR250      NOT LAST INTERRUPT
*
*          SERVICE THIS INTERRUPT
*
08E9 0 C008      PR251 LD      REQ23      LEVEL 23 TO LEVEL
08EA 00 06000A8B  STO L2 IN17V  SRVCO SEQUENCE MSG
08EC 0 7202      MOX      2 2
08ED 00 4CC008E2  BOSC I PR125  BRANCH RESET
*
08EF 0 0804      PR250 XIO PR252      INTERRUPT FOR LVL 22
08F0 0 1000      NOP
08F1 0 70F7      MDX      PR251
*
08F2 0 0203      REQ23 OC      /D2D3      23
08F3 0 0000      OC      0
*
08F4 0 0080      PR252 DC      /D08D      INTRP 22 IOCC
08F5 0 04A1      OC      /04A1
*
*          ROUTINE SIX TRACE INTRPT
*
08F6 0 0000      TRACE DC      0
08F7 0 0028      STO      TRCN2      SAVE ACCUMULATOR
08F8 00 74FF08F6  MDX L TRACE,-1
08FA 0 C0FB      LD      TRACE      GET INTERP I COUNT
08FB 0 9025      S      TRCNO      CK IF INSTR TO TRACE
08FC 00 4C280903  BSC L TRACO,+2  BRANCH IF TRACING
08FE 00 740108F6  MOX L TRACE,1  RESTORE RETURN
0900 0 C022      LO      TRCN2      RESTORE ACCUMULATOR
0901 00 4CB00BF6  BSC I TRACE      GO COMPLETE CNTRL OP
*
0903 00 C6000930  TRACO LD L2 INLVT+2  SET EXPECTED INSTRN
0905 00 D4000B1C  STO L INM19+23  NO. IN ERROR MESSAGE
*
0907 0 C0EE      LD      TRACE      GET INTERP I COUNT
0908 00 96000924  S L2 NSAD      COMPR WITH EXPECTED
090A 00 4C200914  BSC L TRAC1,Z  BRANCH IF WRONG ISTR
```

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```
090C 0 7201      MOX      2 1
090D 00 7401048D  MOX L TRINO,1  STEP TRACF INOICATOR
090F 00 740108F6  MOX L TRACE,1  RESTORE RETURN
0911 0 C011      LO      TRCN2      RESTORE ACCUMULATOR
0912 00 4CC008F6  TRAC2 BOSC I TRACE      EXIT
*
0914 0 6A0D      TRAC1 STX 2 TRCN1
0915 0 800C      A      TRCN1
0916 0 D008      STO      TRCN1
0917 00 65800922  LOX I1 TRCN1  SET INOEX TO DIFRNC
0919 00 C5000930  LO L1 INLVT+2  SET INTERRUPTING
091B 00 D4000B20  STO L INM19+27 INSTR TO ERROR MSG
*
*****
091D 00 440004F9  BSI L ERALT  LOG FAILING TRACE
091F 0 D805      OC      INM19  INSTRUCTION
*****
*
0920 0 70E8      MOX      TPAC1-8
*
0921 0 0467      TRCNO OC      RT501
0922 0 0000      TRCN1 OC      0
0923 0 0000      TRCN2 OC      0
*
*          TRACE INSTRUCTION ADDRESS
*
0924 0 045D      INSAO OC      RT500
0925 0 045E      OC      RT500+1
0926 0 045F      DC      RT500+2
0927 0 0460      DC      RT500+3
0928 0 0461      OC      RT500+4
0929 0 0462      OC      RT500+5
092A 0 0463      DC      RT500+6
092B 0 0464      OC      RT500+7
092C 0 0465      DC      RT500+8
092D 0 0466      DC      RT500+9
*
*          HEX INTERRUPT LEVEL TABLE
*
092E 0 3529      INLVT OC      /3529      ER
092F 0 0A0A      OC      /0A0A      00
0930 0 0A01      DC      /0A01      01
0931 0 0A02      OC      /0A02      02
0932 0 0A03      OC      /0A03      03
0933 0 0A04      OC      /0A04      04
0934 0 0A05      OC      /0A05      05
0935 0 0A06      OC      /0A06      06
0936 0 0A07      OC      /0A07      07
0937 0 0A08      DC      /0A08      08
0938 0 0A09      OC      /0A09      09
0939 0 010A      DC      /D10A      10
093A 0 0101      OC      /0101      11
093B 0 0102      OC      /0102      12
093C 0 0103      OC      /0103      13
093D 0 0104      DC      /0104      14
093E 0 0105      OC      /D105      15
093F 0 0106      DC      /0106      16
0940 0 0107      OC      /0107      17
0941 0 0108      DC      /0108      18
0942 0 0109      OC      /0109      19
0943 0 020A      OC      /D20A      20
0944 0 0201      OC      /0201      21
0945 0 0202      DC      /0202      22
0946 0 0203      DC      /0203      23
0947 0 1329      DC      /1329      TR
0948 0 3335      DC      /3335      CE
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INTERRUPT FUNCTION TEST

```
*
0949 0 0013      INM01 DC    /0013  WORD COUNT
094A 0 330A      DC    /330A  CO
094B 0 0A01      UC    /0A01  O1
094C 0 0000      DC    /0000  SPACE
094D 0 0000      DC    /0000  SPACE
094E 0 1314      OC    /1314  TU
094F 0 2925      DC    /2925  RN
0950 0 0034      DC    /0034  O
0951 0 3912      OC    /3912  IS
0952 0 3132      DC    /3132  AB
0953 0 2335      DC    /2335  LE
0954 0 0012      OC    /0012  S
0955 0 1600      OC    /1600  W
0956 0 2625      OC    /2625  ON
0957 0 0027      OC    /0027  P
0958 0 1412      OC    /1412  US
0959 0 3800      OC    /3800  H
095A 0 1213      DC    /1213  ST
095B 0 3129      OC    /3129  AR
095C 0 1300      DC    /1300  T
095D 0 FFFF      OC    /FFFF  TERM

*
095E 0 000E      INM02 OC    /000E  WORD COUNT
095F 0 330A      DC    /330A  CO
0960 0 0A02      OC    /0A02  O2
0961 0 0000      OC    /0000  SPACE
0962 0 0000      OC    /0000  SPACE
0963 0 1314      DC    /1314  TJ
0964 0 2925      OC    /2925  RN
0965 0 0034      OC    /0034  D
0966 0 3912      DC    /3912  IS
0967 0 3132      DC    /3132  AB
0968 0 2335      DC    /2335  LE
0969 0 0012      DC    /0012  S
096A 0 1600      OC    /1600  W
096B 0 2636      OC    /2636  OF
096C 0 3600      OC    /3600  F
096D 0 FFFF      DC    /FFFF  TERM

*
096E 0 0013      INM03 OC    /0013  WORD COUNT
096F 0 350A      DC    /350A  EO
0970 0 0A01      OC    /0A01  O1
0971 0 0000      DC    /0000  SPACE
0972 0 0000      OC    /0000  SPACE
0973 0 2913      DC    /2913  RT
0974 0 2500      OC    /2500  N
0975 0 0000      DC    /0000  ROUTINE NUMBER
0976 0 0023      UC    /0023  L
0977 0 3515      OC    /3515  EV
0978 0 2300      DC    /2300  L
0979 0 0000      OC    /0000  LEVEL NUMBER
097A 0 0036      DC    /0036  F
097B 0 3139      DC    /3139  AI
097C 0 2335      OC    /2335  LE
097D 0 3400      OC    /3400  D
097E 0 1326      OC    /1326  TO
097F 0 0039      OC    /0039  I
0980 0 2513      DC    /2513  NI
0981 0 2927      OC    /2927  RP
0982 0 FFFF      OC    /FFFF  TERM

*
0983 0 0015      INM04 OC    /0015  WORD COUNT
0984 0 350A      OC    /350A  EO
0985 0 0A02      DC    /0A02  O2
0986 0 0000      DC    /0000  SPACE
0987 0 0000      DC    /0000  SPACE
0988 0 2913      DC    /2913  RT
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88327390
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INTERRUPT FUNCTION TEST

```
0989 0 2500      DC    /2500  N
098A 0 0000      DC    /0000  ROUTINE NUMBER
098B 0 0029      DC    /0029  R
098C 0 3528      DC    /3528  EQ
098D 0 0000      OC    /0000  BLANK
098E 0 0000      OC    /0000  REQUEST NUMBER
098F 0 0037      OC    /0037  G
0990 0 3915      OC    /3915  IV
0991 0 3525      DC    /3525  EN
0992 0 0023      OC    /0023  L
0993 0 3515      DC    /3515  EV
0994 0 2300      OC    /2300  L
0995 0 0000      DC    /0000  LEVEL NUMBER
0996 0 0012      OC    /0012  S
0997 0 2915      OC    /2915  RV
0998 0 3334      DC    /3334  CD
0999 0 FFFF      DC    /FFFF  TERM

*
099A 0 0018      INM05 DC    /0018  WORD COUNT
099B 0 350A      OC    /350A  EO
099C 0 0A03      DC    /0A03  O3
099D 0 0000      DC    /0000  SPACE
099E 0 0000      DC    /0000  SPACE
099F 0 2913      DC    /2913  RT
09A0 0 2500      DC    /2500  N
09A1 0 0000      OC    /0000  ROUTINE NUMBER
09A2 0 0029      DC    /0029  R
09A3 0 3528      DC    /3528  EQ
09A4 0 0000      DC    /0000  BLANK
09A5 0 0000      DC    /0000  REQUEST NUMBER
09A6 0 0039      DC    /0039  I
09A7 0 2513      DC    /2513  NT
09A8 0 2927      DC    /2927  RP
09A9 0 3400      OC    /3400  D
09AA 0 1639      OC    /1639  W1
09AB 0 1338      DC    /1338  TH
09AC 0 0034      OC    /0034  O
09AD 0 3912      OC    /3912  IS
09AE 0 3132      OC    /3132  AB
09AF 0 2335      DC    /2335  LE
09B0 0 0012      DC    /0012  S
09B1 0 1600      OC    /1600  W
09B2 0 2625      DC    /2625  ON
09B3 0 FFFF      OC    /FFFF  TERM

*
09B4 0 0013      INM06 DC    /0013  WORD COUNT
09B5 0 350A      DC    /350A  EO
09B6 0 0A04      DC    /0A04  O4
09B7 0 0000      DC    /0000  SPACE
09B8 0 0000      DC    /0000  SPACE
09B9 0 2913      OC    /2913  RT
09BA 0 2500      DC    /2500  N
09BB 0 0000      OC    /0000  ROUTINE NUMBER
09BC 0 0016      DC    /0016  W
09BD 0 2926      DC    /2926  RD
09BE 0 2537      DC    /2537  NG
09BF 0 0039      DC    /0039  I
09C0 0 2312      DC    /2312  LS
09C1 0 1600      DC    /1600  W
09C2 0 2625      DC    /2625  ON
09C3 0 0035      OC    /0035  E
09C4 0 2929      DC    /2929  RR
09C5 0 0039      DC    /0039  I
09C6 0 2513      DC    /2513  NT
09C7 0 2927      DC    /2927  RP
09C8 0 FFFF      DC    /FFFF  TERM

*
09C9 0 000C      INM07 DC    /000C  WORD COUNT
```

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88327890
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IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

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INTERPUPT FUNCTION TEST

09CA 0	310A	DC	/310A	AO
09CB 0	0A01	OC	/0A01	O1
09CC 0	0000	OC	/0000	SPACE
09CD 0	0000	OC	/0000	SPACE
09CE 0	2729	OC	/2729	PR
09CF 0	2637	DC	/2637	OG
0900 0	2931	OC	/2931	RA
0901 0	2400	OC	/2400	M
0902 0	3326	OC	/3326	CO
0903 0	2427	OC	/2427	MP
0904 0	2335	OC	/2335	LE
0905 0	1335	OC	/1335	TE
0906 0	FFFF	OC	/FFFF	TERM
* INM08				
0907 0	000E	OC	/000E	WORD COUNT
0908 0	330A	OC	/330A	CO
0909 0	0A03	OC	/0A03	O3
090A 0	0000	OC	/0000	SPACE
090B 0	0000	OC	/0000	SPACE
090C 0	2714	OC	/2714	PU
090D 0	1238	OC	/1238	SH
090E 0	0033	OC	/0033	C
090F 0	3500	DC	/3500	E
09E0 0	3925	OC	/3925	IN
09E1 0	1329	OC	/1329	TR
09E2 0	2700	OC	/2700	P
09E3 0	3214	OC	/3214	BU
09E4 0	1313	OC	/1313	TT
09E5 0	2625	DC	/2625	ON
09E6 0	FFFF	OC	/FFFF	TERM
* INM09				
09E7 0	000F	OC	/000F	WORD COUNT
09E8 0	330A	OC	/330A	CO
09E9 0	0A04	OC	/0A04	O4
09EA 0	0000	OC	/0000	SPACE
09EB 0	0000	OC	/0000	SPACE
09EC 0	2714	OC	/2714	PU
09ED 0	1238	OC	/1238	SH
09EE 0	0033	OC	/0033	C
09EF 0	2625	OC	/2625	ON
09F0 0	1200	OC	/1200	S
09F1 0	3925	OC	/3925	IN
09F2 0	1329	OC	/1329	TR
09F3 0	2700	OC	/2700	P
09F4 0	3214	OC	/3214	BU
09F5 0	1313	OC	/1313	TT
09F6 0	2625	OC	/2625	ON
09F7 0	FFFF	OC	/FFFF	TERM
* INM10				
09F8 0	0011	OC	/0011	WORD COUNT
09F9 0	330A	OC	/330A	CO
09FA 0	0A05	OC	/0A05	O5
09FB 0	0000	OC	/0000	SPACE
09FC 0	0000	OC	/0000	SPACE
09FD 0	1235	OC	/1235	SE
09FE 0	1300	OC	/1300	T
09FF 0	1329	OC	/1329	TR
0A00 0	3133	OC	/3133	AC
0A01 0	3500	OC	/3500	E
0A02 0	2426	OC	/2426	MO
0A03 0	3435	OC	/3435	OE
0A04 0	0000	OC	/0000	BLANK
0A05 0	2714	OC	/2714	PU
0A06 0	1238	OC	/1238	SH
0A07 0	0012	OC	/0012	S
0A08 0	1331	OC	/1331	TA
0A09 0	2913	OC	/2913	RT
0A0A 0	FFFF	OC	/FFFF	TERM

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INTERRUPT FUNCTION TEST

0A0B 0	0010	* INM11	OC	/0010	WORD COUNT
0A0C 0	330A		OC	/330A	CO
0A0D 0	0A06		OC	/0A06	O6
0A0E 0	0000		OC	/0000	SPACE
0A0F 0	0000		OC	/0000	SPACE
0A10 0	1235		OC	/1235	SE
0A11 0	1300		OC	/1300	T
0A12 0	2914		OC	/2914	RU
0A13 0	2500		OC	/2500	N
0A14 0	2426		OC	/2426	MO
0A15 0	3435		OC	/3435	OE
0A16 0	0000		OC	/0000	BLANK
0A17 0	2714		DC	/2714	PU
0A18 0	1238		OC	/1238	SH
0A19 0	0012		OC	/0012	S
0A1A 0	1331		OC	/1331	TA
0A1B 0	2913		OC	/2913	RT
0A1C 0	FFFF		OC	/FFFF	TERM
* INM12					
0A1D 0	000F	OC	/000F	WORD COUNT	
0A1E 0	350A	OC	/350A	EO	
0A1F 0	0A05	OC	/0A05	O5	
0A20 0	0000	OC	/0000	SPACE	
0A21 0	0000	OC	/0000	SPACE	
0A22 0	1235	OC	/1235	SE	
0A23 0	2814	OC	/2814	QU	
0A24 0	3525	OC	/3525	EN	
0A25 0	3335	DC	/3335	CE	
0A26 0	0035	OC	/0035	E	
0A27 0	2929	OC	/2929	RR	
0A28 0	2629	OC	/2629	OR	
0A29 0	0029	OC	/0029	R	
0A2A 0	1325	OC	/1325	TN	
0A2B 0	0000	OC	/0000	BLANK	
0A2C 0	0000	OC	/0000	ROUTINE NUMBER	
0A2D 0	FFFF	OC	/FFFF	TERM	
* INM13					
0A2E 0	0015	OC	/0015	WORD COUNT	
0A2F 0	350A	OC	/350A	EO	
0A30 0	0A06	OC	/0A06	O6	
0A31 0	0000	OC	/0000	SPACE	
0A32 0	0000	OC	/0000	SPACE	
0A33 0	2913	OC	/2913	RT	
0A34 0	2500	OC	/2500	N	
0A35 0	0400	OC	/0400	4	
0A36 0	2335	OC	/2335	LE	
0A37 0	1535	OC	/1535	VE	
0A38 0	2300	DC	/2300	L	
0A39 0	0000	OC	/0000	REQUEST NUMBER	
0A3A 0	0039	OC	/0039	I	
0A3B 0	2513	OC	/2513	NT	
0A3C 0	2927	OC	/2927	RP	
0A3D 0	3400	OC	/3400	D	
0A3E 0	1638	OC	/1638	WH	
0A3F 0	3923	OC	/3923	IL	
0A40 0	3500	OC	/3500	E	
0A41 0	2431	OC	/2431	MA	
0A42 0	1222	OC	/1222	SK	
0A43 0	3534	DC	/3534	EO	
0A44 0	FFFF	OC	/FFFF	TERM	
* INM14					
0A45 0	0011	OC	/0011	WORD COUNT	
0A46 0	350A	OC	/350A	EO	
0A47 0	0A07	OC	/0A07	O7	
0A48 0	0000	OC	/0000	SPACE	
0A49 0	0000	OC	/0000	SPACE	
0A4A 0	2913	OC	/2913	RT	

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INTERRUPT FUNCTION TEST

0A4B 0	2500	DC	/2500	N	88329930
0A4C 0	0200	DC	/0200	2	88329940
0A4D 0	3925	OC	/3925	IN	88329950
0A4E 0	1300	DC	/1300	T	88329960
0A4F 0	0000	DC	/0000	LEVEL NUMBER	88329970
0A50 0	0039	OC	/0039	I	88329980
0A51 0	2312	OC	/2312	LS	88329990
0A52 0	1600	OC	/1600	W	88330000
0A53 0	2526	OC	/2526	NO	88330010
0A54 0	1300	DC	/1300	T	88330020
0A55 0	1935	OC	/1935	ZE	88330030
0A56 0	2926	OC	/2926	RO	88330040
0A57 0	FFFF	DC	/FFFF	TERM	88330050
0A58 0	000F	INM15 DC	/000F	WORD COUNT	88330060
0A59 0	340A	DC	/340A	DO	88330070
0A5A 0	0A01	DC	/0A01	01	88330080
0A5B 0	0000	DC	/0000	SPACE	88330090
0A5C 0	0000	DC	/0000	SPACE	88330100
0A5D 0	2913	DC	/2913	RT	88330110
0A5E 0	2500	DC	/2500	N	88330120
0A5F 0	0000	DC	/0000	ROUTINE NUMBER	88330130
0A60 0	0000	DC	/0000	BLANK	88330140
0A61 0	2729	DC	/2729	PR	88330150
0A62 0	3926	DC	/3926	IO	88330160
0A63 0	2939	DC	/2939	RI	88330170
0A64 0	1318	DC	/1318	TY	88330180
0A65 0	0033	DC	/0033	C	88330190
0A66 0	3835	DC	/3835	ME	88330200
0A67 0	3322	DC	/3322	CK	88330210
0A68 0	FFFF	DC	/FFFF	TERM	88330220
0A6A 00	00000000	DEC	0		88330230
0A6C 0	003E	INM16 DC	/003E	WORD COUNT	88330240
0A6D 0	0000	DC	/0000	SPACE	88330250
0A6E 0	0000	DC	/0000	SPACE	88330260
0A6F 0	0000	DC	/0000	SPACE	88330270
0A70 0	0000	DC	/0000	SPACE	88330280
0A71 0	2935	DC	/2935	RE	88330290
0A72 0	2800	DC	/2800	Q	88330300
0A73 0	1235	DC	/1235	SE	88330310
0A74 0	2814	DC	/2814	QU	88330320
0A75 0	3525	DC	/3525	EM	88330330
0A76 0	3335	DC	/3335	CE	88330340
0A77 0	0000	DC	/0000	BLANK	88330350
0A78 0034		IN16V BSS	52		88330360
0AAC 0	003E	INM17 DC	/003E	WORD COUNT	88330370
0AAD 0	0000	DC	/0000	SPACE	88330380
0AAE 0	0000	DC	/0000	SPACE	88330390
0AAF 0	0000	DC	/0000	SPACE	88330400
0AB0 0	0000	DC	/0000	SPACE	88330410
0AB1 0	1229	DC	/1229	SR	88330420
0AB2 0	1533	DC	/1533	VC	88330430
0AB3 0	0012	DC	/0012	S	88330440
0AB4 0	3528	DC	/3528	EQ	88330450
0AB5 0	1435	DC	/1435	UE	88330460
0AB6 0	2533	DC	/2533	NC	88330470
0AB7 0	3500	DC	/3500	I	88330480
0AB8 0034		IN17V BSS	52		88330490
0AEC 0	0017	INM18 DC	/0017	WORD COUNT	88330500
0AEO 0	350A	DC	/350A	EO	88330510
0AEE 0	0A08	DC	/0A08	OB	88330520
0AEF 0	0000	DC	/0000	SPACE	88330530
0AFO 0	0000	OC	/0000	SPACE	88330540
0AF1 0	2913	DC	/2913	RT	88330550
0AF2 0	2500	DC	/2500	N	88330560

INTERRUPT FUNCTION TEST

0AF3 0	0600	OC	/0600	6	88330610
0AF4 0	1329	OC	/1329	TR	88330620
0AF5 0	3133	OC	/3133	AC	88330630
0AF6 0	3500	OC	/3500	E	88330640
0AF7 0	3439	OC	/3439	01	88330650
0AF8 0	3400	OC	/3400	0	88330660
0AF9 0	2526	DC	/2526	ND	88330670
0AFA 0	1300	OC	/1300	T	88330680
0AFB 0	3925	OC	/3925	IN	88330690
0AFC 0	1329	OC	/1329	TR	88330700
0AFD 0	2713	OC	/2713	PT	88330710
0AFE 0	0026	DC	/0026	0	88330720
0AFF 0	2500	OC	/2500	N	88330730
0B00 0	2731	DC	/2731	PA	88330740
0B01 0	1212	OC	/1212	SS	88330750
0B02 0	0000	OC	/0000	BLANK	88330760
0B03 0	000C	DC	/0000	PASS NUMBER	88330770
0B04 0	FFFF	DC	/FFFF	TERM	88330780
0B05 0	0018	INM19 DC	/0018	WORD COUNT	88330790
0B06 0	350A	OC	/350A	EO	88330800
0B07 0	0A09	OC	/0A09	09	88330810
0B08 0	0000	OC	/0000	SPACE	88330820
0B09 0	0000	DC	/0000	SPACE	88330830
0B0A 0	2913	DC	/2913	RT	88330840
0B0B 0	2500	OC	/2500	N	88330850
0B0C 0	0600	DC	/0600	6	88330860
0B0D 0	3517	DC	/3517	EX	88330870
0B0E 0	2735	OC	/2735	PE	88330880
0B0F 0	3313	DC	/3313	CT	88330890
0B10 0	3534	DC	/3534	ED	88330900
0B11 0	0039	DC	/0039	I	88330910
0B12 0	2513	DC	/2513	NT	88330920
0B13 0	2927	OC	/2927	RP	88330930
0B14 0	1300	DC	/1300	T	88330940
0B15 0	3629	OC	/3629	FR	88330950
0B16 0	2624	DC	/2624	OM	88330960
0B17 0	0039	DC	/0039	I	88330970
0B18 0	2512	DC	/2512	NS	88330980
0B19 0	1329	OC	/1329	TR	88330990
0B1A 0	2500	DC	/2500	N	88331000
0B1B 0	0000	DC	/0000	BLANK	88331010
0B1C 0	0000	OC	/0000	EXPECTED NUMBER	88331020
0B1D 0	0000	DC	/0000	BLANK	88331030
0B1E 0	3726	OC	/3726	JO	88331040
0B1F 0	1300	OC	/1300	T	88331050
0B20 0	0000	OC	/0000	ACTUAL NUMBER	88331060
0B21 0	FFFF	OC	/FFFF	TERM	88331070
0B22 0	000F	INM20 DC	/000F	WORD COUNT	88331080
0B23 0	350A	DC	/350A	EO	88331090
0B24 0	0A31	DC	/0A31	OA	88331100
0B25 0	0000	DC	/0000	SPACE	88331110
0B26 0	0000	DC	/0000	SPACE	88331120
0B27 0	3326	DC	/3326	CO	88331130
0B28 0	2512	DC	/2512	NS	88331140
0B29 0	2623	OC	/2623	DL	88331150
0B2A 0	3500	OC	/3500	E	88331160
0B2B 0	3214	OC	/3214	BU	88331170
0B2C 0	1313	OC	/1313	TT	88331180
0B2D 0	2625	OC	/2625	ON	88331190
0B2E 0	0036	DC	/0036	F	88331200
0B2F 0	3139	OC	/3139	A1	88331210
0B30 0	2335	OC	/2335	LE	88331220
0B31 0	3400	OC	/3400	D	88331230
0B32 0	FFFF	DC	/FFFF	TERM	88331240
0B33 0	0017	INM21 DC	/0017	WORD COUNT	88331250

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INTERRUPT FUNCTION TEST

INTERRUPT FUNCTION TEST

0834 0	340A	DC	/340A	00
0835 0	0A02	OC	/0A02	02
0836 0	0000	DC	/0000	SPACE
0837 0	0000	OC	/0000	SPACE
0838 0	3326	OC	/3326	CO
0839 0	2512	DC	/2512	NS
083A 0	2623	DC	/2623	DL
083B 0	3500	DC	/3500	E
083C 0	3214	DC	/3214	BU
083D 0	1313	OC	/1313	TT
083E 0	2625	DC	/2625	ON
083F 0	0026	OC	/0026	D
0840 0	2500	DC	/2500	N
0841 0	2335	OC	/2335	LE
0842 0	1535	OC	/1535	VE
0843 0	2300	OC	/2300	L
0844 0	0000	OC	/0000	LEVEL NUMBER
0845 0	0039	OC	/0039	I
0846 0	2312	DC	/2312	LS
0847 0	1600	DC	/1600	M
0848 0	3239	OC	/3239	BI
0849 0	1300	OC	/1300	T
084A 0	0000	OC	0	ILSW BIT
084B 0	FFFF	OC	/FFFF	TERM

084C 0	0021	INM22 DC	/0021	WORD COUNT
084D 0	330A	OC	/330A	CO
084E 0	0A07	DC	/0A07	07
084F 0	0000	DC	/0000	SPACE
0850 0	0000	OC	/0000	SPACE
0851 0	1235	OC	/1235	SE
0852 0	1300	DC	/1300	T
0853 0	3439	OC	/3439	OI
0854 0	1231	DC	/1231	SA
0855 0	3223	DC	/3223	BL
0856 0	3500	DC	/3500	E
0857 0	2625	OC	/2625	DN
0858 0	0038	DC	/0038	M
0859 0	3913	DC	/3913	IT
085A 0	0033	DC	/0033	C
085B 0	3500	OC	/3500	E
085C 0	3125	OC	/3125	AN
085D 0	3400	DC	/3400	D
085E 0	3326	DC	/3326	CO
085F 0	2512	DC	/2512	NS
0860 0	0032	DC	/0032	B
0861 0	1325	DC	/1325	TM
0862 0	1200	DC	/1200	S
0863 0	0000	DC	/0000	BLANK
0864 0	1235	DC	/1235	SE
0865 0	1300	DC	/1300	T
0866 0	1329	DC	/1329	TR
0867 0	3133	DC	/3133	AC
0868 0	3500	DC	/3500	E
0869 0	3125	DC	/3125	AN
086A 0	3400	DC	/3400	D
086B 0	1213	DC	/1213	ST
086C 0	3129	DC	/3129	AR
086D 0	1300	DC	/1300	T
086E 0	FFFF	DC	/FFFF	TERM

0870 0	0000	BSS E 0		
0870 0	0017	INM23 DC	/0017	WORD COUNT
0871 0	350A	DC	/350A	EO
0872 0	CA32	DC	/0A32	08
0873 0	0000	DC	/0000	SPACE
0874 0	0000	DC	/0000	SPACE
0875 0	2913	DC	/2913	RT

0876 0	2500	DC	/2500	N
0877 0	0100	OC	/0100	I
0878 0	3925	OC	/3925	IN
0879 0	1329	OC	/1329	TR
087A 0	2713	OC	/2713	PT
087B 0	0025	OC	/0025	N
087C 0	2613	OC	/2613	OT
087D 0	0039	OC	/0039	I
087E 0	2538	OC	/2538	NH
087F 0	3932	OC	/3932	IB
0880 0	3913	OC	/3913	IT
0881 0	3534	OC	/3534	EO
0882 0	0031	OC	/0031	A
0883 0	3613	OC	/3613	FT
0884 0	3529	OC	/3529	ER
0885 0	0000	OC	0	SPACE
0886 0	0000	OC	/0000	
0887 0	0000	OC	/0000	
0888 0	FFFF	OC	/FFFF	TERM
0889 0	0014	INM24 OC	/0014	WORD COUNT
088A 0	330A	DC	/330A	CO
088B 0	0A08	OC	/0A08	OC
088C 0	0000	OC	0	SPACE
088D 0	0000	DC	0	SPACE
088E 0	2935	OC	/2935	RE
088F 0	2731	OC	/2731	PA
0890 0	3929	OC	/3929	IR
0891 0	0036	OC	/0036	F
0892 0	3139	DC	/3139	AI
0893 0	2314	OC	/2314	LV
0894 0	2935	OC	/2935	RE
0895 0	0032	OC	/0032	B
0896 0	3536	OC	/3536	EF
0897 0	2629	OC	/2629	OR
0898 0	3500	DC	/3500	E
0899 0	3326	OC	/3326	CO
089A 0	2513	OC	/2513	NT
089B 0	3925	DC	/3925	IN
089C 0	1439	DC	/1439	UI
089D 0	2537	OC	/2537	NG
089E 0	FFFF	OC	/FFFF	TERM
089F 0	0000	INM25 OC	/0000	WORD COUNT
08A0 0	350A	OC	/350A	EO
08A1 0	0A33	OC	/0A33	OC
08A2 0	0000	DC	0	SPACE
08A3 0	0000	OC	0	SPACE
08A4 0	3923	OC	/3923	IL
08A5 0	2335	OC	/2335	LE
08A6 0	3731	DC	/3731	GA
08A7 0	2300	OC	/2300	L
08A8 0	2913	OC	/2913	RT
08A9 0	2500	OC	/2500	N
08AA 0	3525	OC	/3525	EN
08AB 0	1329	OC	/1329	TR
08AC 0	1800	OC	/1800	Y
08AD 0	FFFF	OC	/FFFF	TERM
08AE 0	012D	ENO	START	

88331970
88331980
88331990
88332000
88332010
88332020
88332030
88332040
88332050
88332060
88332070
88332080
88332090
88332100
88332110
88332120
88332130
88332140
88332150
88332160
88332170
88332180
88332190
88332200
88332210
88332220
88332230
88332240
88332250
88332260
88332270
88332280
88332290
88332300
88332310
88332320
88332330
88332340
88332350
88332360
88332370
88332380
88332390
88332400
88332410
88332420
88332430
88332440
88332450
88332460
88332470
88332480
88332490
88332500
88332510
88332520
88332530
88332540
88332550

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INTERRUPT FUNCTION TEST

INTERRUPT FUNCTION TEST

CROSS REFERENCE LISTING

SYMBOL	VALUE	REFERENCES
BSI	01FC	01C9
BSICK	01F8	0107
BSWO	018A	013D, 016C, 04E3, 0501, 050C, 0513
BSW00	0192	0136, 013F, 0147, 016D, 018A, 04E5, 0503, 050E, 0515
BSW01	0193	018C
BSW1	018C	
CMTRP	068B	03A6, 0419, 063B, 063E, 0641, 0644, 0647, 064A, 0640, 0650, 0653, 0656, 0659, 065C, 065F, 0662, 0665, 0668, 066B, 066E, 0671, 0674, 0677, 067A, 067D, 0680, 0683, 0686, 0689, 068C
CMT00	06AC	06A7
CMT01	06B1	06A8
CMT02	06A8	06A3
CMT03	06B4	06A9
CMT04	06C2	0601
CMT05	06BF	068B
CMT06	06CB	0688
CNC02	0194	012F
CNC03	0196	0132
CNM00	06CA	0309, 043D, 0692, 06A8, 06C4, 06CC
CNSNS	0436	039E, 0417, 06D8, 06E7
CNST0	04DC	048B
CNST1	04DD	048F
CNST2	04E0	04C7
CNTRL	015B	0179
CN001	01F9	010B
CN101	0280	020A, 0261, 0263, 02BF, 0300
CN102	0281	020C, 0234, 0247, 027A, 06A1
CN103	0282	0212, 0252
CN104	0283	
CN105	0284	020E, 0280
CN200	02B2	029C, 02A2
CN201	02B4	02AA
CN300	0319	02C1, 020A, 02DF, 02F5, 0306
CN301	031E	02CC, 02D8, 02F9, 02F0, 02FF, 0302, 0304
CN302	031A	0303
CN303	031B	02E9, 069D
CN400	042B	013B, 0380, 0391, 043B
CN401	0429	013B, 0349, 034A, 0405
CN402	042A	0352
CN403	042B	0410, 0697
CN404	042C	036B, 03EA
CN405	042D	03DA
CN406	042E	03FB
CN407	042F	03FE
CN408	0430	0401
CN409	0431	03B0
CN500	048A	045D, 0460, 0463
CN501	048B	045F, 0461, 0462
CN502	048C	0483
COOCV	058E	0566, 058C
COOC1	0595	05B1
COOC2	05A7	05A6
COOC3	05B2	05AC
COOC4	05B6	05BF, 0590, 0591
CODW0	05BE	0561, 0568, 0595, 05B5
COD00	05C0	059C, 059D, 05A1, 05A2
COD01	05C1	05A9, 05B2
COD02	05C2	05B4
COMP	0433	03B7
CON01	013E	
CON03	016C	0165
CON05	0166	0161
CON06	0159	0170
CTRL1	014E	0149
CTRL2	0155	0150

CTRL3	0158	014D, 0154
DELAY	05F3	036F, 039C, 05FA, 05FC, 05FD
DELY1	05FF	05F6
DELY2	0600	05F7, 05F8
ECKSW	0286	0214, 0230
ERALT	04F9	0410, 04F8, 04FA, 0910
ERRIO	0522	04FF, 0508
ERROR	04F5	01ED, 023B, 02EE, 03CF, 0315, 034C, 0371, 0387, 03A0, 0441, 047B, 04FB, 04FC, 051A, 051C, 06AC, 0681, 06BF, 06CE
ERR01	050B	04FE
ERR02	050A	0507
ERR03	0513	051F
FAIL	01E1	01C4
HOLD	0432	0355, 0386, 0388, 03B9
ICTR	01F6	01C5, 0106, 0605
ILSAV	06D6	0350, 0359, 03AA, 0594, 068A, 06C6, 06CB
ILSW	06D4	0602, 060B, 0693, 06DA, 06E9
INCN	049F	0494
INLVT	092E	017C, 01FE, 0226, 0269, 0292, 0285, 02D0, 032C, 0342, 035C, 0374, 038B, 044E, 0686, 0903, 0919
INM01	0949	0270
INM02	095E	024E, 040C
INM03	096E	0200, 0228, 023A, 0268, 02B7, 0202, 02F0, 032E, 0344, 034E, 0360, 0373, 0378, 0389, 0684
INM04	0983	0202, 022A, 026D, 02B9, 02D4, 0317, 0330, 0346, 035E, 0376, 0443, 068E, 0690, 06B3
INM05	099A	0204, 022C, 026F, 0332, 041B, 041F, 0420, 06AE
INM06	09B4	0206, 0336, 0358, 06D0
INM07	09C9	0173
INM08	0907	036E, 03E4
INM09	09E7	039B
INM10	09F8	0384, 03C0, 045B
INM11	0A0B	038C, 03D4, 0408, 0473
INM12	0A1D	017E, 01B2
INM13	0A2E	02D6, 0311
INM14	0A45	022E, 0271, 06C1
INM15	0A58	0294, 0334, 04EC
INM16	0A6C	04EF
INM17	0AAC	04F2
INM18	0AEC	0479, 047D
INM19	0B05	0905, 091B, 091F
INM20	0B22	03A2
INM21	0B33	03AB, 03B0, 03BF, 03C3
INM22	0B4C	03F5
INM23	0B70	01B2, 01CA, 01EF
INM24	0B89	01F2
INM25	0B9F	01A4
INSA0	0924	0908
INTER	01A2	01A1
INTST	048E	012D, 01DD, 0255, 02A6, 02E5, 0413, 0486, 049D
INT00	01A6	019B, 01E9
INT01	01FE	019C
INT02	0292	019D
INT03	02B5	019E
INT04	032B	019F
INT05	0446	01A0
IN001	01C1	01E7
IN002	010A	01E8
IN16V	0A7B	04C8, 04CE, 06E2, 06F0, 0704, 071B, 072C, 0740, 0754, 0768, 077C, 0790, 07A4, 0788, 07CC, 07E0, 07F4, 0808, 081C, 0830, 0844, 085B, 086C, 0880, 0894, 08A8, 08BC, 0800, 08E4
IN17V	0ABB	04CA, 04D0, 06E4, 06F6, 070A, 071E, 0732, 0746, 075A, 076E, 0782, 0796, 07AA, 07BE, 0702, 07E6, 07FA, 080E, 0822, 0836, 084A, 085E, 0872, 0886, 089A, 08AE, 08C2, 08D6, 08EA
IDARA	0580	0555, 056A, 0573, 0575, 058C
IS1NT	028A	0186, 01C1, 010C, 01E1, 01E4, 0224, 0230, 0259, 0250, 0260, 0262, 0265
LH1NO	05BF	0593, 0597, 05AB, 05AE

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LOG 0523 0171,0180,01A2,01F0,024C,0278,0356,036C,0382,038A,
0399,03C1,03C8,03D2,03E2,03F3,0406,040A,0459,0471,
04EA,04E0,04F0,0508,0520,0547,0549,055C
LOG01 0524
LOG02 0530 0538,053A
LOG05 0538 0536
LOG06 0541 0524,0564
LPERR 0520 01E8,021E,0275,02C4,033E,0364,037C,0395,03F1,0453,
0519
LVLIX 018E 0158,0220,0298,02C8,0402
LVLST 04AA 0208,0288,0338,03E8,04B4
LVLS1 0486 04A0
LVLS2 0487 0480
LVLO1 063A 0486
LVLO2 0643
LVLO3 0646
LVLO4 0649
LVLO5 064C
LVLO6 064F
LVLO7 0652
LVLO8 0655
LVLO9 0658
LVLO10 0658
LVLO11 065E
LVLO12 0661
LVLO13 0664
LVLO14 0667
LVLO15 066A
LVLO16 066D
LVLO17 0670
LVLO18 0673
LVLO19 0676
LVLO20 0679
LVLO21 067C
LVLO22 067F
LVLO23 0682
LVLO24 0685
LVLO25 0688
LVLO26 0630
LVLO27 0640 042C
LVSAV 0198 0148,0152,0156,0216,0219,029D,02CD,03C6,03DD
LVST1 04AE 0483
MASK0 0320 01A6,02C6,0312,048F,0525
MASK1 0322 01A8,02C7,0313,0491,0527
NEST1 04A0 024A,03F9,04A7
NSTCN 04A9 04A2
OPINO 019A 0146,0529
PLEXT 0607 0188,01CE
PL1 01C4 0189,01F7
PL2 0105 01CC,01F8
POLER 01E9 01C7,0108
POLL 0601 01F5,0604
PRIPT 04E2 02A4,0300,03E6,04F3
PRIST 0488 0296,03C4,03D8,040A
PRIXT 04F3 04E9
PRIO1 06E0 04C2,06E8
PRIO2 0716 0721
PRIO3 072A 0735
PRIO4 073E 0749
PRIO5 0752 0750
PRIO6 0766 0771
PRIO7 077A 0785
PRIO8 078E 0799
PRIO9 07A2 07AD
PRIO10 0786 07C1
PRIO11 07CA 0705
PRIO12 070E 07E9
PRIO13 07F2 07FD

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PRI14 0806 0811
PRI15 081A 0825
PRI16 082E 0839
PRI17 0842 0840
PRI18 0856 0861
PRI19 086A 0875
PRI20 087E 0889
PRI21 0892 0890
PRI22 08A6 08B1
PRI23 088A 08C5
PRI24 08CE 0809
PRI25 08E2 08E0
PRI26 06EE 040C,06F9
PRI27 0702 0420
PRSN 0586 053C
PRSNS 0584 0530,0540
PRWRT 0588 052F,0538
PRO20 0723 071C
PRO21 0710 0725
PRO30 0737 0730
PRO31 0731 0739
PRO32 073C 0737
PRO40 0748 0744
PRO41 0745 0740
PRO42 0750 0748
PRO50 075F 0758
PRO51 0759 0761
PRO52 0764 075F
PRO60 0773 076C
PRO61 0760 0775
PRO62 0778 0773
PRO70 0787 0780
PRO71 0781 0789
PRO72 078C 0787
PRO80 0798 0794
PRO81 0795 0790
PRO82 07A0 0798
PRO90 07AF 07A8
PRO91 07A9 07B1
PRO92 07B4 07AF
PRIO0 07C3 078C
PRIO1 0780 07C5
PRIO2 07C8 07C3
PRIO10 0707 07D0
PRIO11 0701 0709
PRIO12 070C 0707
PRIO20 07E8 07E4
PRIO21 07E5 07E0
PRIO22 07F0 07E8
PRIO30 07FF 07F8
PRIO31 07F9 0801
PRIO32 0804 07FF
PRIO40 0813 080C
PRIO41 0800 0815
PRIO42 0818 0813
PRIO50 0827 0820
PRIO51 0821 0829
PRIO52 082C 0827
PRIO60 0838 0834
PRIO61 0835 0830
PRIO62 0840 0838
PRIO70 084F 0848
PRIO71 0849 0851
PRIO72 0854 084F
PRIO80 0863 085C
PRIO81 0850 0865
PRIO82 0868 0863
PRIO90 0877 0870

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PR191	0871	0879
PR192	087C	0877
PR200	088B	0884
PR201	0885	088D
PR202	0890	0888
PR210	089F	0898
PR211	0899	08A1
PR212	08A4	089F
PR220	0883	08AC
PR221	08A0	0895
PR222	0888	0883
PR230	08C7	08C0
PR231	08C1	08C9
PR232	08CC	08C7
PR240	08D8	08D4
PR241	08D5	08D0
PR242	08E0	08D8
PR250	08EF	08E8
PR251	08E9	08F1
PR252	08F4	08EF
PR260	06F8	06F4
PR261	06F5	06F0
PR262	0700	04D6,06FB
PR270	070F	0708
PR271	0709	0711
PR272	0714	04D8,070F
PSSW	0285	0210,0240,028E,02E2
REOCE	0712	0703,0709
REGER	06E0	06E1
REQTR	06FE	0422,06EF,06F5
RE000	0726	0717,0710
RE001	073A	0728,0731
RE002	074E	073F,0745
RE003	0762	0753,0759
RE004	0776	0767,076D
RE005	078A	0778,0781
RE006	079E	078F,0795
RE007	0782	07A3,07A9
REQ08	07C6	07B7,07BD
REQ09	070A	07C8,0701
RE010	07EE	070F,07E5
RE011	0802	07F3,07F9
REQ12	0816	0807,080D
REQ13	082A	0818,0821
REQ14	083E	082F,0835
REQ15	0852	0843,0849
REQ16	0866	0857,085D
REQ17	087A	0868,0871
REQ18	088E	087F,0885
REQ19	08A2	0893,0899
REQ20	08B6	08A7,08A0
REQ21	08CA	08B8,08C1
REQ22	08DE	08CF,08D5
REQ23	08F2	08E3,08E9
RTN	0198	016A
RTNNO	018F	015A,0160,0162,0166,016B,017A,010A,0250,02A8,02E7,
		040E,0481,0695,0698
		010F,0257,02A0,02EC,0415,0488
RTNRT	0177	0242,027F
RT100	0212	021C,025F
RT101	0226	0237,06AF,068D,06C8
RT104	0238	023C,0266
RT105	0259	023F,0273
RT106	0267	0249
RT107	0279	0258
RT108	0260	0278
RT109	0234	0245
RT110	0250	02E4
RT300	028F	

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RT301	0200	02C2,02FE,0305
RT302	0208	
RT303	02EE	020C
RT304	02F9	02DE
RT305	0200	02F3,0300,0314,0318
RT306	02C8	02F8
RT307	02FF	02FB
RT308	0306	069F
RT309	030F	0308
RT310	0315	030C
RT311	02F4	02E1
RT400	034A	033C
RT401	0438	033A,0445
RT402	0350	0340
RT403	0374	0366
RT404	036C	0362
RT405	038A	037E
RT406	03A4	0397
RT407	0300	
RT408	03E6	
RT409	03E2	
RT410	0417	03E0
RT411	0420	03EF
RT412	0382	037A
RT413	035C	034F,0355
RT414	040E	032A
RT415	03C4	03A3
RT500	045D	047F,0924,0925,0926,0927,0928,0929,092A,092B,092C,
		092D
RT501	0467	0466,047E,0921
RT502	047F	0451,0470
RT504	0481	0448,0475
RUNSW	0199	0137,0143,0243,0328,0446,06C2
SEQCK	0191	0177,01DC,0253,02A8,02EA,0411,0484
SERVIC	0607	0232,0283,042E,04A9,06A5,0608
SET01	048C	04C1
SET02	04C8	04C0
SIX	0190	0163
SNSWS	0188	0158
START	0120	0175,0184,0195,08AE
SVEXT	062E	0627
SVINT	0609	02F1,038E,03A4,0306,0438,049F,04F8,062E
SVINO	0613	0623,062C
SVIN1	0615	0610
SVIO	0638	060A,0619,061A,062D
SV0	0630	0621
SV1	0631	0613
SV2	0632	0611
SV3	0633	0628
SV4	0634	0610,0615,061E,0620,0628
SV5	0635	0614,0617,0618
SV6	0636	0612,0618,0629
SV7	0637	0600,0624
TRACE	08F6	044A,08F8,08FA,08FE,0901,0907,090F,0912
TRACO	0903	08FC
TRAC1	0914	090A,0920
TRAC2	0912	
TRAER	0477	0450,0468,046A
TRCNO	0921	08F8
TRCN1	0922	0914,0915,0916,0917
TRCN2	0923	08F7,0900,0911
TRINO	0480	0457,0467,046E,090D
TWRTR	0548	0528,0553
TWRT0	0570	0554
TWRT1	057E	0563
TWR01	0554	0550
TWR02	055F	055E,057C
TWR03	0579	0572

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INTERRUPT FUNCTION TEST

TWSNS	058A	0540,0557,056C
TWVRT	058C	0556,056B
UMSK0	0324	0180,02F6,0499,0543
UMSK1	0326	018F,02F7,049B,0545
VCTOR	01F5	01AB
WROSR	057F	054C,0570,0576,057A
WTA	0305	300A
WTB	03E5	300B
WTC	03F6	300C,0424,0426
WTD	0409	300D
WTE	0400	300E
WTF	045C	300F
WT1	013C	3001,01A5,01F3
WT10	0474	3010
WT11	051E	3011,0512
WT12	0537	3012,0531
WT13	0539	3013,0534
WT14	0552	3014
WT15	06C7	3015
WT2	0174	3002
WT3	0183	3003
WT4	027E	3004
WT5	024F	3005
WT6	035B	3006
WT7	0385	3007
WT8	0380	3008,0393
WT9	03CE	3009
XIO	01FA	01B1
XIOCC	028C	01B4,0222,029A,02CA,0404
XIOCK	01F7	01C6
XIOSN	056C	056F
XIOWR	056B	057B
ZONE	05C3	05A4
ZONEN	05C7	05C3
ZONE1	0502	05C4
ZONE2	0500	05C5
ZONE3	05E7	05C6

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1. PURPOSE

THE PURPOSE OF THE 1800 PROCESSOR CONTROL (PC) FUNCTION TEST IS TO LOCATE FAILING INSTRUCTIONS. EACH SEPARATE PC INSTRUCTION IS TESTED AND CHECKED FOR COMPLIANCE WITH THE PRODUCT SPECIFICATIONS. FEATURES THAT ARE NOT UNIQUE TO AN OPERATION CODE (INDEXING, INDIRECT ADDRESSING, ETC.) ARE ALSO TESTED. I/O RELATED FEATURES (INTERRUPT, CYCLE STEAL, ETC.) ARE NOT TESTED.

PROGRAM RUNNING TIME
2 USEC MACHINE - APPROXIMATELY 1 MINUTE
4 USEC MACHINE - APPROXIMATELY 2 MINUTES

2. PREREQUISITES
- 2.1 PROGRAM PREREQUISITES
- THE PROGRAM CAN BE OPERATED BY ITSELF BUT MUST BE LOADED BY THE 1800 BASIC DIAGNOSTIC LOADER.
- 2.2 EQUIPMENT PREREQUISITES
- A. 1800 PC HAVING 4096-WORD STORAGE.
B. 1442 CARD READ/PUNCH OR 1054 PAPER TAPE READER.
3. USE PROCEDURE
- 3.1 PROGRAM LOADING
- THE 1800 P C FUNCTION TEST (0884) IS LOADED BY THE 1800 BASIC LOADER. SEE THE 1800 BASIC LOADER DOCUMENTATION FOR THE DESCRIPTION OF THE LOADING PROCEDURE.

3.2 PROGRAM OPERATION

AFTER THE PROGRAM IS LOADED THE FOLLOWING NORMAL WAITS OCCUR,

LOCATION B REG SYMBOLIC	DESCRIPTION AND ACTION
3000 1X0001	START OF PROGRAM. SET ALL BIT SWITCHES ON. PRESS START.
3001 1X0011	TESTING OF BIT SWITCHES ON COMPLETE, TURN OFF, PRESS START.
3002 1X0031	TESTING OF BIT SWITCHES OFF COMPLETE SET IN OPTION, PRESS START.
3003 1X0071	PROGRAM COMPLETED. PUSH START TO RERUN PROGRAM. IF OTHER WAITS OCCUR, REFER TO SECTION 3.5 FOR ERROR ISOLATION.

ANY WAITS OTHER THAN THOSE ABOVE ARE ERROR WAITS.

- WHEN AN ERROR WAIT IS OBTAINED,
- SEE THE PROGRAM LISTING TO DETERMINE THE PROBLEM. ERROR WAITS ARE DOCUMENTED AT THE FRONT OF THE PROGRAM LISTING BY THE CONTENTS OF THE B REGISTER.
 - IF THE ERROR WAIT HAS B REGISTER LESS THAN 3069, THE OPERATOR CANNOT LOOP ON THAT ERROR. INSTEAD, THE OPERATOR SHOULD SINGLE INSTRUCTION STARTING AT THE BEGINNING OF THE FAILING ROUTINE TO DETERMINE THE EXACT FAILURE. (SECTION 3.5)
 - IF THE ERROR WAIT HAS B REGISTER GREATER THAN 3068, THE OPERATOR SHOULD, (SECTION 3.5)
 - LOOP INSTRUCTION BEING TESTED (BIT SW 8 ON)
OR IF A LARGER LOOP IS DESIRED
LOCK ON ERROR (BIT SW 12 ON)
OR
LOOP ON ROUTINE (BIT SW 10 ON)
 - SINGLE STEP TO LOCATE THE EXACT FAILURE.
 - IF NO ERROR OCCURS, BYPASS THE ERROR WAIT (BIT SW 14 ON) AND USE A SCOPE TO DETERMINE THE FAILURE.

TABLE 1

DATA ENTRY SWITCHES	DESCRIPTION
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15	
1.....	BYPASS ERROR WAIT (SEE NOTE)
1.....	LOCK ON ERROR
1.....	LOOP PROGRAM
1.....	LOOP ON ROUTINE
1.....	LOOP ON INSTRUCTION BEING TESTED
1.....	BYPASS MPL/DIV TEST

NOTE- IF ERROR OCCURS, BITS 12 OR 8 MUST BE ON TO MAKE BIT 14 EFFECTIVE.

3.3 TERMINATION

NORMAL TERMINATION OCCURS WITH PROGRAM STOPPING AT WAIT WITH B REG = 3003.

3.4 RESTART PROCEDURE

PRESS STOP, RESET, AND START BUTTONS.

3.5 ERROR WAITS

THERE ARE TWO TYPES OF ERROR CONDITIONS WHICH CAUSE ERROR WAITS.

1. ERRORS WHICH USE THE COMMON ERROR CONTROL ROUTINE (F000).
2. ERRORS WHICH OCCUR BEFORE SUFFICIENT PORTIONS OF THE HARDWARE HAVE BEEN CHECKED OUT TO ALLOW USE OF THE COMMON ERROR CONTROL ROUTINE.

ERRORS WHICH USE THE COMMON ERROR CONTROL ROUTINE HAVE B REG NUMBERS FROM /3069 AND UP. WHEN A NUMBERED WAIT OCCURS, BITS 5-15 OF THE STORAGE (B REG = 3XXX). WHEN A NUMBERED WAIT OCCURS, BITS 5-15 OF THE STORAGE BUFFER REGISTER GIVE THE ERROR IDENTIFICATION NUMBER. TO FIND THE FAILING ROUTINE, LOOK IN THE ERROR IDENTIFICATION TABLE (IN FRONT OF THE LISTING). THIS WILL GIVE YOU THE SYMBOLIC AND ACTUAL STARTING ADDRESS OF THE ROUTINE THAT FAILED.

ERRORS WHICH DO NOT USE THE COMMON ERROR CONTROL ROUTINE HAVE B REG NUMBERS FROM /3003 THRU /3068. THE INSTRUCTION REG WILL POINT DIRECTLY TO THE FAILING ROUTINE. TO FACILITATE FINDING THE START OF A TEST ROUTINE EACH TEST ROUTINE BEGINS WITH A LABEL HAVING AN A OR B AS ITS FIRST LETTER. IN THE LISTING EACH ROUTINE IS FURTHER BRACKETED BY A SOLID LINE OF ASTRISKS. TO FIND THE FAILING ROUTINE OF ERRORS WHICH DO NOT USE THE COMMON ERROR CONTROL START AT THE LOCATION SPECIFIED BY THE ERROR WAIT AND WORK UP THE LISTING (BACKWARDS) UNTIL THE FIRST SYMBOLIC LOCATION WHICH HAS A LABEL BEGINNING WITH A AND B. THIS IS THE START OF THE FAILING ROUTINE.

TWO WAYS OF LOCATING A FAILURE ARE AS FOLLOWS-

- A. DETERMINE WHAT FAILURE CAUSED THE ERROR WAIT. TO LOCATE THE FAILURE, IT IS RECOMMENDED THAT THE PROGRAM BE MANUALLY ENTERED AT THE START OF THE FAILING ROUTINE AND SINGLE INSTRUCTION, FOLLOWING THE LISTING TO DETERMINE THE EXACT FAILURE.
- B. USE AN OSCILLOSCOPE TO HELP LOCATE THE FAILURE. IF THE FAILURE IS IN THE COMMON-ERROR ROUTINE, SIMPLY TURN ON CONSOLE ENTRY SWITCH 8 AND DEPRESS START PUSHBUTTON TO LOOP ON THE INSTRUCTION BEING TESTED. IF THE FAILURE IS IN THE FIRST PART OF THE PROGRAM (BEFORE THE COMMON ERROR ROUTINE INSTRUCTIONS HAVE BEEN CHECKED OUT), A BRANCH (MOX) TO THE BEGINNING OF THE ROUTINE MAY BE MANUALLY INSERTED IN PLACE OF THE WAIT INSTRUCTION. THEN, THE ROUTINE MAY BE LOOPEO.

4. PRINTOUTS (NONE)

5. COMMENTS

THE 1800 P-C FUNCTION TEST STARTS WITH VERY SIMPLE INSTRUCTIONS AND DETERMINES IF EACH INSTRUCTION PERFORMS TO SPECIFICATIONS. EACH SUCCESSIVE ROUTINE ATTEMPTS TO UTILIZE ONLY AN INSTRUCTION THAT HAS NOT BEEN PREVIOUSLY TESTED. THE PROGRAM OPTIONS PROVIDE A MEANS FOR CONTINUOUSLY LOOPING THE ENTIRE PROGRAM AND ALSO ALLOW FAILING ROUTINES TO BE LOOPEO.

AN ATTEMPT IS MADE DURING THE EARLY STAGES OF THE PROGRAM TO DEVELOP THOSE INSTRUCTIONS WHICH ALLOW THE USAGE OF THE COMMON CONTROL (F00E AND F005) AND ERROR (F000) ROUTINES. AFTER THESE INSTRUCTIONS HAVE BEEN TESTED THE USER THEN HAS THE ABILITY TO REQUEST VARIOUS CONTROL OPTIONS BY MEANS OF THE DATA ENTRY SWITCHES.

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5.1 OPERATING MODES

THE NORMAL MODE OF OPERATION IS WITH THE DATA ENTRY SWITCHES SET TO /0000. THIS CAUSES A SINGLE PASS THROUGH THE PROGRAM WITH AN ERROR WAIT OCCURRING IF AN ERROR IS DETECTED.

IF AN ERROR IS DETECTED AND THE COMMON ERROR WAIT OCCURS, THE USER SHOULD TURN ON THE "LOOP ON ROUTINE" (DATA ENTRY SWITCHES SET TO /0020) AND SINGLE INSTRUCTION THROUGH THE FAILING ROUTINE TO ISOLATE THE FAILING INSTRUCTION.

IF THE FAILING ROUTINE DOES NOT FAIL WHEN EXECUTED IN SINGLE INSTRUCTION MODE, THE USER CAN TURN ON THE "BYPASS ERROR WAIT" SWITCH AND THE "LOOP ROUTINE" SWITCH (DATA ENTRY SWITCHES SET TO /0022) AND PROCEED TO USE SCOPING TECHNIQUES TO ISOLATE THE FAILURE.

5.2 PROGRAM LABELS

LABELS OCCURRING IN THE PROGRAM LISTING CAN BE QUICKLY IDENTIFIED AS FOLLOWS-

- A. LABELS STARTING WITH A OR B INDICATE THE BEGINNING OF A TEST ROUTINE.
- B. LABELS STARTING WITH G, H, J, OR K INDICATE COMMUNICATION LABELS WITH A ROUTINE.
- C. LABELS STARTING WITH V OR X ARE RESERVED FOR WAITS.
- D. LABELS STARTING WITH N, R, OR S INDICATE A CONSTANT OR WORK AREA.
- E. LABELS STARTING WITH F, W, Z OR U ARE USED IN COMMON OR SPECIAL ROUTINES THAT ARE NOT A REGULAR TEST ROUTINE.

6. APPENDIX (NONE)

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PROCESSOR-CONTROLLER FUNCTION TEST

```
028C      ABS      88400030
          ORG      /3000      88400040
          *****      88400050
          * THIS ENGINEERING CHANGE REFLECTS MINOR BUT      88400060
          * SIGNIFICANT MODIFICATIONS TO THE PROGRAM AND      88400070
          * PROGRAM DESCRIPTION.      88400080
          *      88400090
          * THE CHANGES ARE--      88400100
          *      88400110
          * 1. THE PROGRAM IDENTIFICATION HAS BEEN ADDED      88400120
          * JUST AFTER THE ORG 300 INSTRUCTION TO ENABLE      88400130
          * THIS PROGRAM TO BE CALLED OFF THE DISK.      88400140
          *      88400150
          * 2. WAIT /3000 AT LABEL X000 HAS BEEN INSERTED      88400160
          * AT THE FRONT OF THE PROGRAM SO THAT THE      88400170
          * INITIAL BIT SWITCH SETTINGS CAN BE MADE.      88400180
          *      88400190
          *      88400200
          *      88400210
          *****      88400220
          *      88400230
          *      88400240
          *      88400250
          *      88400260
          *      88400270
          *      88400280
          *      88400290
          *      88400300
          *      88400310
          *      88400320
          *      88400330
          *      88400340
          *      88400350
          *      88400360
          *      88400370
          *      88400380
          *      88400390
          *      88400400
          *      88400410
          *      88400420
          *      88400430
          *      88400440
          *      88400450
          *      88400460
          *      88400470
          *      88400480
          *      88400490
          *      88400500
          *      88400510
          *      88400520
          *      88400530
          *      88400540
          *****      88400550
          *      88400560
          *      88400570
          *      88400580
          *      88400590
          *      88400600
          *      88400610
          *      88400620
          *      88400630
          *      88400640
          *      88400650
          *      88400660
          *      88400670
          *      88400680
          *      88400690
          *      88400700

          OPERATING INSTRUCTIONS
          BIT SWITCH SETTINGS
          BIT 14 ON BYPASS ERROR WAIT
          BIT 12 OR 8 MUST BE ON TO
          MAKE BIT 14 EFFECTIVE.
          BIT 13 NOT USED
          BIT 12 LOCK ON ERROR
          BIT 11 ON LOOP PROGRAM
          BIT 10 ON LOOP ROUTINE
          BIT 9 NOT USED
          BIT 8 LOOP ON INSTRUCTION BEING
          TESTED
          BIT 7 ON BYPASS MPL/DIV TEST
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
3002 0 02C8      OC      X003+1      SET SWITCHES FOR OPTIONS      88400710
                                     AND PRESS START      88400720
3003 0 02FD      OC      X007+1      PROGRAM COMPLETED      88400730
                                     *****      88400740
                                     *****      88400750
                                     * ERROR IDENTIFICATION LISTING      88400760
                                     *      88400770
                                     *      88400780
                                     *      88400790
                                     *      88400800
                                     *      88400810
                                     *      88400820
          *****      88400830
          ADDRESS      *      88400840
          OF      *      88400850
          B-REG ROUTINE * A REG Q REG XR-1 XR-2 XR-3 STATUS      88400860
          *****      88400870
          3004 0 012E      DC      A080      MOX      88400880
          * SHORT FORM MOX FAILED TO MODIFY I CTR +1      88400890
          *      88400900
          *      88400910
          3005 0 012E      DC      A080      MOX      MOD+0      88400920
          3006 0 012E      OC      A080      +1      88400930
          * SHORT FORM MOX-SHOULD HAVE MODIFIED I CTR      88400940
          * +2 BUT MODIFIED BY 0 OR +1      88400950
          *      88400960
          *      88400970
          3007 0 012E      DC      A080      MDX      MOD+0      88400980
          3008 0 012E      OC      A080      +1      88400990
          3009 0 012E      DC      A080      +2      88401000
          300A 0 012E      OC      A080      +3      88401010
          * SHORT FORM MDX SHOULD HAVE MODIFIED I CTR      88401020
          * +4 BUT MODIFIED BY 0, +1, +2 OR +3      88401030
          *      88401040
          *      88401050
          300B 0 012E      DC      A080      MDX      88401060
          * MOX SHORT FORM FAILED TO MODIFY I CTR      88401070
          *      88401080
          *      88401090
          300C 0 012E      OC      A080      MDX      MOD+0      88401100
          300D 0 012E      OC      A080      +1      88401110
          300E 0 012E      OC      A080      +2      88401120
          * MOX SHORT FORM-SHOULD HAVE MODIFIED I CTR      88401130
          * -2, 010 MODIFY BY 0, +1 OR +2      88401140
          *      88401150
          *      88401160
          300F 0 013F      DC      A0C0      BSC,C      88401170
          * N/A N/A N/A N/A N/A C+0      88401180
          * BSC SKIPPED-SHOULD NOT HAVE      88401190
          *      88401200
          *      88401210
          3010 0 013F      OC      A0C0      BSC,0      88401220
          3011 0 013F      DC      A0C0      88401230
          * N/A N/A N/A N/A N/A C+0 AFTER LDS      88401240
          * N/A N/A N/A N/A N/A C AFTER 1ST BSC      88401250
          * FIRST BSC SKIPPED-SHOULD NOT HAVE      88401260
          * SECOND BSC FAILED TO SKIP-INDICATING 1ST BSC      88401270
          * FAILED TO TURN OFF OVERFLOW      88401280
          *      88401290
          *      88401300
          3012 0 013F      DC      A0C0      BSC,C      88401310
          * N/A N/A N/A N/A N/A OFF      88401320
          * BSC 010 NOT SKIP WITH OVERFLOW OFF      88401330
          *      88401340
          *      88401350
          3013 0 014C      OC      A100      LD      88401360
          * 0000 N/A N/A N/A N/A N/A      88401370
          * ACCUM NOT EQUAL TO 0000      88401380
```


PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS
OF
B-REG ROUTINE
*****
3014 0 014C
      OC      A100      LO
      * 0000 N/A N/A N/A N/A 1ST LD
      * 0000 N/A N/A N/A N/A 2ND LD
      * A LOAD 0000 FOLLOWED BY LOAD 0000 DID NOT
      * LEAVE ACCUM EQUAL TO 0000
      *
3015 0 014C
      DC      A100      BSC,E
      * 0000 N/A N/A N/A N/A
      * BSC FAILED TO SKIP
      *
3016 0 0154
      DC      A140      LO
      * 0000 N/A N/A N/A N/A 1ST VALVE
      * FFFF N/A N/A N/A N/A AFTER LD
      * LOAD FFFF ON TOP OF 0000 DID NOT LEAVE ACC
      * NEGATIVE
      *
3017 0 0154
      DC      A140      BSC,+
      * FFFF N/A N/A N/A N/A
      *
3018 0 0154
      DC      A140      BSC,E
      * FFFF N/A N/A N/A N/A
      * BSC SKIPPED SHOULD NOT HAVE
      *
3019 0 0154      OC      A140      ACCUM NOT EQUAL 7FFF
301A 0 0154      DC      A140      ACCUM NOT EQUAL 3FFF
301B 0 0154      DC      A140      ACCUM NOT EQUAL 1FFF
301C 0 0154      OC      A140      ACCUM NOT EQUAL 0FFF
301D 0 0154      OC      A140      ACCUM NOT EQUAL 07FF
301E 0 0154      OC      A140      ACCUM NOT EQUAL 03FF
301F 0 0154      OC      A140      ACCUM NOT EQUAL 01FF
3020 0 0154      OC      A140      ACCUM NOT EQUAL 00FF
3021 0 0154      DC      A140      ACCUM NOT EQUAL 007F
3022 0 0154      DC      A140      ACCUM NOT EQUAL 003F
3023 0 0154      DC      A140      ACCUM NOT EQUAL 001F
3024 0 0154      OC      A140      ACCUM NOT EQUAL 000F
3025 0 0154      OC      A140      ACCUM NOT EQUAL 0007
3026 0 0154      OC      A140      ACCUM NOT EQUAL 0003
3027 0 0154      OC      A140      ACCUM NOT EQUAL 0001
3028 0 0154      DC      A140      ACCUM NOT EQUAL 0000
3029 0 0154      OC      A140      ACCUM NOT EQUAL 0000
      * FFFF N/A N/A N/A N/A LOADED
      * 0000 N/A N/A N/A N/A AFTER SRA'S
      * THE ABOVE WAITS OCCUR AS A RESULT OF A
      * FAILURE ON A ROUTINE THAT LOADS FFFF ON
      * 0000 AND CHECKS USING SRA 1 AND BSC E.
      *
302A 0 01A0      DC      A180      ACCUM NOT EQUAL FFFF
302B 0 01A0      OC      A180      ACCUM NOT EQUAL FFFF
302C 0 01A0      OC      A180      ACCUM NOT EQUAL 7FFF
302D 0 01A0      DC      A180      ACCUM NOT EQUAL 3FFF
302E 0 0154      OC      A140      ACCUM NOT EQUAL 1FFF
302F 0 01A0      DC      A180      ACCUM NOT EQUAL 0FFF
3030 0 01A0      OC      A180      ACCUM NOT EQUAL 07FF
3031 0 01A0      OC      A180      ACCUM NOT EQUAL 03FF
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS
OF
B-REG ROUTINE
*****
3032 0 01A0      OC      A180      ACCUM NOT EQUAL 01FF
3033 0 01A0      DC      A180      ACCUM NOT EQUAL 00FF
3034 0 01A0      OC      A180      ACCUM NOT EQUAL 007F
3035 0 01A0      DC      A180      ACCUM NOT EQUAL 003F
3036 0 01A0      DC      A180      ACCUM NOT EQUAL 001F
3037 0 01A0      OC      A180      ACCUM NOT EQUAL 000F
3038 0 01A0      DC      A180      ACCUM NOT EQUAL 0007
3039 0 01A0      OC      A180      ACCUM NOT EQUAL 0003
303A 0 01A0      DC      A180      ACCUM NOT EQUAL 0001
303B 0 01A0      DC      A180      ACCUM NOT EQUAL 0000
303C 0 01A0      DC      A180      ACCUM NOT EQUAL 0000
      * FFFF N/A N/A N/A N/A LOADED
      * 0000 N/A N/A N/A N/A AFTER SRA'S
      * THE ABOVE WAITS OCCUR AS A RESULT OF A
      * FAILURE ON A ROUTINE THAT LOADS FFFF ON
      * FFFF AND CHECKS USING SRA 1 AND BSC E.
      *
303D 0 01EB      OC      A1C0      LD 0000 ON 0000
      * 0000 N/A N/A N/A N/A
      * ACCUM NOT EQUAL 0000
      *
303E 0 01EB      DC      A1C0      LD FFFF ON 0000
      * 0000 N/A N/A N/A N/A BEFORE LD
      * FFFF N/A N/A N/A N/A AFTER LD
      * ACCUM NOT EQUAL FFFF
      *
303F 0 01F5      OC      A100      LD
      * 0000 N/A N/A N/A N/A
      * ACCUM NOT EQUAL 0000
      *
3040 0 01F5      DC      A100      EOR
      * 0000 N/A N/A N/A N/A
      * 0000 N/A N/A N/A N/A
      * WITH ACCUM EQUAL 0000 AN EOR USING 0000 010
      * NOT RESULT IN ACCUM EQUAL 0000
      *
3041 0 01F5      OC      A100      EOR
      * FFFF N/A N/A N/A N/A LOADED + EOR
      * 0000 N/A N/A N/A N/A SHOULD BE
      * WITH ACCUM EQUAL FFFF AN EOR USING FFFF 010
      * NOT RESULT IN ACCUM EQUAL 0000
      *
3042 0 01F5      DC      A100      EOR
3043 0 01F5      OC      A100
      * 0000 N/A N/A N/A N/A BEFORE
      * FFFF N/A N/A N/A N/A S/B AFTER
      * WITH ACCUM EQUAL 0000 AN EOR USING FFFF 010
      * NOT RESULT IN ACCUM EQUAL FFFF
      *
3044 0 01F5      OC      A100      EOR
      * FFFF N/A N/A N/A N/A BEFORE EOR
      * FFFF N/A N/A N/A N/A S/B AFTER
      * WITH ACCUM EQUAL FFFF AN EOR USING 0000 010
      * NOT RESULT IN ACCUM EQUAL FFFF
      *
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3045 0 01F5 DC A100 SRA + EOR
* 7FFF N/A N/A N/A N/A N/A S/B AFTER SRA
* 0000 N/A N/A N/A N/A N/A S/B AFTER EOR
* WITH ACCM EQUAL 7FFF AN EOR USING 7FFF D10 NOT
* RESULT IN ACCM EQUAL TO 0000
* RESULT IN ACCM EQUAL TO 0000
*
3046 0 0214 DC A1E0 LO LONG FORM
* 0000 N/A N/A N/A N/A N/A S/B AFTER LO
* ACCM NOT EQUAL 0000-INDICATING WRONG
* LOCATION WAS LOA0E0
*
3047 0 0214 DC A1E0 LO LONG FORM
* C,NIEU. N/A N/A N/A N/A N/A S/B AFTER LO
* 0000 N/A N/A N/A N/A N/A S/B AFTER EOR
* ACCM NET EQUAL 0000 INOICATING WRONG LOCATION
* WAS LOA0E0
*
3048 0 0220 DC A1F0 LD INO
3049 0 0220 OC A1F0 LD IND
* 0000 N/A N/A N/A N/A N/A S/B FOR BSC
* ACCM NOT EQUAL 0000 INOICATING WRONG
* LOCATION WAS LOA0E0
*
304A 0 0220 OC A200 BSC LONG FORM
* UNCONDITIONAL BSC DID NOT BRANCH
*
304B 0 0220 OC A200 BSC LONG FORM
* UNCONDITIONAL BSC SKIPPED-SHOULD BRANCH
*
304C 0 0220 OC A200 BSC,E LONG FORM
304D 0 0220 OC A200
* FFFF N/A N/A N/A N/A N/A
* BSC FELL THRU OR SKIPPED-SHOULD BRANCH
* 010 NOT SKIP OR SKIPPED - SHOULD BR.
*
304E 0 0220 OC A200 BSC,+ LONG FORM
304F 0 0220 OC A200
* FFFF N/A N/A N/A N/A N/A S/B AT TEST
* 010 NOT SKIP OR SKIPPED - SHOULD BR.
*
3050 0 0220 OC A200 BSC,Z LONG FORM
3051 0 0220 OC A200
* FFFF N/A N/A N/A N/A N/A S/B AT TEST
* BSC DID NOT SKIP OR SKIPPED - SHOULD BR.
*
3052 0 0220 DC A200 BSC,- LONG FORM
* FFFF N/A N/A N/A N/A N/A S/B AT TEST
* BSC BRANCHED-SHOULD NOT
*
3053 0 0220 DC A200 BSC,C LONG FORM
3054 0 0220 OC A200
* N/A N/A N/A N/A N/A C+D S/B AT TEST
* BSC 010 NOT SKIF OR SKIPPED-SHOULD BRANCH
*
```

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```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3055 0 022D DC A200 BSC,D LONG FORM
3056 0 0220 OC A200
* N/A N/A N/A N/A N/A C+D S/B AT TEST
* BSC 010 NOT SKIP OR SKIPPED-SHOULD BRANCH
*
3057 0 0220 OC A200 BSC,D LONG FORM
* N/A N/A N/A N/A N/A C S/B AT TEST
* BSC FAILED TO TURN OFF OVERFLOW
*
3058 0 0220 OC A200 BSC,C LONG FORM
* N/A N/A N/A N/A N/A OFF S/B AT TEST
* BSC BRANCHED-SHOULD NOT
*
3059 0 0220 OC A200 BSC,D LONG FORM
* N/A N/A N/A N/A N/A OFF S/B AT TEST
* BSC BRANCHED-SHOULD NOT
*
305A 0 0220 OC A200 BSC,+ LONG FORM
305B 0 0220 DC A200
* 0000 N/A N/A N/A N/A N/A
* BSC 010 NOT SKIP OR SKIPPED-SHOULD BRANCH
*
305C 0 0220 DC A200 BSC,+ LONG FORM
* FFFF N/A N/A N/A N/A N/A S/B AT TEST
* BSC BRANCHED-SHOULD NOT
*
305D 0 0220 DC A200 BSC,+ LONG FORM
* 0001 N/A N/A N/A N/A N/A S/B AT TEST
* BSC BRANCHED SHOULD NOT
*
305E 0 0220 OC A200 BSC INOIRECT
305F 0 0220 OC A200
* BSC 010 NOT SKIP OR SKIPPED-SHOULD BRANCH
*
3060 0 0270 DC A240 BSI
* UNCONDITIONAL BSI 010 NOT BRANCH
*
3061 0 0270 OC A240 BSI
* UNCONDITIONAL BSI 010 NOT STORE I CTR
* CORRECTLY
*
3062 0 0270 OC A240 BSI,+ LONG FORM
3063 0 0270 OC A240
* 0000 N/A N/A N/A N/A N/A S/B AT TEST
* BSI 010 NOT SKIP OR SKIPPED-SHOULD BRANCH
*
3064 0 0270 OC A240 BSI,+ LONG FORM
* BSI 010 NOT STJRE THE I CTR CORRECTLY
*
3065 0 0282 DC A900 STORE
* STORE INSTRUCTION FAILED
```

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ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS

3066 0 0282 OC A900 XIO SENSE/PROG SWS
* FFOO N/A N/A N/A N/A S/B AT TEST
* ACCUM NOT EQUAL TO FFOO-- SENSE/PROG SWS
* WERE INCORRECTLY SENSED
*
3067 0 0282 DC A900 XIO DATA ENTRY SWS
* FFOO N/A N/A N/A N/A S/B AT TEST
* ACCUM NOT EQUAL TO FFFF-- DATA ENTRY SWS
* WERE INCORRECTLY READ
*
3068 0 0282 DC A900 XIO SENSE/PROG SWS
* FFOO N/A N/A N/A N/A S/B AT TEST
* ACCUM NOT EQUAL TO 0000-- SENSE/PROG SWS
* WERE INCORRECTLY SENSED
*
3069 0 0282 OC A900 XIO
* 0000 N/A N/A N/A N/A NT/8 AT TEST
* ACCUM NOT EQUAL TO 0000--DATA ENTRY SWS
* WERE INCORRECTLY READ
*

* THE FOLLOWING ERRORS ARE HANDLED BY THE
* COMMON ERROR CONTROL ROUTINE. THE IO NUMBER
* SHOWN FOR EACH ERROR WILL APPEAR IN BITS
* 5 THRU 15 OF THE WAIT INSTRUCTION.

306A 0 0209 OC A280 SRA 16
* FFFF N/A N/A N/A N/A S/B AFTER LO
* 0000 N/A N/A N/A N/A S/B AFTER SRA
* ACCUM NOT ZERO
*
306B 0 02E3 OC A281 SRA 15
* 8000 N/A N/A N/A N/A S/B AFTER LD
* 0001 N/A N/A N/A N/A S/B AFTER SRA
* ACCUM NOT EQUAL 0001
*
306C 0 02EE OC A282 SRA 1
* AAAA N/A N/A N/A N/A S/B AFTER LD
* 5555 N/A N/A N/A N/A S/B AFTER SRA
* ACCUM NOT EQUAL 5555
*
306D 0 02F9 OC A283 SRA 1
* 5555 N/A N/A N/A N/A S/B AFTER LD
* 2AAA N/A N/A N/A N/A S/B AFTER SRA
* ACCUM NOT EQUAL 2AAA
*
306E 0 0304 OC A284 SERIES OF SRAS-15
* TOTAL SHIFTS
* 8000 N/A N/A N/A N/A S/B AFTER LD
* 0001 N/A N/A N/A N/A S/B AFTER SRA
* ACCUM NOT EQUAL 0001

PROCESSOR-CONTROLLER FUNCTION TEST

ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS

306F 0 0319 OC A2C0 ANO-MEMORY=0000
* 0000 N/A N/A N/A N/A S/B AFTER LO
* 0000 N/A N/A N/A N/A AFTER ANO
* ACCUM NOT EQUAL 0000
*
3070 0 0323 OC A2C4 ANO-MEMORY=FFFF
* 0000 N/A N/A N/A N/A
* 0000 N/A N/A N/A N/A
* ACCUM NOT EQUAL 0000
*
3071 0 0320 OC A2C8 ANO-MEMORY=0000
* FFFF N/A N/A N/A N/A
* 0000 N/A N/A N/A N/A
* ACCUM NOT EQUAL 0000
*
3072 0 0337 OC A2CC ANO-MEMORY=FFFF
* FFFF N/A N/A N/A N/A
* FFFF N/A N/A N/A N/A
* ACCUM NOT EQUAL FFFF
*
3073 0 0345 OC A300 OR-MEMORY = 0000
* 0000 N/A N/A N/A N/A S/B AFTER LO+OR
* 0000 N/A N/A N/A N/A AFTER EOR
* ACCUM NOT EQUAL 0000
*
3074 0 034F OC A302 OR-MEMORY=FFFF
* 0000 N/A N/A N/A N/A S/B AFTER LO + OR
* FFFF N/A N/A N/A N/A AFTER EOR
* ACCUM NOT EQUAL FFFF
*
3075 0 035A OC A304 OR-MEMORY=FFFF
* FFFF N/A N/A N/A N/A S/B AFTER LO+OR
* FFFF N/A N/A N/A N/A AFTER EOR
* ACCUM NOT EQUAL FFFF
*
3076 0 0368 OC A340 RTE 16
* FFFF 0000 N/A N/A N/A N/A BEFORE RTE
* 0000 FFFF N/A N/A N/A N/A AFTER RTE
* ACCUM NOT EQUAL 0000
*
3077 0 0368 OC A340 RTE 16
* 0000 FFFF N/A N/A N/A N/A BEFORE RTE
* FFFF 0000 N/A N/A N/A N/A AFTER RTE
* ACCUM NOT EQUAL FFFF
*
3078 0 0381 OC A380 SRT 32
* 8000 N/A N/A N/A N/A N/A BEFORE SRT
* FFFF FFFF N/A N/A N/A N/A AFTER SRT
* ACCUM NOT EQUAL FFFF
*



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*****
ADDRESS * 88405470
OF * 88405480
8-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88405490
***** 88405500
88405510
3079 0 0381 * DC A380 SRT 32 + RTE 16 88405520
* 8000 N/A N/A N/A N/A N/A BEFORE SRT 88405530
* FFFF FFFF N/A N/A N/A N/A AFTER SRT+RTE 88405540
* ACCUM NOT EQUAL FFFF-INDICATING Q REG FAILED 88405550
88405560
88405570
88405580
307A 0 0396 * DC A384 SRT 32 88405590
* 4000 N/A N/A N/A N/A N/A AFTER LD 88405600
* 0000 0000 N/A N/A N/A N/A AFTER SRT 88405610
* ACCUM NOT EQUAL 0000 88405620
88405630
88405640
3078 0 0396 * DC A384 SRT 32 + RTE 16 88405650
* 4000 N/A N/A N/A N/A N/A AFTER LD 88405660
* 0000 0000 N/A N/A N/A N/A AFTER SRT 88405670
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88405680
88405690
88405700
307C 0 03A9 * DC A388 SRT 15 88405710
* 5555 N/A N/A N/A N/A N/A AFTER LO 88405720
* 0000 AAAA N/A N/A N/A N/A AFTER SRT 88405730
* ACCUM NOT EQUAL 0000 88405740
88405750
307D 0 03A9 * DC A388 SRT 15 + RTE 16 88405760
* 5555 N/A N/A N/A N/A N/A AFTER LO 88405770
* 0000 AAAA N/A N/A N/A N/A AFTER SRT 15 88405780
* AAAA 0000 N/A N/A N/A N/A AFTER RTE 16 88405790
* ACCUM NOT EQUAL AAAA-INDICATING Q REG FAILED 88405800
88405810
88405820
307E 0 0380 * OC A38C SERIES OF SRTS-30 88405830
* *TOTAL SHIFTS 88405840
* 5555 N/A N/A N/A N/A N/A AFTER LO 88405850
* 0000 0001 N/A N/A N/A N/A AFTER SRT'S 88405860
* ACCUM NOT EQUAL 0000 88405870
88405880
88405890
307F 0 0380 * DC A38C SERIES OF SRTS-30 88405900
* *TOTAL SHIFTS + 88405910
* *RTE 16 88405920
* 5555 N/A N/A N/A N/A N/A AFTER LO 88405930
* 0000 0001 N/A N/A N/A N/A AFTER SRT'S 88405940
* 0001 0000 N/A N/A N/A N/A AFTER RTE 16 88405950
* ACCUM NOT EQUAL 0001-INDICATING Q REG FAILED 88405960
88405970
88405980
88405990
3080 0 03DD * OC A3C0 RTE 15 88406000
* 5555 AAAA N/A N/A N/A N/A AFTER LO'S 88406010
* 5554 AAAB N/A N/A N/A N/A AFTER RTE 15 88406020
* ACCUM NOT EQUAL 5554 - RTE 15 Q TO A FAILED 88406030
88406040
88406050
3081 0 03DD * OC A3C0 RTE 15 + RTE 16 88406060
* 5555 AAAA N/A N/A N/A N/A AFTER LO'S 88406070
* 5554 AAAB N/A N/A N/A N/A AFTER RTE 15 88406080
* AAAB 5554 N/A N/A N/A N/A AFTER RTE 16 88406090
* ACCUM NOT EQUAL AAAB-INDICATING Q REG FAILED 88406100
88406110
88406120
88406130
88406140
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```
*****
ADDRESS * 88406150
OF * 88406160
8-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88406170
***** 88406180
88406190
3082 0 03F4 * OC A3C4 SERIES OF RTE-31 88406200
* *TOTAL SHIFTS 88406210
* 0000 8000 N/A N/A N/A N/A AFTER LO 88406220
* 0001 0000 N/A N/A N/A N/A AFTER RTE'S 88406230
* ACCUM NOT EQUAL 0001 88406240
88406250
88406260
3083 0 03F4 * DC A3C4 SERIES OF RTE-31 88406270
* *TOTAL SHIFTS 88406280
* *FOLLOWED BY RTE 16 88406290
* 0000 8000 N/A N/A N/A N/A AFTER LD 88406300
* 0001 0000 N/A N/A N/A N/A AFTER RTE'S 88406310
* 0000 0001 N/A N/A N/A N/A AFTER RTE 16 88406320
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88406330
88406340
88406350
3084 0 0419 * OC A400 SLA 16 88406360
* FFFF FFFF N/A N/A N/A N/A AFTER LD 88406370
* 0000 FFFF N/A N/A N/A N/A AFTER SLA 88406380
* ACCUM NOT EQUAL 0000 88406390
88406400
3085 0 0419 * DC A400 SLA 16 88406410
* FFFF FFFF N/A N/A N/A N/A OFF AFTER LO 88406420
* 0000 FFFF N/A N/A N/A N/A C AFTER SLA 88406430
* CARRY NOT SET 88406440
88406450
88406460
3086 0 0419 * OC A400 SLA 16 + RTE 16 88406470
* FFFF FFFF N/A N/A N/A N/A AFTER LD 88406480
* 0000 FFFF N/A N/A N/A N/A AFTER SLA 88406490
* FFFF 0000 N/A N/A N/A N/A AFTER RTE 16 88406500
* ACCUM NOT EQUAL FFFF-INDICATING Q REG FAILED 88406510
88406520
88406530
88406540
3087 0 043A * OC A408 SLA 16 88406550
* 0001 0000 N/A N/A N/A N/A AFTER LD 88406560
* 0000 0000 N/A N/A N/A N/A AFTER SLA 88406570
* ACCUM NOT EQUAL 0000 88406580
88406590
88406600
3088 0 043A * OC A408 SLA 16 88406610
* 0001 0000 N/A N/A N/A N/A C AFTER LO 88406620
* 0000 0000 N/A N/A N/A N/A C AFTER SLA 88406630
* CARRY NOT SET 88406640
88406650
88406660
3089 0 043A * DC A408 SLA 16 + RTE 16 88406670
* 0001 0000 N/A N/A N/A N/A AFTER LO 88406680
* 0000 0000 N/A N/A N/A N/A AFTER SLA 88406690
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16 88406700
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88406710
88406720
88406730
308A 0 045A * OC 8400 SLA 1 88406740
* AAAA 0000 N/A N/A N/A N/A AFTER LO 88406750
* 5554 0000 N/A N/A N/A N/A AFTER SLA 88406760
* ACCUM NOT EQUAL 5554 88406770
88406780
88406790
88406800
88406810
88406820
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PROCESSOR-CONTROLLER FUNCTION TEST

ADDRESS * 88406830
OF * 88406840
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88406850
***** 88406860
308B 0 045A OC B400 SLA 1 88406870
* AAAA 0000 N/A N/A N/A C 88406880
* 5554 0000 N/A N/A N/A C 88406890
* CARRY NOT SET 88406900
* 88406910
* 88406920
* 88406930
308C 0 045A OC B400 SLA 1 + RTE 16 88406940
* AAAA 0000 N/A N/A N/A N/A 88406950
* 5554 0000 N/A N/A N/A N/A 88406960
* 0000 5554 N/A N/A N/A N/A AFTER RTE 88406970
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88406980
* 88406990
* 88407000
308D 0 0478 OC B406 SLA 1 88407010
* 5555 0000 N/A N/A N/A N/A AFTER LD 88407020
* AAAA 0000 N/A N/A N/A N/A AFTER SLA 88407030
* ACCUM NOT EQUAL AAAA 88407040
* 88407050
* 88407060
308E 0 0478 DC B406 SLA 1 88407070
* 5555 0000 N/A N/A N/A C AFTER LD 88407080
* AAAA 0000 N/A N/A N/A OFF AFTER SLA 88407090
* CARRY SET-SHOLD BE CLEAR 88407100
* 88407110
* 88407120
308F 0 0478 OC B406 SLA 1 + RTE 16 88407130
* 5555 0000 N/A N/A N/A N/A AFTER LD 88407140
* AAAA 0000 N/A N/A N/A N/A AFTER SLA 88407150
* 0000 AAAA N/A N/A N/A N/A AFTER RTE 88407160
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88407170
* 88407180
* 88407190
3090 0 0497 OC B40A SERIES OF SLAS-16 88407200
* *TOTAL SHIFTS 88407210
* 0001 0000 N/A N/A N/A N/A AFTER SLA 0 88407220
* 0000 0000 N/A N/A N/A N/A AFTER SLA'S 88407230
* ACCUM NOT EQUAL 0000 88407240
* 88407250
* 88407260
3091 0 0497 DC B40A SERIES OF SLAS-16 88407270
* *TOTAL SHIFTS 88407280
* 0001 0000 N/A N/A N/A C AFTER SLA 0 88407290
* 0000 0000 N/A N/A N/A C AFTER SLA'S 88407300
* CARRY NOT SET 88407310
* 88407320
* 88407330
3092 0 0497 DC B40A SERIES OF SLAS-16 88407340
* *TOTAL SHIFTS + 88407350
* *RTE 16 88407360
* 0001 0000 N/A N/A N/A N/A AFTER SLA 0 88407370
* 0000 0000 N/A N/A N/A N/A AFTER SLA'S 88407380
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16 88407390
* ACC NOT EQUAL 0000-INDICATING Q REG FAILED 88407400
* 88407410
* 88407420
3093 0 04C4 DC A440 SLT 32 88407430
* 0000 0001 N/A N/A N/A N/A AFTER LD 88407440
* 0000 0000 N/A N/A N/A N/A AFTER SLT 32 88407450
* ACCUM NOT EQUAL 0000 88407460
* 88407470
* 88407480
* 88407490
* 88407500

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***** 88407510
ADDRESS * 88407520
OF * 88407530
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88407540
***** 88407550
3094 0 04C4 DC A440 SLT 32 88407560
* 0000 0001 N/A N/A N/A N/A AFTER LD 88407570
* 0000 0000 N/A N/A N/A C AFTER SLT 32 88407580
* CARRY NOT SET 88407590
* 88407600
* 88407610
3095 0 04C4 OC A440 SLT 32 + RTE 16 88407620
* 0000 0001 N/A N/A N/A N/A AFTER LD 88407630
* 0000 0000 N/A N/A N/A N/A AFTER SLT 32 88407640
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16 88407650
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88407660
* 88407670
* 88407680
3096 0 04E1 DC A444 SLT 16 88407690
* 0000 FFFF N/A N/A N/A N/A AFTER LD 88407700
* FFFF 0000 N/A N/A N/A N/A AFTER SLT 16 88407710
* ACCUM NOT EQUAL FFFF 88407720
* 88407730
* 88407740
3097 0 04E1 OC A444 SLT 16 88407750
* 0000 FFFF N/A N/A N/A N/A AFTER LD 88407760
* FFFF 0000 N/A N/A N/A N/A OFF AFTER SLT 16 88407770
* CARRY ON SHOULD NOT BE 88407780
* 88407790
* 88407800
3098 0 04E1 OC A444 SLT 16 + RTE 16 88407810
* 0000 FFFF N/A N/A N/A N/A AFTER LD 88407820
* FFFF 0000 N/A N/A N/A N/A AFTER SLT 16 88407830
* 0000 FFFF N/A N/A N/A N/A AFTER RTE 16 88407840
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88407850
* 88407860
* 88407870
3099 0 0500 OC A44A SLT 15 88407880
* 0000 5555 N/A N/A N/A N/A AFTER LD 88407890
* 2AAA 8000 N/A N/A N/A N/A AFTER SLT 15 88407900
* ACCUM NOT EQUAL 2AAA 88407910
* 88407920
* 88407930
309A 0 0500 OC A44A SLT 15 88407940
* 0000 5555 N/A N/A N/A N/A AFTER LD 88407950
* 2AAA 8000 N/A N/A N/A N/A OFF AFTER SLT 15 88407960
* CARRY SET-SHOULD NOT BE 88407970
* 88407980
* 88407990
309B 0 0500 OC A44A SLT 15 + RTE 16 88408000
* 0000 5555 N/A N/A N/A N/A AFTER LD 88408010
* 2AAA 8000 N/A N/A N/A N/A AFTER SLT 15 88408020
* 8000 2AAA N/A N/A N/A N/A AFTER RTE 16 88408030
* ACCUM NOT EQUAL 8000-INDICATING Q REG FAILED 88408040
* 88408050
* 88408060
309C 0 0520 OC B440 SERIES OF SLTS-32 88408070
* *TOTAL SHIFTS 88408080
* 0000 0001 N/A N/A N/A N/A AFTER LD 88408090
* 0000 0000 N/A N/A N/A N/A AFTER SLT'S 88408100
* ACCUM NOT EQUAL 0000 88408110
* 88408120
* 88408130
309D 0 0520 DC B440 SERIES OF SLTS-32 88408140
* *TOTAL SHIFTS 88408150
* 0000 0001 N/A N/A N/A N/A AFTER LD 88408160
* 0000 0000 N/A N/A N/A N/A C AFTER SLT'S 88408170
* CARRY NOT ON 88408180

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```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG C-REG XR-1 XR-2 XR-3 STATUS
*****
309E 0 0520 DC B440 SERIES OF SLTS-32
*
* TOTAL SHIFTS +
* RTE 16
* 0000 0001 N/A N/A N/A AFTER LD
* 0000 0000 N/A N/A N/A AFTER SLT'S
* 0000 0000 N/A N/A N/A AFTER RTE 16
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED
*
309F 0 0549 DC A480 STO
* 0000 N/A N/A N/A N/A
* STORING 0000 INTO A STORAGE LOCATION
* CONTAINING FFFF DID NOT RETURN 0000 WHEN
* RELOADED IN THE ACCUM
*
30A0 0 0555 DC A482 STO
* FFFF N/A N/A N/A N/A
* STORING FFFF INTO A STORAGE LOCATION
* CONTAINING 0000 DID NOT RETURN FFFF WHEN
* RELOADED IN THE ACCUM
*
30A1 0 0566 OC A4C0 STS
* N/A N/A N/A N/A OFF BEFORE STS
* 0000 N/A N/A N/A N/A OFF AFTER LD
* STS OF 0000 INTO STORAGE LOCATION
* CONTAINING 0003 DID NOT RETURN 0000 WHEN
* RELOADED IN THE ACCUM
*
30A2 0 0571 DC A4C2 STS
* N/A N/A N/A N/A C+O AFTER LDS
* N/A N/A N/A N/A OFF AFTER STS
* STS DID NOT CLEAR CARRY
*
30A3 0 0571 DC A4C2 STS CK ACC
* INITIALLY ACC HAS CORE LOCATION OF
* SYMBOLIC LABEL A4C2
* ACC DESTROYED AFTER STS
*
30A4 0 0571 DC A4C2 STS
* N/A N/A N/A N/A C+O AFTER LOS
* N/A N/A N/A N/A OFF AFTER STS
* STS DID NOT CLEAR OVERFLOW AFTER STS
*
30A5 0 0571 DC A4C2 STS
* N/A N/A N/A N/A BEFORE LO
* 0003 N/A N/A N/A N/A AFTER LD
* STS OF 0003 INTO A STORAGE LOCATION
* CONTAINING 0000 DID NOT RETURN 0003 WHEN
* RELOADED IN THE ACCUM
*
30A6 0 0596 DC A4CB STS
* N/A N/A N/A N/A C AFTER LDS
* 0002 N/A N/A N/A N/A OFF AFTER LD
* STS OF 0002 INTO A STORAGE LOCATION
* CONTAINING 0003 DID NOT RETURN 0002 WHEN
* RELOADED IN THE ACCUM
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS *
OF *
B-REC ROUTINE * A-REG C-REG XR-1 XR-2 XR-3 STATUS
*****
30A7 0 0596 DC A4C8 STS
* N/A N/A N/A N/A C AFTER LDS
* 0000 N/A N/A N/A OFF AFTER STS
* STS DID NOT CLEAR CARRY
*
30A8 0 05AC DC A4CC STS
* N/A N/A N/A N/A O AFTER LDS
* 0001 N/A N/A N/A OFF AFTER STS
* STS OF 0001 INTO A STORAGE LOCATION
* CONTAINING 0002 DID NOT RETURN 0001 WHEN
* RELOADED IN THE ACCUM
*
30A9 0 05AC DC A4CC STS
* N/A N/A N/A N/A O AFTER LDS
* 0000 N/A N/A N/A OFF AFTER LDA
* STS DID NOT CLEAR OVERFLOW
*
30AA 0 05C8 DC A500 BSC, O+EZC
* 8001 N/A N/A N/A C+O
* BSC SKIPPED-SHOULD NOT HAVE
*
30AB 0 05D3 DC A502 BSC, -DC+
* 0000 N/A N/A N/A C+O
* BSC SKIPPED-SHOULD NOT HAVE
*
30AC 0 05DE DC A504 BSC, O-E
* 8000 N/A N/A N/A C+O
* BSC FAILED TO SKIP
*
30AD 0 05DE DC A504 BSC, O
* 8000 N/A N/A N/A C
* BSC FAILED TO CLEAR OVERFLOW
*
30AE 0 05F5 DC A508 BSC, C+Z
* 0001 N/A N/A N/A OFF
* BSC FAILED TO SKIP
*
30AF 0 0600 DC A50A BSC, +OCE LONG FORM
* 8001 N/A N/A N/A C+O
* BSC DID NOT BRANCH - SHOULD HAVE
*
30B0 0 0600 DC A50A BSC, +OCE LONG FORM
* 8001 N/A N/A N/A C+O
* BSC SKIPPED-SHOULD BRANCH
*
30B1 0 061D DC A50C BSC, -Z LONG FORM
* 0004 N/A N/A N/A C+O
* BSC DID NOT BRANCH - SHOULD HAVE
*
30B2 0 061D DC A50C BSC, -Z LONG FORM
* 0004 N/A N/A N/A C+O
* BSC SKIPPED-SHOULD BRANCH
```


PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
***** / *****
30B3 0 0631 DC A50E BSC,+EOCZ LONG
*FORM
* 8001 N/A N/A N/A N/A C+D
* BSC BRANCHED-SHOULD NOT
30B4 0 0631 DC A50E BSC,+EOCZ LONG
*FORM
* 8001 N/A N/A N/A N/A C+D
* BSC SKIPPED-SHOULD NOT
30B5 0 0645 DC B500 BSC,+
* 0001 N/A N/A N/A N/A C+D
* BSC ON PLUS CLEARED THE OVERFLOW F-F
30B6 0 0645 DC B500 BSC,+
* 0001 N/A N/A N/A N/A N/A
* BSC FAILED TO SKIP
30B7 0 0663 DC A540 BSI,ECO+Z LONG
*FORM
* 8001 N/A N/A N/A N/A C+D
* BSI DIO NOT BRANCH - SHOULD HAVE
30B8 0 0663 DC A540 BSI,ECO+Z LONG
*FORM
* 8001 N/A N/A N/A N/A C+D
* BSI SKIPPED-SHOULD BRANCH
30B9 0 0663 DC A540 BSI,ECO+Z LONG
*FORM
* 8001 N/A N/A N/A N/A C+D AFTER LDS
* 8001 N/A N/A N/A N/A C AFTER BSI
* BSI DID NOT CLEAR OVERFLOW
30BA 0 06B7 DC A544 BSI,Z- LONG FORM
* 0002 N/A N/A N/A N/A N/A
* BSI DID NOT BRANCH - SHOULD HAVE
30BB 0 06B7 DC A544 BSI,Z- LONG FORM
* 0002 N/A N/A N/A N/A N/A
* BSI SKIPPED-SHOULD BRANCH
30BC 0 069C DC A546 BSI,Z LONG FORM
* 0000 N/A N/A N/A N/A N/A
* BSI BRANCHED-SHOULD NOT
30BD 0 069C DC A546 BSI,Z LONG FORM
* 0000 N/A N/A N/A N/A N/A
* BSI SKIPPED-SHOULD NOT
U
*****
88409550
88409560
88409570
88409580
88409590
88409600
88409610
88409620
88409630
88409640
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88409660
88409670
88409680
88409690
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88409940
88409950
88409960
88409970
88409980
88409990
88410000
88410010
88410020
88410030
88410040
88410050
88410060
88410070
88410080
88410090
88410100
88410110
88410120
88410130
88410140
88410150
88410160
88410170
88410180
88410190
88410200
88410210
88410220
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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
***** / *****
30BE 0 06AF DC A548 BSI,- LONG FORM
* 8001 N/A N/A N/A N/A N/A
* BSI SKIPPED-SHOULD NOT
30BF 0 06AF DC A548 BSI,- LONG FORM
* 8001 N/A N/A N/A N/A N/A
* BSI BRANCHED-SHOULD NOT
30C0 0 06C1 DC A54A BSI,+ LONG FORM
* 0002 N/A N/A N/A N/A N/A
* BSI SKIPPED-SHOULD NOT
30C1 0 06C1 DC A54A BSI,+ LONG FORM
* 0002 N/A N/A N/A N/A N/A
* BSI BRANCHED-SHOULD NOT
30C2 0 06D3 DC A54C BSI,E LONG FORM
* 0002 N/A N/A N/A N/A N/A
* BSI SKIPPED-SHOULD NOT
30C3 0 06D3 DC A54C BSI,E LONG FORM
* 0002 N/A N/A N/A N/A N/A
* BSI BRANCHED-SHOULD NOT
30C4 0 06E5 DC A54E BSI,C LONG FORM
* N/A N/A N/A N/A N/A C
* BSI SKIPPED-SHOULD NOT
30C5 0 06E5 DC A54E BSI,C LONG FORM
* N/A N/A N/A N/A N/A C
* BSI BRANCHED SHOULD NOT
30C6 0 06F7 DC A54F BSI,D LONG FORM
* N/A N/A N/A N/A N/A 0
* BSI SKIPPED-SHOULD NOT
30C7 0 06F7 DC A54F BSI,D LONG FORM
* N/A N/A N/A N/A N/A 0
* BSI BRANCHED-SHOULD NOT
30C8 0 070F DC A580 LDD
* 0000 0000 N/A N/A N/A N/A
* ACCUM NOT EQUAL 0000
30C9 0 070F DC A580 LDD + RTE 16
* 0000 0000 N/A N/A N/A N/A AFTER LDD
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED
30CA 0 0721 DC A584 LDD
* FFFF FFFF N/A N/A N/A N/A
* ACCUM NOT EQUAL FFFF
*****
88410230
88410240
88410250
88410260
88410270
88410280
88410290
88410300
88410310
88410320
88410330
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88410370
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88410800
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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
30CB 0 0721 DC A584 LDD + RTE 16
* FFFF FFFF N/A N/A N/A N/A AFTER LDD
* FFFF FFFF N/A N/A N/A N/A AFTER RTE 16
* ACCUM NOT EQUAL FFFF-INDICATING Q REG FAILED
*
30CC 0 0735 DC A588 LDD ODD ADDRESS
* 0000 0000 N/A N/A N/A N/A
* ACCUM NOT EQUAL 0000
*
30CD 0 0735 DC A588 LDD-ODD ADDRESS
* + RTE 16
* 0000 0000 N/A N/A N/A N/A AFTER LDD
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED
*
30CE 0 074C OC A5C0 STD
* 0000 0000 N/A N/A N/A N/A
* USING STD-ACCUM NOT STORED IN LOCATION EA
*
30CF 0 074C DC A5C0 STD
* 0000 0000 N/A N/A N/A N/A
* USING STD-Q REG NOT STORED IN LOCATION EA+1
*
30D0 0 0760 DC A5C4 STD
* FFFF FFFF N/A N/A N/A N/A
* USING STD-ACCUM NOT STORED IN LOCATION EA
*
30D1 0 0760 DC A5C4 STD
* FFFF FFFF N/A N/A N/A N/A
* USING STD-Q REG NOT STORED IN LOCATION EA+1
*
30D2 0 0779 DC A5C8 STD ODD ADDRESS
* 0000 0000 N/A N/A N/A N/A
* STD USING ODD ADDRESS-ACCUM NOT STORED IN EA
*
30D3 0 0779 DC A5C8 STD-ODD ADDRESS
* 0000 0000 N/A N/A N/A N/A
* STD USING ODD ADDRESS-ACCUM NOT STORED
  IN EA+1
*
30D4 0 079F DC A600 LDX 1
* N/A N/A N/A N/A N/A N/A
* TAG REG BIT 7 WILL NOT SET
*
30D5 0 07A8 OC A602 LDX 2
* N/A N/A N/A N/A N/A N/A
* TAG REG BIT 6 WILL NOT SET
*
30D6 0 07B1 DC A604 LDX 1
* N/A N/A 0000 N/A N/A N/A
* INDEX REG 1 NOT EQUAL 0000
*
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
30D7 0 078D DC A606 LDX 2
* N/A N/A N/A 0000 N/A N/A
* INDEX REG 2 NOT EQUAL 0000
*
30D8 0 07CA DC A608 LDX 3
* N/A N/A N/A N/A 0000 N/A
* INDEX REG 3 NOT EQUAL 0000
*
30D9 0 07D7 DC A60A LDX 1
* N/A N/A FFFF N/A N/A N/A
* INDEX REG 1 NOT EQUAL FFFF
*
30DA 0 07E4 DC A60C LDX 2
* N/A N/A N/A FFFF N/A N/A
* INDEX REG 2 NOT EQUAL FFFF
*
30DB 0 07F1 DC A60E LDX 3
* N/A N/A N/A N/A FFFF N/A
* INDEX REG 3 NOT EQUAL FFFF
*
30DC 0 07FE DC B600 LDX 1 LONG FORM
* N/A N/A 0001 N/A N/A N/A
* INDEX REG 1 NOT EQUAL 0001
*
30DD 0 080C DC B602 LDX 3 INDIRECT
* N/A N/A N/A N/A FFFF N/A
* INDEX REG 3 NOT EQUAL FFFF
*
30DE 0 0820 DC A640 STX
* N/A N/A N/A N/A N/A N/A
* STX WITH NO TAG DID NOT STORE I-CIR CORRECT
*
30DF 0 0837 DC A642 STX 1
* N/A N/A 0000 N/A N/A N/A
* INDEX REG 1 WAS NOT STORED BY STX
*
30E0 0 0844 DC A644 STX 2
* N/A N/A N/A 0000 N/A N/A
* INDEX REG 2 NOT STORED BY STX
*
30E1 0 0851 DC A646 STX 3
* N/A N/A N/A N/A 0000 N/A
* INDEX REG 3 NOT STORED BY STX
*
30E2 0 085E DC A648 STX 1
* N/A N/A FFFF N/A N/A N/A
* INDEX REG 1 NOT STORED BY STX
*
30E3 0 086C DC A64A STX 2
* N/A N/A N/A FFFF N/A N/A
* INDEX REG 2 NOT STORED BY STX
*
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG G-REG XR-1 XR-2 XR-3 STATUS
*****
30E4 0 087A      DC      A64C      STX 3
* N/A N/A N/A N/A FFFF N/A
* INDEX REG 3 NOT STORED BY STX
*
30E5 0 08EC      DC      A680      ADD
* FFFF N/A N/A N/A N/A C AFTER LD+LDS
* FFFF N/A N/A N/A N/A C AFTER A
* ADD FFFF + 0000 TURNED ON OVERFLOW
*
30E6 0 08EC      DC      A680      ADD
* FFFF N/A N/A N/A N/A N/A AFTER LD
* FFFF N/A N/A N/A N/A N/A AFTER A
* ADD FFFF + 0000 FAILED TO EQUAL FFFF
*
30E7 0 0901      DC      A684      ADD
* FFFF N/A N/A N/A N/A OFF AFTER LD+LDS
* 0000 N/A N/A N/A N/A C AFTER A
* ADD FFFF + 0001 DID NOT TURN ON CARRY
*
30E8 0 0901      DC      A684      ADD
* FFFF N/A N/A N/A N/A N/A AFTER LD+LDS
* 0000 N/A N/A N/A N/A N/A AFTER A
* ADD FFFF + 0001 DID NOT EQUAL 0000
*
30E9 0 0914      DC      A688      ADD
* FFFF N/A N/A N/A N/A OFF AFTER LD+LDS
* FFFF N/A N/A N/A N/A C AFTER A
* ADD FFFF + FFFF DID NOT TURN ON CARRY
*
30EA 0 0914      DC      A688      ADD
* FFFF N/A N/A N/A N/A N/A AFTER LD+LDS
* FFFF N/A N/A N/A N/A N/A AFTER A
* ADD FFFF + FFFF DID NOT EQUAL FFFE
*
30EB 0 0928      DC      A68C      ADD
* 4000 N/A N/A N/A N/A OFF AFTER LD
* 8000 N/A N/A N/A N/A 0 AFTER A
* ADD 4000 + 4000 DID NOT TURN ON OVERFLOW
*
30EC 0 0928      DC      A68C      ADD
* 4000 N/A N/A N/A N/A N/A
* ADD 4000 + 4000 DID NOT EQUAL 8000
*
30ED 0 093C      DC      B680      ADD
* 8000 N/A N/A N/A N/A N/A AFTER LD
* 0000 N/A N/A N/A N/A N/A AFTER A
* ADD 8000 + 8000 NOT EQUAL 0000
*
30EE 0 093C      DC      B680      ADD
* 8000 N/A N/A N/A N/A OFF AFTER LD
* 0000 N/A N/A N/A N/A C+0 AFTER A
* ADD 8000 + 8000 DID NOT TURN ON OVERFLOW
*****
```

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```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG G-REG XR-1 XR-2 XR-3 STATUS
*****
30EF 0 093C      DC      B680      ADD
* 8000 N/A N/A N/A N/A DFF AFTER LD
* 0000 N/A N/A N/A N/A C+0 AFTER A
* ADD 8000 + 8000 DID NOT TURN ON CARRY
*
30F0 0 0964      DC      A6C0      LDX 1
* N/A N/A FFF4 N/A N/A N/A
* INDEX REG 1 WAS NOT LOADED EQUAL FFF4
*
30F1 0 0964      DC      A6C0      LD 1
* N/A N/A FFF4 N/A N/A N/A
* A LOAD INSTR INDEXED BY INDEX REG 1
* LOADED THE WRONG LOCATION
*
30F2 0 097C      DC      A6C2      LDX 2
* N/A N/A N/A 0004 N/A N/A
* INDEX REG 2 NOT LOADED EQUAL 0004
*
30F3 0 097C      DC      A6C2      LD 2
* N/A N/A N/A 0004 N/A N/A
* A LOAD INSTR INDEXED BY INDEX REG 2
* LOADED THE WRONG LOCATION
*
30F4 0 0994      DL      A6C4      LDX 3
* N/A N/A N/A N/A 0000 N/A
* INDEX REG 3 NOT LOADED EQUAL 0000
*
30F5 0 0994      DC      A6C4      LD 3
* N/A N/A N/A N/A 0000 N/A
* A LOAD INSTR INDEXED BY INDEX REG 3
* LOADED THE WRONG LOCATION
*
30F6 0 09AB      DC      A6C6      LDX 3
* N/A N/A N/A N/A 0001 N/A
* INDEX REG 3 NOT EQUAL 0001
*
30F7 0 09AB      DC      A6C6      LD 3 LONG FORM
* N/A N/A N/A N/A 0001 N/A
* A LONG FORM LOAD INDEXED BY INDEX REG 3
* LOADED THE WRONG LOCATION
*
30F8 0 09C3      DC      A6C8      LDX 3
* N/A N/A N/A N/A FFFF N/A
* INDEX REG 3 NOT EQUAL FFFF
*
30F9 0 09C3      DC      A6C8      LD 3 INDIRECT
* N/A N/A N/A N/A FFFF N/A
* AN INDIRECT LOAD INDEXED BY INDEX REG 3
* LOADED THE WRONG LOCATION
*****
```

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```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
30FA 0 0A48 DC A700 SUB
* 0000 N/A N/A N/A N/A N/A AFTER LD
* FFFF N/A N/A N/A N/A N/A AFTER S
* SUB 0001 FROM 0000 DID NOT EQUAL FFFF
*
30FB 0 0A48 DC A700 SUB
* 0000 N/A N/A N/A N/A OFF AFTER LD
* FFFF N/A N/A N/A N/A C AFTER S
* SUB 0001 FROM 0000 DID NOT SET CARRY
*
30FC 0 0A5F DC A704 SUB
* 0000 N/A N/A N/A N/A N/A AFTER LD
* 0001 N/A N/A N/A N/A N/A AFTER S
* SUB FFFF FROM 0000 DID NOT EQUAL 0001
*
30FD 0 0A5F DC A704 SUB
* 0000 N/A N/A N/A N/A OFF AFTER LD
* 0001 N/A N/A N/A N/A C AFTER S
* SUB FFFF FROM 0000 DID NOT SET CARRY
*
30FE 0 0A76 DC A708 SUB
* 8000 N/A N/A N/A N/A N/A AFTER LD
* 7FFF N/A N/A N/A N/A N/A AFTER S
* SUB 0001 FROM 8000 DID NOT EQUAL 7FFF
*
30FF 0 0A76 DC A708 SUB
* 8000 N/A N/A N/A N/A OFF AFTER LD
* 0001 N/A N/A N/A N/A C AFTER CARRY
* AND OVERFLOW CONDITION HAD BEEN LOADED INTO
* ACCUMULATOR AS A NUMBER
* SUB 0001 FROM 8000 DID NOT TURN ON OVERFLOW
*
3100 0 0A80 OC A70C SUB
* 0000 N/A N/A N/A N/A N/A AFTER LD
* 8000 N/A N/A N/A N/A N/A AFTER S
* SUB 8000 FROM 0000 DID NOT EQUAL 8000
*
3101 0 0A80 DC A70C SUB
* 0000 N/A N/A N/A N/A OFF AFTER LD
* 8000 N/A N/A N/A N/A C+O AFTER S
* SUB 8000 FROM 0000 DID NOT TURN ON OVERFLOW
*
3102 0 0A80 DC A70C SUB
* 0000 N/A N/A N/A N/A OFF AFTER LD
* 8000 N/A N/A N/A N/A C+O AFTER S
* SUB 9000 FROM 0000 DID NOT TURN ON CARRY
*
3103 0 0A88 OC A740 AO-0000 0000
* FFFF FFFF N/A N/A N/A N/A AFTER LDO
* FFFF FFFF N/A N/A N/A N/A AFTER AD
* ACCUM NOT EQUAL FFFF
*
*****
88413630
88413640
88413650
88413660
88413670
88413680
88413690
88413700
88413710
88413720
88413730
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88413990
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88414020
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88414190
88414200
88414210
88414220
88414230
88414240
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88414300
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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3104 0 0A88 OC A740 AO-0000 0000
* FFFF FFFF N/A N/A N/A N/A AFTER AOO
* FFFF FFFF N/A N/A N/A N/A AFTER RTE
* Q REG NOT EQUAL FFFF
*
3105 0 0A88 DC A740 AD-0000 0000
* FFFF FFFF N/A N/A N/A OFF AFTER LDO
* FFFF FFFF N/A N/A N/A OFF AFTER RTE
* OVERFLOW SET SHOULD NOT BE
*
3106 0 0A88 DC A740 AD-0000 0000
* FFFF FFFF N/A N/A N/A OFF AFTER LDO
* FFFF FFFF N/A N/A N/A OFF AFTER RTE
* CARRY SET-SHOULD NOT BE
*
3107 0 0AE7 DC A746 AD-FFFF FFFF
* 0000 0001 N/A N/A N/A N/A AFTER LDO
* 0000 0000 N/A N/A N/A N/A AFTER AO
* ACCUM NOT EQUAL 0000
*
3108 0 0AE7 OC A746 AO-FFFF FFFF
* 0000 0001 N/A N/A N/A N/A AFTER LDO
* 0000 0000 N/A N/A N/A N/A AFTER AO
* Q REG NOT EQUAL 0000
*
3109 0 0AE7 OC A746 AO-FFFF FFFF
* 0000 0001 N/A N/A N/A OFF AFTER LDO
* 0000 0000 N/A N/A N/A C AFTER AD
* OVERFLOW SET-SHOULD NOT BE
*
310A 0 0AE7 OC A746 AO-FFFF FFFF
* 0000 0001 N/A N/A N/A OFF AFTER LDO
* 0000 0000 N/A N/A N/A C AFTER AO
* CARRY NOT SET-SHOULD BE
*
310B 0 0B14 OC A74C AD-FFFF FFFF
* FFFF FFFF N/A N/A N/A N/A AFTER LDO
* FFFF FFFF N/A N/A N/A N/A AFTER AO
* ACCUM NOT EQUAL FFFF
*
310C 0 0B14 OC A74C AO-FFFF FFFF
* FFFF FFFF N/A N/A N/A N/A AFTER LDO
* FFFF FFFF N/A N/A N/A N/A AFTER AD
* Q REG NOT EQUAL FFFF
*
310D 0 0B14 DC A74C AO-FFFF FFFF
* FFFF FFFF N/A N/A N/A OFF AFTER LDO
* FFFF FFFF N/A N/A N/A C AFTER AO
* OVERFLOW ON-SHOULD NOT BE
*
*****
88414310
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88414400
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88414690
88414700
88414710
88414720
88414730
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88414770
88414780
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88414800
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88414970
88414980
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PROCESSOR-CONTROLLER FUNCTION TEST

*****							88414990		
ADDRESS		*					88415000		
OF		*					88415010		
B-REG	ROUTINE	*	A-REG	C-RLG	XR-1	XR-2	XR-3	STATUS	88415020
*****							88415030		
310E 0	0B14		OC	A74C		AD-FFFF	FFFF		88415040
		*	FFFF	FFFF	N/A	N/A	N/A	OFF AFTER LOD	88415050
		*	FFFF	FFFE	N/A	N/A	N/A	C AFTER AO	88415060
		*	CARRY NOT ON-SHOULD BE						88415070
		*							88415080
		*							88415090
310F 0	0B3E		OC	8742		AD-FFFF	FFFF		88415100
		*	FFFF	7FFF	N/A	N/A	N/A	N/A AFTER LOD	88415110
		*	FFFF	7FFE	N/A	N/A	N/A	N/A AFTER AO	88415120
		*	ACCUM NOT EQUAL FFFF						88415130
		*							88415140
		*							88415150
3110 0	0B3E		OC	8742		AD-FFFF	FFFF		88415160
		*	FFFF	7FFF	N/A	N/A	N/A	N/A AFTER LOD	88415170
		*	FFFF	7FFE	N/A	N/A	N/A	N/A AFTER AD	88415180
		*	Q REG NOT EQUAL 7FFE						88415190
		*							88415200
		*							88415210
3111 0	0B3E		OC	8742		AO-FFFF	FFFF		88415220
		*	FFFF	7FFF	N/A	N/A	N/A	OFF AFTER LOD	88415230
		*	FFFF	7FFE	N/A	N/A	N/A	C AFTER AD	88415240
		*	OVERFLOW SET-SHOULD NOT BE						88415250
		*							88415260
		*							88415270
3112 0	0B3E		DC	8742		AD-FFFF	FFFF		88415280
		*	FFFF	7FFF	N/A	N/A	N/A	OFF AFTER LOD	88415290
		*	FFFF	7FFE	N/A	N/A	N/A	C AFTER AD	88415300
		*	CARRY NOT SET-SHOULD BE						88415310
		*							88415320
		*							88415330
3113 0	0B6B		OC	8747		AD-0001	DDO LDC		88415340
		*	0000	0001	N/A	N/A	N/A	N/A AFTER LOD	88415350
		*	0001	0002	N/A	N/A	N/A	N/A AFTER AD	88415360
		*	ACCUM NOT EQUAL 0001						88415370
		*							88415380
		*							88415390
3114 0	0B6B		DC	8747		AO-0001	DOD LDC		88415400
		*	0000	0001	N/A	N/A	N/A	N/A AFTER LCD	88415410
		*	0001	0002	N/A	N/A	N/A	N/A AFTER AO	88415420
		*	Q REG NOT EQUAL 0002						88415430
		*							88415440
		*							88415450
3115 0	0B8C		DC	A780		SO-0000	0001		88415460
		*	0000	0000	N/A	N/A	N/A	N/A AFTER LOD	88415470
		*	FFFF	FFFF	N/A	N/A	N/A	N/A AFTER SD	88415480
		*	ACCUM NOT EQUAL FFFF						88415490
		*							88415500
		*							88415510
3116 0	0B8C		DC	A780		SO-0000	0001		88415520
		*	0000	0000	N/A	N/A	N/A	N/A AFTER LOD	88415530
		*	FFFF	FFFF	N/A	N/A	N/A	N/A AFTER SD	88415540
		*	Q REG NOT EQUAL FFFF						88415550
		*							88415560
		*							88415570
3117 0	088C		DC	A780		SD-0000	0001		88415580
		*	0000	0000	N/A	N/A	N/A	OFF AFTER LOD	88415590
		*	FFFF	FFFF	N/A	N/A	N/A	C AFTER SD	8841560

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ADDRESS	DF	8-REG	RDUTINE	A-REG	Q-REG	XR-I	XR-2	XR-3	STATUS	
3118 0	088C			OC	A780	SD-0000	0001			
				* 0000	0000	N/A	N/A	N/A	OFF	AFTER LOD
				* FFFF	FFFF	N/A	N/A	N/A	C	AFTER SD
				* CARRY	NOT ON-SHOULD BE					
3119 0	0886			DC	A786	SD-FFFF	FFFF			
				* 0000	0000	N/A	N/A	N/A	N/A	AFTER LOD
				* 0000	0001	N/A	N/A	N/A	N/A	AFTER SD
				* ACCUM	NOT EQUAL TC	0000				
311A 0	0886			OC	A786	SD-FFFF	FFFF			
				* 0000	0000	N/A	N/A	N/A	N/A	AFTER LOD
				* 0000	0001	N/A	N/A	N/A	N/A	AFTER SD
				* Q REG	NOT EQUAL	0001				
3118 0	08CB			DC	A78A	SD-FFFF	FFFF			
				* 0000	C000	N/A	N/A	N/A	N/A	AFTER LOD
				* 0000	C001	N/A	N/A	N/A	N/A	AFTER SD
				* ACCUM	NOT EQUAL	0000				
311C 0	08CB			OC	A78A	SD-FFFF	FFFF			
				* 0000	C000	N/A	N/A	N/A	N/A	AFTER LOD
				* 0000	C001	N/A	N/A	N/A	N/A	AFTER SD
				* Q REG	NOT EQUAL	C001				
311D 0	08DF			OC	A78E	SD-FFFF	DDD	LDC		
				* 0000	0000	N/A	N/A	N/A	N/A	AFTER LOD
				* 0000	0001	N/A	N/A	N/A	N/A	AFTER SD
				* ACCUM	NOT EQUAL	0000				
311E 0	08DF			DC	A78E	SD-FFFF	DDO	LDC		
				* 0000	0000	N/A	N/A	N/A	N/A	AFTER LOD
				* 0000	0001	N/A	N/A	N/A	N/A	AFTER SD
				* Q REG	NOT EQUAL	0001				
311F 0	0C01			OC	A7C0	MULT-2AAA				
				* 5555	N/A	N/A	N/A	N/A	N/A	AFTER LO
				* 0E38	9C72	N/A	N/A	N/A	N/A	AFTER M
				* ACCUM	NOT EQUAL	0E38				
3120 0	0C01			OC	A7C0	MULT-2AAA				
				* 5555	N/A	N/A	N/A	N/A	N/A	AFTER LO
				* 0E38	9C72	N/A	N/A	N/A	N/A	AFTER M
				* Q REG	NOT EQUAL	9C72				
3121 0	0C16			DC	A7C4	MULT-FFFF				
				* FFFF	N/A	N/A	N/A	N/A	N/A	AFTER LO
				* 0000	0001	N/A	N/A	N/A	N/A	AFTER M
				* ACCUM	NOT EQUAL	0000				

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*****										88416350
ADDRESS										88416360
OF										88416370
B-REG ROUTINE A-REG Q-REG XP-1 XH-2 XR-3 STATUS										88416380
*****										88416390
3122 0	UC16	DC	A7C4	MULT-FFFF						88416400
			FFFF	N/A	N/A	N/A	N/A	N/A	AFTER LD	88416410
			0000	0001	N/A	N/A	N/A	N/A	AFTER M	88416420
			Q REG NOT EQUAL 0001							88416430
			*							88416440
			*							88416450
3123 0	UC2A	DC	A7C8	MULT-FFFF						88416460
			0000	N/A	N/A	N/A	N/A	N/A	AFTER LD	88416470
			0000	0000	N/A	N/A	N/A	N/A	AFTER M	88416480
			ACCUM NOT EQUAL 0000							88416490
			*							88416500
			*							88416510
3124 0	UC2A	DC	A7C8	MULT-FFFF						88416520
			0000	N/A	N/A	N/A	N/A	N/A	AFTER LD	88416530
			0000	0000	N/A	N/A	N/A	N/A	AFTER M	88416540
			Q REG NOT EQUAL 0000							88416550
			*							88416560
			*							88416570
3125 0	UC30	DC	A7CC	MULT-0000						88416580
			FFFF	N/A	N/A	N/A	N/A	N/A	AFTER LD	88416590
			0000	0000	N/A	N/A	N/A	N/A	AFTER M	88416600
			ACCUM NOT EQUAL 0000							88416610
			*							88416620
			*							88416630
3126 0	UC30	DC	A7CC	MULT-0000						88416640
			FFFF	N/A	N/A	N/A	N/A	N/A	AFTER LD	88416650
			0000	0000	N/A	N/A	N/A	N/A	AFTER M	88416660
			Q REG NOT EQUAL 0000							88416670
			*							88416680
			*							88416690
3127 0	UC58	DC	A800	DVD-8000						88416700
			4000	7FFF	N/A	N/A	N/A	N/A	AFTER LDD	88416710
			8000	7FFF	N/A	N/A	N/A	N/A	AFTER D	88416720
			ACCUM NOT EQUAL 8000							88416730
			*							88416740
			*							88416750
3128 0	UC58	DC	A800	DVD-8000						88416760
			4000	7FFF	N/A	N/A	N/A	N/A	AFTER LDD	88416770
			8000	7FFF	N/A	N/A	N/A	N/A	AFTER D	88416780
			Q REG NOT EQUAL 7FFF							88416790
			*							88416800
			*							88416810
3129 0	UC58	DC	A800	DVD-8000						88416820
			4000	7FFF	N/A	N/A	N/A	OFF	AFTER LDD	88416830
			8000	7FFF	N/A	N/A	N/A	N/A	AFTER D	88416840
			OVERFLOW ON-SHOULD NOT BE							88416850
			*							88416860
			*							88416870
312A 0	UC58	DC	A800	DVD-8000						88416880
			4000	7FFF	N/A	N/A	N/A	OFF	AFTER LDD	88416890
			8000	7FFF	N/A	N/A	N/A	N/A	AFTER D	88416900
			CARRY ON-SHOULD NOT BE							88416910
			*							88416920
			*							88416930
3128 0	UC87	DC	A806	DVD-5555						88416940
			1C71	8BE3	N/A	N/A	N/A	N/A	AFTER LDD	88416950
			5555	2DAA	N/A	N/A	N/A	N/A	AFTER D	88416960
			ACCUM NOT EQUAL 5555							88416970
			*							88416980
</										

PROCESSOR-CONTROLLER FUNCTION TEST

ADDRESS	OF	A-REG	Q-REG	XK-1	XK-2	XR-3	STATUS	
B-REG ROUTINE								88417030
*****								88417040
312C 0 OC87	OC	A806						88417050
								88417060
								88417070
								88417080
								88417090
								88417100
								88417110
								88417120
								88417130
3120 0 OC87	DC	A806						88417140
								88417150
								88417160
								88417170
								88417180
								88417190
312E 0 OC87	DC	A806						88417200
								88417210
								88417220
								88417230
								88417240
								88417250
312F 0 OC82	DC	A80C						88417260
								88417270
								88417280
								88417290
								88417300
								88417310
3130 0 OC8D	DC	A80E						88417320
								88417330
								88417340
								88417350
								88417360
								88417370
3131 0 OCC8	DC	B800						88417380
								88417390
								88417400
								88417410
								88417420
								88417430
3132 0 OC03	DC	B802						88417440
								88417450
								88417460
								88417470
								88417480
								88417490
3133 0 OCDE	DC	B804						88417500
								88417510
								88417520
								88417530
								88417540
								88417550
3134 0 OCE9	DC	B806						88417560
								88417570
								88417580
								88417590
								88417600
								88417610
3135 0 OD6A	UC	A840						88417620
								88417630
								88417640
								88417650

PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3137 0 0D8D DC A844 MDX 2 LONG FORM
* N/A N/A N/A FFFF N/A N/A AFTER LDX
* N/A N/A N/A FFFF N/A N/A AFTER MDX 2
* INDEX REG 2 NOT EQUAL TO FFFF AFTER MDX +1
* TO INDEX REG 2
*
3138 0 0D9C DC A846 MDX 3
* N/A N/A N/A N/A FFFF N/A AFTER LDX
* N/A N/A N/A N/A 0000 N/A AFTER MDX 2
* MDX DID NOT CAUSE A SKIP WHEN INDEX REG 3
* WENT TO 0000
*
3139 0 0DA6 DC A848 MDX 1
* N/A N/A FFFF N/A N/A N/A AFTER LDX
* N/A N/A 0003 N/A N/A N/A AFTER MDX 1
* MDX DID NOT CAUSE A SKIP WHEN THE SIGN
* CHANGED ON INDEX REG 1
*
313A 0 0D80 DC A849 MDX 1 INDIRECT
* N/A N/A FFFF N/A N/A N/A AFTER LDX
* N/A N/A FFFF N/A N/A N/A AFTER LDX 11
* INDIRECT MDX OF INDEX REG 1 BY +1 FAILED
*
313B 0 0DE4 JC A880 SLC-A-XR 1
* 0000 N/A 0010 N/A N/A N/A AFTER LDX
* 0000 N/A 0000 N/A N/A N/A AFTER SLCA
* ACCUM NOT EQUAL 0000
*
313C 0 0DE4 DC A880 SLC-A-XR 1
* 0000 N/A 0010 N/A N/A N/A AFTER LDX
* 0000 N/A 0000 N/A N/A N/A AFTER SLAC
* INDEX REG 1 NOT EQUAL 0000
*
313D 0 0E0B DC A884 SLC-A-XR 1
* 0001 N/A FF00 N/A N/A N/A AFTER LDX
* 0000 N/A FFC1 N/A N/A N/A AFTER ASCL
* ACCUM NOT EQUAL 8000
*
313E 0 0E0B DC A884 SLC-A-XR 1
* 0001 N/A FF00 N/A N/A N/A AFTER LDX
* 8000 N/A FFC1 N/A N/A N/A AFTER LDX
* INDEX REG 1 NOT EQUAL FFC1
*
313F 0 0F33 OC A888 SLC-A-XR 1
* 8000 N/A 0010 N/A N/A N/A AFTER LDX
* 8000 N/A 0010 N/A N/A N/A AFTER SLCA
* ACCUM NOT EQUAL 8000
*
3140 0 0E33 OC A888 SLC-A-XR 1
* 8000 N/A 0010 N/A N/A N/A AFTER LDX
* 8000 N/A 0010 N/A N/A N/A AFTER SLCA
* INDEX REG 1 NOT EQUAL 0010
*
88417710
88417720
88417730
88417740
88417750
88417760
88417770
88417780
88417790
88417800
88417810
88417820
88417830
88417840
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88417860
88417870
88417880
88417890
88417900
88417910
88417920
88417930
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88417950
88417960
88417970
88417980
88417990
88418000
88418010
88418020
88418030
88418040
88418050
88418060
88418070
88418080
88418090
88418100
88418110
88418120
88418130
88418140
88418150
88418160
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88418260
88418270
88418280
88418290
88418300
88418310
88418320
88418330
88418340
88418350
88418360
88418370
88418380
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```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3141 0 0E6E DC A88C SLC-XR 1
* 0000 0000 0020 N/A N/A N/A AFTER LDX
* 0000 0000 0000 N/A N/A N/A AFTER SLC
* ACCUM NOT EQUAL 0000
*
3142 0 0E6E DC A88C SLC-XR 1
* 0000 0000 0020 N/A N/A N/A AFTER LDX
* 0000 0000 0000 N/A N/A N/A AFTER SLC
* Q REG NOT EQUAL 0000
*
3143 0 0E6E DC A88C SLC-XR 1
* 0000 0000 0020 N/A N/A N/A AFTER LDX
* 0000 0000 0000 N/A N/A N/A AFTER SLC
* INDEX REG 1 NOT EQUAL 0000
*
3144 0 0E8D OC 8882 SLC-XR 1
* 0000 0002 FFOF N/A N/A N/A AFTER LDX
* 8000 0000 FFC1 N/A N/A N/A AFTER SLC
* ACCUM NOT EQUAL 8000
*
3145 0 0E8D OC 8882 SLC-XR 1
* 0000 0002 FFOF N/A N/A N/A AFTER LDX
* 8000 0000 FFC1 N/A N/A N/A AFTER SLC
* Q REG NOT EQUAL 0000
*
3146 0 0E8D DC 8882 SLC-XR 1
* 0000 0002 FFOF N/A N/A N/A AFTER LDX
* 8000 0000 FFC1 N/A N/A N/A AFTER SLC
* INDEX REG 1 NOT EQUAL FFC1
*
3147 0 0EAF DC 8884 SLC-XR 1
* 0000 0002 001F N/A N/A N/A AFTER LDX+LOX
* 8000 0000 0001 N/A N/A C AFTER SLC
* A SLC TERMINATED BY A CNE 81T IN ACCUM 81T
* ZERO DID NOT TURN ON CARRY
*
3148 0 0EAF OC 8884 SLC-XR 1
* 0000 0002 001F N/A N/A N/A AFTER LDX+LOX
* 8000 0000 0001 N/A N/A C AFTER SLC
* ACCUM WAS NOT EQUAL TO 8000
*
3149 0 0EAF DC 8884 SLC-XR 1
* 0000 0002 001F N/A N/A N/A AFTER LDX+LOX
* 8000 0002 0001 N/A N/A C AFTER SLC
* A SLC TERMINATED BY A CNE IN ACCUM 81T
* ZERO DID NOT LEAVE XR 1 EQUAL 0001
*
314A 0 0ECF DC 8885 SLC-1X 1
* 0000 0002 001C N/A N/A N/A AFTER LDX+LOX
* 2000 0000 0000 N/A N/A OFF AFTER SLC
* A SLC TERMINATED BY XR 1 GOING TO ZERO LEFT
* THE CARRY FF SET
*
88418390
88418400
88418410
88418420
88418430
88418440
88418450
88418460
88418470
88418480
88418490
88418500
88418510
88418520
88418530
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88418570
88418580
88418590
88418600
88418610
88418620
88418630
88418640
88418650
88418660
88418670
88418680
88418690
88418700
88418710
88418720
88418730
88418740
88418750
88418760
88418770
88418780
88418790
88418800
88418810
88418820
88418830
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88418900
88418910
88418920
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88418970
88418980
88418990
88419000
88419010
88419020
88419030
88419040
88419050
88419060
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PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
314B 0 0EEC      DC      B8A0      CMP  A GREATER M
* 4000 N/A      N/A      N/A      N/A      N/A
* A GREATER THAN M CMP FAILED
*
314C 0 0EEC      DC      B8A0      CMP  A GREATER M
* 4000 N/A      N/A      N/A      N/A      N/A AFTER LO
* 4000 N/A      N/A      N/A      N/A      N/A AFTER CMP
* ACC DESTROYED AFTER CMP
*
3140 0 0F07      DC      B8A1      CMP  A LESS M
* 0000 N/A      N/A      N/A      N/A      N/A
* ACC LESS THAN M FAILS
*
314E 0 0F11      DC      B8A2      CMP  A LESS M
* 0000 N/A      N/A      N/A      N/A      N/A
* ACC LESS THAN M FAILS
*
314F 0 0F18      OC      B8A3      CMP  A LESS M
* 0000 N/A      N/A      N/A      N/A      N/A
* ACC LESS THAN M FAILS
*
3150 0 0F25      DC      B8A4      CMP  A LESS M
* 8000 N/A      N/A      N/A      N/A      N/A
* ACC LESS THAN M FAILS
*
3151 0 0F2F      DC      B8A5      CMP  A EQ M
* 1000 N/A      N/A      N/A      N/A      N/A
* ACC EQ M FAILED
*
3152 0 0F3A      OC      B8C0      DCM  AQ GTR M,M+1
* 8000 0001 N/A      N/A      N/A      N/A
* DCM AQ GREATER THAN M, M+1 FAILED
*
3153 0 0F3A      OC      B8C0      DCM  AQ GTR M, M+1
* 8000 0001 N/A      N/A      N/A      N/A
* ACC DESTROYED AFTER DCM
*
3154 0 0F3A      DC      B8C0      DCM  AQ GTR M,M+1
* 8000 0001 N/A      N/A      N/A      N/A
* J REG DESTROYED AFTER DCM
*
3155 0 0F54      OC      B8C1      DCM  AQ LESS M,M+1
* 0000 8000 N/A      N/A      N/A      N/A
* DCM FAILED WHEN A,Q LESS THAN M, M+1
*
3156 0 0F5D      DC      B8C2      DCM  AQ EQ M,M+1
* 0000 8000 N/A      N/A      N/A      N/A
* DCM FAILED WHEN A,Q EQ M, M+1
*
*****
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```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3157 0 08BF      DC      A660      LDX 1 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A FFFF 0000 DC00 N/A AFTER LDX 1
* INDEX 2 CHANGED
*
3158 0 08BF      DC      A660      LDX 1 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A FFFF 0000 0000 N/A AFTER LDX 1
* INDEX 3 CHANGED
*
3159 0 0BA7      OC      A662      LDX 2 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A 0000 FFFF 0000 N/A AFTER LDX 2
* INDEX 1 CHANGED
*
315A 0 0BA7      DC      A662      LDX 2 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A 0000 FFFF 0000 N/A AFTER LDX 2
* INDEX 3 CHANGED
*
315B 0 08BF      DC      A664      LDX 3 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A 0000 0000 FFFF N/A AFTER LDX 3
* INDEX 1 CHANGED
*
315C 0 08BF      DC      A664      LDX 3 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A 0000 0000 FFFF N/A AFTER LDX 3
* INDEX 2 CHANGED
*
315D 0 09EC      DC      A6D0      INDEXED INST F=0
* INITIALLY XR 1 HAS CORE LOCATION OF
* SYMBOLIC LABEL N6C1
* AFTER THE TEST THE ACC SHOULD HAVE
* CORE LOCATION OF SYMBOLIC LABEL N6C0
* SHORT FORM INDEXED INST FAILED (X=1)
*
315E 0 09FB      OC      A6D2      INDEXED INST F=0
* INITIALLY XR 2 HAS CORE LOCATION OF
* SYMBOLIC LABEL N6C1
* AFTER THE TEST THE ACC SHOULD HAVE
* CORE LOCATION OF SYMBOLIC LABEL N6C2
* SHORT FORM INDEXED INST FAILED (X=2)
*
315F 0 0A04      OC      A603      INDEXED INST F=0
* INITIALLY XR 3 HAS CORE LOCATION OF
* SYMBOLIC LABEL N6C1
* AFTER THE TEST THE ACC SHOULD HAVE
* CORE LOCATION OF SYMBOLIC LABEL N6C1
* SHORT FORM INDEXED INST. FAILED (X=3)
*
*****
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3160 0 0DE4 DC A880 SLCA CK CARRY
* 0000 FFFF 000A N/A N/A C AFTER LDD+LDS
* 0000 FFFF 0000 N/A N/A OFF AFTER STS
* CARRY ON SHOULD BE OFF
*
3161 0 0E08 OC A884 SLCA CK CARRY
* 0001 0010 FF00 N/A N/A OFF AFTER LDD+LDX
* 8000 0010 FF01 N/A N/A C AFTER SLCA
* CARRY OFF, SHOULD BE ON
*
3162 0 0E50 OC A889 NON INDEXED SLCA
* 0001 N/A 0010 0010 0010 N/A AFTER LD
* 0002 N/A N/A N/A N/A AFTER SLCA
* SLCA T=0 FAILED
*
3163 0 0A10 DC A605 INDEXED SLA
* 0001 N/A 0002 N/A N/A N/A AFTER LO+LDX
* 0004 N/A N/A N/A N/A AFTER SLA
* INDEXED SLA FAILED
*
3164 0 0A1C OC A606 INDEXED SRA
* 0004 N/A N/A 0002 N/A N/A AFTER LDX+LD
* 0001 N/A N/A N/A N/A AFTER SRA
* INDEXED SRA FAILED
*
3165 0 0A28 OC A6F0 INDEXED BSC
* INITIALLY ACC HAS CORE LOCATION OF
* SYMBOLIC LABEL M6F1
* ACC DESTROYED AFTER INDEXED BSC
*
3166 0 0A39 OC A6F1 INDIR, INDEX BSC
* N/A N/A 0001 N/A M/A N/A AFTER LOX
* N/A N/A N/A N/A N/A AFTER BSC
* INDIRECT, INDEXED BSC FAILED
*
3167 0 0820 OC A640 STX CK ACC
* INITIALLY ACC HAS CORE LOCATION OF
* SYMBOLIC LABEL M640
* ACC DESTROYED AFTER STX
*
3168 0 0DB0 OC A849 MOX CK ACC
* INITIALLY ACC HAS CORE LOCATION OF
* SYMBOLIC LABEL M849
* ACC DESTROYED AFTER MOX
*
3169 0 08D9 DC A670 ACC DECODE
* 0001 N/A 0010 N/A N/A N/A
* 0000 N/A 0000 N/A N/A N/A
* FALSE DECODE OF ACC BE ZERO
* * EACH BIT POSITION IS TESTED
*
*****
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
ADDRESS *
OF *
B-REC ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
316A 0 0D18 OC B807 DVD OVFLD
* 6100 0000 N/A N/A N/A OFF AFTER LDD
* N/A N/A N/A N/A C AFTER D
* OVFLD NOT ON
*
3168 0 0D23 DC 8808 DVO OVFLD
* 8000 0000 N/A N/A N/A OFF AFTER LOO
* N/A N/A N/A N/A O AFTER O
* OVFLD NOT ON
*
316C 0 0D2E OC B809 DVD NO OVFLD
* FFFF FFFF N/A N/A N/A OFF AFTER LOO
* N/A N/A N/A N/A N/A OFF AFTER D
* OVFLD ON, SHOULD BE OFF
*
3160 0 0D3A DC B810 MPY-DIV ZERO REM
* ACC WRONG AFTER MPY-DIV TEST
*
316E 0 0D3A DC B810 MPY-DIV ZERO REM
* Q REG WRONG AFTER MPY-DIV TEST
*
316F 0 0078 OC A842 MDX CK ACC
* INITIALLY ACC HAS CORE LOCATION OF
* SYMBOLIC LABEL M844
* ACC DESTROYED AFTER ADD TO MEMORY
*
3170 0 0600 OC A50A BSC CK ACC
* 8001 N/A N/A N/A N/A N/A AFTER LD
* 8001 N/A N/A N/A N/A N/A AFTER BSC
* ACC DESTROYED AFTER BSC CONDITIONS MET
*
3171 0 0DC6 DC A84A MOX MEM CK SKIP
* MEMORY LOC HAS ZERO
* MOX FAILED TO SKIP
*
3172 0 00D0 OC A85A MOX MEM CK NO SKP
* MEMORY LOC IS NON ZERO
* MOX SKIPPED, SHOULD NOT HAVE
*
3173 0 0E60 DC A88A SW 15 NO INDEX
* 0000 FFFF 0010 0010 0010 NAFIER LDX'S
* 7FFF N/A N/A N/A N/A NAFIER SLCA
* ACCUM NOT EQ TO 7FFF
*
3174 0 0F83 OC F000 IMPROPER CONTROL
* OPERATION SPECIFIED,
* BIT SW 14 ON WITHOUT
* BIT SW 8 ON 12 ON.
* CORRECT SWs AND PUSH
* START TO CONTINUE
*
*****
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
      ORG      300
0175 012C 0 8400 DC /B400 PID
*****
*
* 8000 N/A N/A N/A N/A 0 AFTER A
* 0000 N/A N/A N/A N/A C+AFTER A
*
* TEST MDX OPERATION
*****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEC= AT RIGHT
*****
012D 0 3000 X000 DC /3000 SET SWITCHES TO RUN
012E 0 7001 A080 MDX G080
012F 0 3004 DC /3004 ERR ID + ERR WAIT
MDX BY 1 FAILED
0130 0 7002 G080 MDX G081
0131 0 3005 DC /3005 ERR ID + ERR WAIT
MDX BY 2 FAILED
0132 0 3006 DC /3006 ERR ID + ERR WAIT
MDX BY 2 FAILED
0133 0 7004 G081 MDX G082
0134 0 3007 DC /3007 ERR ID + ERR WAIT
MDX BY 4 FAILED
0135 0 3008 DC /3008 ERR ID + ERR WAIT
MDX BY 4 FAILED
0136 0 3009 DC /3009 ERR ID + ERR WAIT
MDX BY 4 FAILED
0137 0 300A DC /300A ERR ID + ERR WAIT
MDX BY 4 FAILED
0138 0 7002 G082 MDX G084
0139 0 3008 DC /3008 ERR ID + ERR WAIT
MDX BY 2 FAILED
013A 0 7004 G083 MDX A0C0
013B 0 70FE G084 MDX G083
013C 0 300C DC /300C ERR ID + ERR WAIT
MDX BY -2 FAILED
013D 0 300D DC /300D ERR ID + ERR WAIT
MDX BY -2 FAILED
013E 0 300E DC /300E ERR ID + ERR WAIT
MDX BY -2 FAILED
*
* TEST OF BSC SKIP WHEN IT
* SHOULD NOT
*****
013F 0 2003 A0C0 LDS 3 SET C AND DF ON
0140 0 4802 BSC C SK IF CARRY IS OFF
0141 0 7002 MDX G0C1
0142 0 300F DC /300F ERR ID + ERR WAIT
BSC-CARRY FAILED
0143 0 0000 N100 DC 0
0144 0 4801 G0C1 BSC 0
0145 0 7001 MDX G0C2
0146 0 3010 DC /3010 ERR ID + ERR WAIT
BSC-OVERFLOW FAILED
0147 0 4801 G0C2 BSC 0 CK IF DF WAS RESET
0148 0 3011 DC /3011 ERR ID + ERR WAIT
BSC-CVFLW SKPD-SHOULD
*NOT HAVE
0149 0 2000 LDS 0 RESET CARRY TO OFF
014A 0 4802 BSC C SK IF CARRY IS OFF
014B 0 3012 DC /3012 ERR ID + ERR WAIT
BSC-C DID NOT SKIP
*****
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
*
* TEST OF ACC ABILITY TO HOLD
* ALL ZEROS
*****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEC= AT RIGHT
*****
014C 0 C0F6 A100 LD N100 LD /0000
014D 0 4820 BSC Z SK IF ZERO
014E 0 3013 DC /3013 ERR ID + ERR WAIT
LD ACC TO 0 FAILED
014F 0 C0F3 LD N100 ACC=0, RFLD TO 0
0150 0 4820 BSC Z SK IF ZERO
0151 0 3014 DC /3014 ERR ID + ERR WAIT
LD ACC TO 0 FAILED
0152 0 4804 BSC E SK IF EVEN
0153 0 3015 DC /3015 ERR ID + ERR WAIT
BSC ON EVEN FAILED
*
*
* CONTAIN ALL LINES
*****
0154 0 C04A A140 LD N140 ACC.=0, RELOAD TO ONES
0155 0 4810 BSC - SK IF MINUS
0156 0 3016 DC /3016 ERR ID + ERR WAIT
LOAD ACC. FAILED OR
*BSC ON NEG. FAILED
0157 0 4803 BSC +
0158 0 7001 MDX G140
0159 0 3017 DC /3017 ERR ID + ERR WAIT
BSC ON + SKPD-
*SHOULD NOT HAVE
015A 0 4804 G140 BSC E
015B 0 7001 MDX G141
015C 0 3018 DC /3018 ERR ID + ERR WAIT
BSC ON E SKPD-
*SHOULD NOT HAVE
015D 0 1801 G141 SPA 1
015E 0 4804 BSC E
015F 0 7001 MDX G142
0160 0 3019 DC /3019 ERR ID + ERR WAIT
ACC NOT = 7FFF
0161 0 1801 G142 SRA 1
0162 0 4804 BSC E
0163 0 7001 MDX G143
0164 0 301A DC /301A ERR ID + ERR WAIT
ACC NOT = 3FFF
0165 0 1801 G143 SRA 1
0166 0 4804 BSC E
0167 0 7001 MDX G144
0168 0 301B DC /301B ERR ID + ERR WAIT
ACC NOT = 1FFF
0169 0 1801 G144 SRA 1
016A 0 4804 BSC E
016B 0 7001 MDX G145
016C 0 0000 DC /301C ERR ID + ERR WAIT
ACC NOT = 0FFF
016D 0 1801 G145 SRA 1
016E 0 4804 BSC E
016F 0 7001 MDX G146
0170 0 301D DC /301D ERR ID + ERR WAIT
ACC NOT = 07FF
0171 0 1801 G146 SRA 1
0172 0 4804 BSC E
0173 0 7001 MDX G147
0174 0 301E DC /301E ERR ID + ERR WAIT
ACC NOT = 03FF
0175 0 1801 G147 SRA 1
*****
```

PROCESSOR-CONTROLLER FUNCTION TEST

0176 0 4804	BSC	E		
0177 0 7001	MOX	G148		
0178 0 301F	OC	/301F	ERR IO + ERR WAIT	
			ACC NOT = 01FF	
0179 0 1801	* G148 SRA	1		
017A 0 4804	BSC	E		
017B 0 7001	MDX	G149		
017C 0 3020	OC	/3020	ERR ID + ERR WAIT	
			ACC NOT = 00FF	
0170 0 1801	* G149 SRA	1		
017E 0 4804	BSC	E		
017F 0 7001	MOX	G14A		
0180 0 3021	DC	/3021	ERR IO + ERR WAIT	
			ACC NOT = 007F	
0181 0 1801	* G14A SRA	1		
0182 0 4804	BSC	E		
0183 0 7001	MOX	G14B		
0184 0 3022	OC	/3022	ERR IO + ERR WAIT	
			ACC NOT = 003F	
0185 0 1801	* G14B SRA	1		
0186 0 4804	BSC	E		
0187 0 7001	MOX	G14C		
0188 0 3023	OC	/3023	ERR ID + ERR WAIT	
			ACC NOT = 001F	
0189 0 1801	* G14C SRA	1		
018A 0 4804	BSC	E		
018B 0 7001	MDX	G140		
018C 0 3024	DC	/3024	ERR IO + ERR WAIT	
			ACC NOT = 000F	
0180 0 1801	* G140 SRA	1		
018E 0 4804	BSC	E		
018F 0 7001	MDX	G14E		
0190 0 3025	OC	/3025	ERR IO + ERR WAIT	
			ACC NOT = 0007	
0191 0 1801	* G14E SRA	1		
0192 0 4804	BSC	E		
0193 0 7001	MOX	G14F		
0194 0 3026	OC	/3026	ERR IO + ERR WAIT	
			ACC NOT = 0003	
0195 0 1801	* G14F SRA	1		
0196 0 4804	BSC	E		
0197 0 7001	MOX	G150		
0198 0 3027	OC	/3027	ERR IO + ERR WAIT	
			ACC NOT = 0001	
0199 0 1801	* G150 SRA	1		
019A 0 4804	BSC	E		
019B 0 3028	DC	/3028	ERR IO + ERR WAIT	
			ACC NOT = 0000	
019C 0 4820	* BSC	Z		
0190 0 3029	OC	/3029	ERR IO + ERR WAIT	
			ACC NOT = 0000	
019E 0 7001	* MOX	A180		
019F 0 FFFF	OC	/FFFF	EXIT TO NEXT ROUTINE	

TEST LOING OF ONES ON ONES

CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT

01A0 0 C049	A180 LO	N180	LO /FFFF	
01A1 0 482C	BSC	+EZ	SK ON +, EVEN OR ZERO	
01A2 0 4810	BSC	-	SK IF MINUS	
01A3 0 302A	DC	/302A	ERR IO + ERR WAIT	
			ACC NOT = FFFF	
01A4 0 C045	* LO	N180	LO /FFFF	
01A5 0 482C	BSC	+EZ		

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01A6 0 4810	BSC	-		
01A7 0 302B	OC	/302B	ERR ID + ERR WAIT	
			ACC NOT = FFFF	
01A8 0 1801	* SRA	1	SHIFT RIGHT ONE	
			TEST ABILITY OF ACC TO SHIFT	
01A9 0 4804	* BSC	E		
01AA 0 7001	MOX	G181		
01AB 0 302C	OC	/302C	ERR IO + ERR WAIT	
			ACC NOT = 7FFF	
01AC 0 1801	* G181 SRA	1		
01AD 0 4804	BSC	E		
01AE 0 7001	MOX	G182		
01AF 0 3020	DC	/3020	ERR ID + ERR WAIT	
			ACC NOT = 3FFF	
0180 0 1801	* G182 SRA	1		
0181 0 4804	BSC	E		
0182 0 7001	MOX	G183		
0183 0 302E	OC	/302E	ERR IO + ERR WAIT	
			ACC NOT = 1FFF	
0184 0 1801	* G183 SRA	1		
0185 0 4804	BSC	E		
0186 0 7001	MOX	G184		
0187 0 302F	DC	/302F	ERR ID + ERR WAIT	
			ACC NOT = 0FFF	
0188 0 1801	* G184 SRA	1		
0189 0 4804	BSC	E		
018A 0 7001	MOX	G185		
018B 0 3030	OC	/3030	ERR IO + ERR WAIT	
			ACC NOT = 07FF	
018C 0 1801	* G185 SRA	1		
018D 0 4804	BSC	E		
018E 0 7001	MOX	G186		
018F 0 3031	DC	/3031	ERR ID + ERR WAIT	
			ACC NOT = 03FF	
01C0 0 1801	* G186 SRA	1		
01C1 0 4804	BSC	E		
01C2 0 7001	MOX	G187		
01C3 0 3032	OC	/3032	ERR IO + ERR WAIT	
			ACC NOT = 01FF	
01C4 0 1801	* G187 SRA	1		
01C5 0 4804	BSC	E		
01C6 0 7001	MOX	G188		
01C7 0 3033	OC	/3033	ERR ID + ERR WAIT	
			ACC NOT = 00FF	
01C8 0 1801	* G188 SRA	1		
01C9 0 4804	BSC	E		
01CA 0 7001	MOX	G189		
01CB 0 3034	OC	/3034	ERR ID + ERR WAIT	
			ACC NOT = 007F	
01CC 0 1801	* G189 SRA	1		
01CD 0 4804	BSC	E		
01CE 0 7001	MOX	G18A		
01CF 0 3035	OC	/3035	ERR IO + ERR WAIT	
			ACC NOT = 003F	
01D0 0 1801	* G18A SRA	1		
01D1 0 4804	BSC	E		
01D2 0 7001	MOX	G18B		
01D3 0 3036	OC	/3036	ERR IO + ERR WAIT	
			ACC NOT = 001F	
01D4 0 1801	* G18B SRA	1		
01D5 0 4804	BSC	E		
01D6 0 7001	MOX	G18C		
01D7 0 3037	OC	/3037	ERR IO + ERR WAIT	
			ACC NOT = 000F	
01D8 0 1801	* G18C SRA	1		
01D9 0 4804	BSC	E		
01DA 0 7001	MOX	G180		
01DB 0 3038	OC	/3038	ERR IO + ERR WAIT	

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```
*
01DC 0 1801      * G18D SRA 1      ACC NOT = 0007      88424510
01DD 0 4804      BSC E          88424520
01DE 0 7001      MDX G18E       88424530
01DF 0 3039      DC /3039      ERR ID + ERR WAIT      88424540
                                ACC NOT = 0003          88424550
                                88424560
01EO 0 1801      * G18E SRA 1      88424570
01E1 0 4804      BSC E          88424580
01E2 0 7001      MDX G18F       88424590
01E3 0 303A      DC /303A      ERR ID + ERR WAIT      88424600
                                ACC NOT = 0001          88424610
                                88424620
01E4 0 1801      * G18F SRA 1      88424630
01E5 0 4804      BSC E          88424640
01E6 0 3038      DC /3038      ERR ID + ERR WAIT      88424650
                                ACC NOT = 0000          88424660
                                88424670
01E7 0 4820      * BSC Z          88424680
01E8 0 303C      DC /303C      ERR ID + ERR WAIT      88424690
                                ACC NOT = 0000          88424700
                                88424710
01E9 0 7001      * MOX A1CO      88424720
01EA 0 FFFF      N180 OC /FFFF  EXIT TO NEXT ROUTINE 88424730
                                88424740
                                88424750
                                88424760
                                88424770
                                88424780
                                88424790
                                88424800
                                88424810
                                88424820
                                88424830
                                88424840
                                88424850
                                88424860
                                88424870
                                88424880
                                88424890
                                88424900
                                88424910
                                88424920
                                88424930
                                88424940
                                88424950
                                88424960
                                88424970
                                88424980
                                88424990
                                88425000
                                88425010
                                88425020
                                88425030
                                88425040
                                88425050
                                88425060
                                88425070
                                88425080
                                88425090
                                88425100
                                88425110
                                88425120
                                88425130
                                88425140
                                88425150
                                88425160
                                88425170
                                88425180

*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
01EB 0 C007      A1CO LD N1CO LD /0000      88424800
01EC 0 4820      BSC Z SK ON ZERO          88424810
01ED 0 303D      DC /303D ERR ID + ERR WAIT      88424820
                                ACC NOT = ZERO      88424830
                                LD /FFFF          88424840
                                88424850
01EE 0 C005      LD N1C1          88424860
01EF 0 482C      BSC +EZ          88424870
01FO 0 4810      BSC -          88424880
01F1 0 303E      OC /303E      SK DN MINUS      88424890
                                ERR ID + ERR WAIT      88424900
                                ACC NOT = FFFF      88424910
                                EXIT TO NEXT ROUTINE 88424920
                                88424930
01F2 0 7002      MDX A1DO          88424940
01F3 0 0000      N1CO DC /0000          88424950
01F4 0 FFFF      N1C1 DC /FFFF          88424960
                                88424970
                                88424980
                                88424990
                                88425000
                                88425010
                                88425020
                                88425030
                                88425040
                                88425050
                                88425060
                                88425070
                                88425080
                                88425090
                                88425100
                                88425110
                                88425120
                                88425130
                                88425140
                                88425150
                                88425160
                                88425170
                                88425180

*****
TEST EDR OPERATION
*****
01F5 0 C01C      A1DO LD N1D1 LD /0000      88424970
01F6 0 4820      BSC Z SK ON ZERO          88424980
01F7 0 303F      DC /303F ERR ID + ERR WAIT      88424990
                                ACC NOT = ZERO      88425000
                                ZERO WITH /0000      88425010
                                SK ON ZERO          88425020
                                ERR ID + ERR WAIT      88425030
                                EDR OF 0 AND 0 FAILED 88425040
                                LD /FFFF          88425050
                                88425060
                                88425070
                                88425080
                                88425090
                                88425100
                                88425110
                                88425120
                                88425130
                                88425140
                                88425150
                                88425160
                                88425170
                                88425180

*****
01F8 0 F019      EDR N1D1          88425090
01F9 0 4820      BSC Z          88425100
01FA 0 3040      DC /3040      ERR ID + ERR WAIT      88425110
                                EDR OF 0 AND 0 FAILED 88425120
                                LD /FFFF          88425130
                                88425140
                                88425150
                                88425160
                                88425170
                                88425180

*****
01FB 0 C015      LD N1D0          88425090
01FC 0 F014      EDR N1D0          88425100
01FD 0 4820      BSC Z          88425110
01FE 0 3041      OC /3041      ERR ID + ERR WAIT      88425120
                                EDR OF 1 AND 1 FAILED 88425130
                                88425140
                                88425150
                                88425160
                                88425170
                                88425180

*****
01FF 0 F011      EDR N1D0          88425090
0200 0 482C      BSC +EZ          88425100
0201 0 4810      BSC -          88425110
0202 0 3042      DC /3042      ERR ID + ERR WAIT      88425120
                                FOR OF 1 AND 0 FAILED 88425130
                                88425140
                                88425150
                                88425160
                                88425170
                                88425180

*****
0203 0 1801      SRA 1          88425150
0204 0 F00E      EDR N1D2          88425160
0205 0 4820      BSC Z          88425170
0206 0 3043      DC /3043      ERR ID + ERR WAIT      88425180
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
*
0207 0 C009      * LO N1D0          88425190
0208 0 F007      EOR N1D1          88425200
0209 0 482C      BSC +EZ          88425210
020A 0 4810      BSC -          88425220
020B 0 3044      OC /3044      ERR ID + ERR WAIT      88425230
                                EDR OF 0 AND 1 FAILED 88425240
                                88425250
                                88425260
                                88425270
                                88425280
                                88425290
                                88425300
                                88425310
                                88425320
                                88425330
                                88425340
                                88425350
                                88425360
                                88425370
                                88425380
                                88425390
                                88425400
                                88425410
                                88425420
                                88425430
                                88425440
                                88425450
                                88425460
                                88425470
                                88425480
                                88425490
                                88425500
                                88425510
                                88425520
                                88425530
                                88425540
                                88425550
                                88425560
                                88425570
                                88425580
                                88425590
                                88425600
                                88425610
                                88425620
                                88425630
                                88425640
                                88425650
                                88425660
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*****
020C 0 1801      * SRA 1          88425290
020D 0 F005      FOR N1D2          88425300
020E 0 4820      BSC Z          88425310
020F 0 3045      DC /3045      ERR ID + ERR WAIT      88425320
                                EDR OF 0 AND 1 FAILED 88425330
                                EXIT TO NEXT ROUTINE 88425340
                                88425350
                                88425360
                                88425370
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*****
0210 0 7003      * MOX A1EO          88425370
0211 0 FFFF      N100 DC /FFFF          88425380
0212 0 0000      N1D1 DC /0000          88425390
0213 0 FFFF      N102 DC /7FFF          88425400
                                88425410
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*****
0214 00 C400021F A1EO LO L N1E1 LO /0000      88425400
0216 0 4820      BSC Z SK ON ZERO          88425410
0217 0 3046      DC /3046      ERR ID + ERR WAIT      88425420
                                WRONG LOCATION LOADED 88425430
                                LD C(N1EO)          88425440
                                88425450
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                                88425500
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*****
0218 00 C400021E LD L N1EO          88425440
021A 0 F003      EOR N1EO          88425450
021B 0 4820      BSC Z          88425460
021C 0 3047      DC /3047      SK ON ZERO          88425470
                                ERR ID + ERR WAIT      88425480
                                WRONG LOCATION LOADED 88425490
                                EXIT TO NEXT ROUTINE 88425500
                                88425510
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*****
021D 0 7002      * MDX A1F0          88425540
021E 0 021E      N1EO DC N1EO          88425550
021F 0 0000      N1E1 DC /0000          88425560
                                88425570
                                88425580
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*****
0220 00 C480022C A1FO LD I N1F2 LD /0000      88425560
0222 0 4820      BSC Z SK ON ZERO          88425570
0223 0 3048      DC /3048      ERR ID + ERR WAIT      88425580
                                WRONG LOCATION LOADED 88425590
                                LD C(N1F1)          88425600
                                ZERO WITH C(N1F1)      88425610
                                88425620
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*****
0224 00 C480022B LD I N1F1          88425600
0226 0 F004      EOR N1F1          88425610
0227 0 4820      BSC Z          88425620
0228 0 3049      DC /3049      ERR ID + ERR WAIT      88425630
                                WRONG LOCATION LOADED 88425640
                                EXIT TO NEXT ROUTINE 88425650
                                88425660
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                                88425680
                                88425690
                                88425700
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*****
0229 0 7003      * MDX A200          88425650
022A 0 0000      N1FO DC /0000          88425660
022B 0 022B      N1F1 DC N1F1          88425670
022C 0 022A      N1F2 DC N1FO          88425680
                                88425690
                                88425700
                                88425710
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*****
TEST OF BSC LONG FORM AND
INDIRECT OPERATION
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
022D 00 C0000231 A200 BSC L G200          88425780
022F 0 304A      DC /304A      ERR ID + ERR WAIT      88425790
                                BSC OLD NOT BRANCH      88425800
                                ERR ID + ERR WAIT      88425810
                                BSC SKPD-SHOULD BRNCH 88425820
                                88425830
                                88425840
                                88425850
                                88425860

*****
0230 0 304B      * OC /304B          88425800
                                88425810
                                88425820
                                88425830
                                88425840
                                88425850
                                88425860

*****
0231 0 C03A      G200 LO N200          88425830
0232 00 C0040236 BSC L G201,E          88425840
0234 0 304C      DC /304C      ERR ID + ERR WAIT      88425850
                                BSC E DID NOT BRANCH 88425860
```


PROCESSOR-CONTROLLER FUNCTION TEST

0235	0	304D			DC			ERR ID + ERR WAIT	88425870
0236	00	4C08023A	*					BSC SKPD-SHOULD BRNCH	88425880
0238	0	304E	G201	BSC	L	G202,+		BR IF NOT PLUS	88425890
				DC		/304E		ERR ID + ERR WAIT	88425900
0239	0	304F	*					BSC - DID NOT BRANCH	88425910
				DC		/304F		ERR ID + ERR WAIT	88425920
023A	00	4C20023E	*					BSC SKPD-SHOULD BRNCH	88425930
023C	0	3050	G202	BSC	L	G203,Z			88425940
				DC		/305D		ERR ID + ERR WAIT	88425950
023D	0	3051	*					BSC Z DID NOT SKIP	88425960
				DC		/3051		ERR ID + ERR WAIT	88425970
023E	00	4C100241	*					BSC SKPD-SHOULD BRNCH	88425980
0240	0	7001	G203	BSC	L	V154,-		BR IF NOT MINUS	88425990
0241	0	3052		MOX		G204			88426000
				DC		/3052		ERR ID + ERR WAIT	88426010
0242	0	2003	*					BSC SKPD-SHOULD NOT	88426020
0243	00	4C020247	G204	LDS		3		SET C AND OF ON	88426030
0245	0	3053		BSC	L	G205,C		BR IF CARRY IS ON	88426040
				DC		/3053		ERR ID + ERR WAIT	88426050
0246	0	3054	*					BSC C DID NOT BRANCH	88426060
				DC		/3054		ERR ID + ERR WAIT	88426070
0247	00	4C010248	*					BSC SKPD-SHOULD BRNCH	88426080
0249	0	3055	G205	BSC	L	G208,C		BR IF OF ON	88426090
				DC		/3055		ERR ID + ERR WAIT	88426100
024A	0	3056	*					BSC O DID NOT BRANCH	88426110
				DC		/3056		ERR ID + ERR WAIT	88426120
0248	00	4C01024E	*					BSC SKPD-SHOULD BRNCH	88426130
024D	0	7001	G208	BSC	L	V168,D		BR IF OF ON	88426140
024E	0	3057		MDX		G206			88426150
				DC		/3057		ERR ID + ERR WAIT	88426160
024F	0	2000	*					BSC BRNCD-SHOULD NOT	88426170
0250	00	4C020253	G206	LDS		D			88426180
0252	0	7001		BSC	L	V170,C		BR IF CARRY IS OFF	88426190
0253	0	3058		MDX		G207			88426200
				DC		/3058		ERR ID + ERR WAIT	88426210
0254	00	4C010257	*					BSC BRNCD-SHOULD NOT	88426220
0256	0	7001	G207	BSC	L	V174,C		BR IF OF ON	88426230
0257	0	3059		MDX		G209			88426240
				DC		/3059		ERR ID + ERR WAIT	88426250
0258	0	C014	*					BSC BRNCD-SHOULD NOT	88426260
0259	00	4C18025D	G209	LD		N201			88426270
025B	0	305A		BSC	L	G20A,+		BR ON ZERO	88426280
				DC		/305A		ERR ID + ERR WAIT	88426290
025C	0	305B	*					BSC +- DID NOT BRANCH	88426300
				DC		/305B		ERR ID + ERR WAIT	88426310
025D	0	C00E	*					BSC SKPD-SHOULD BRNCH	88426320
025E	00	4C180261	G20A	LD		N200			88426330
0260	0	7001		BSC	L	V180,+			88426340
0261	0	305C		MDX		G200			88426350
				DC		/305C		ERR ID + ERR WAIT	88426360
0262	0	C008	*					BSC BRNCHED-SHOULDNT	88426370
0263	00	4C180266	G20D	LO		N202			88426380
0265	0	7001		BSC	L	V184,+			88426390
0266	0	305D		MDX		G208			88426400

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PROCESSOR-CONTROLLER FUNCTION TEST

***** 8SI *****										88426550
* ,										88426560
*****										88426570
*****										88426580
CORE	DATA OR	*LA- OPER-							88426590	
ADDR	INSTRUCTION	*BEL ATION	FT	OPERANDS	+	REMARKS	ID+SEQ=	AT RIGHT	88426600	
*****										88426610
0270	D	4002	A240	BSI	N241	STORE ADDRESS CF I REG			88426620	
0271	U	D271	N240	DC	N240	STORE ADDRESS CF I REG			88426630	
0272	0	306D		DC	/3060	ERR ID + ERR WAIT			88426640	
*****										88426650
0273	0	0000	N241	DC	/0000	BSI SKPC-SHOULD BRNCH			88426660	
0274	0	C0FE		LO	N241	RETURN ADDR FOR MAIN PROG			88426670	
0275	0	F0FB		EOR	N240	LD RETURN ADDR			88426680	
0276	0	4820		BSC	Z	ZERO IN RETURN ADDR			88426690	
0277	0	3061		DC	/3061	ERR ID + ERR WAIT			88426700	
*****										88426710
0278	00	4408027D		BSI	L N243, +	BSI NOT STORED I REG			88426720	
027A	0	3062	V1AC	DC	/3062	STORE ADDR OF I REG			88426730	
*****										88426740
027B	0	3063		DC	/3063	ERR ID + ERR WAIT			88426750	
*****										88426760
027C	0	027A	N242	DC	V1AC	BSI SKPC-SHOULD BRNCH			88426770	
027D	0	0000	N243	DC	/0000	RETURN ADDR FOR MAIN PROG			88426780	
027E	0	C0FE		LO	N243				88426790	
027F	0	F0FC		EOR	N242				88426800	
0280	0	4820		BSC	Z				88426810	
0281	0	3064		DC	/3064	ERR ID + ERR WAIT			88426820	
*****										88426830
*****										88426840
*****										88426850
*****										88426860
*****										88426870
*****										88426880
0282	0	C04A	A900	LD	F911	LO A NUMBER			88426890	
0283	0	D04A		STO	F912				88426900	
0284	0	C04A		LD	F913				88426910	
0285	0	C048		LD	F912				88426920	
0286	0	F046		EOR	F911				88426930	
0287	0	4820		BSC	Z				88426940	
0288	0	3065		DC	/3065	ERR ID + ERR WAIT			88426950	
*****										88426960
0289	0	C049		LD	F918	STORE FAILED			88426970	
028A	0	4820		BSC	Z	CK FIRST PASS SW (10002)			88426980	
028B	0	704D		MDX	A280	IS SW ON			88426990	
028C	0	C044		LD	F916	YES GO TO NEXT ROUTINE			88427000	
028D	0	D045		STO	F918	GET 0002			88427010	
028E	0	1810		SRA	16	STORE /0002			88427020	
028F	00	D4000001		STO	L /0001	CLEAR ACC			88427030	
0291	0	61FF		LDX	1 -1	ZERO WITH /0001			88427040	
0292	00	C4000001		LD	L /0001	LD XR I WITH -1			88427050	
0294	0	4820		BSC	Z	ZERO IN 1800 -1 FOR 1130			88427060	
0295	0	7010		MDX	G901	ZERC FCR 1800			88427070	
0296	0	C030		LD	F919	1130 CPU			88427080	
0297	0	D033		STO	F903	1800 P-C LD /0240			88427090	
0298	00	D4000F81		STO	L N8C2	STO /0240 THIS IS AREA,				

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PROCESSOR-CONTROLLER FUNCTION TEST

```
*
*
02A5 0 70F8 MDX G902 SENSE/PROG SWS NOT 88427230
02A6 0 C02E LD F920 * EQUAL TO /FF00 88427240
02A7 0 D023 STO F903 REPEAT TEST 88427250
02A8 00 D4000F81 STO L N8C2 1130 CPU LD /3A00 88427260
02AA 00 D4000FED STO L F004 * AREA, FUNCTION + 88427270
02AC 00 D4000FF4 STO L F007 * MODIFIER FOR READING 88427280
02AE 0 0818 L900 XIO F902 * DATA ENTRY SWITCHES 88427290
02AF 0 C022 LD F917 TEST DATA ENTRY SWS 88427300
02B0 0 F01F EOR F915 * FOR /FFFF 88427310
02B1 00 4C1802B6 BSC L X001,+ BRANCH ON ZERO 88427320
02B3 0 F01C FOR F915 88427330
02B4 0 3067 DC /3067 ERR ID + ERR WAIT 88427340
* DATA ENTRY SWS NOT 88427350
* EQUAL TO /FFFF 88427360
02B5 0 70F8 MDX G900 88427370
02B6 0 3001 X001 DC /3001 88427380
* SET SENSE/PROG AND 88427390
* DATA ENTRY SWS TO 88427400
* 88427410
* 88427420
02B7 0 C013 LD F903 88427430
02B8 0 F01C EOR F920 88427440
02B9 00 4C1802C1 BSC L G904,+ XFER IF 1130 88427450
02BA 0 081A XIO F922 TEST SENSE/PROG SWS 88427460
02BC 0 E01B AND F923 IGNORE CE SWS. (/FF00) 88427470
02BD 00 4C1802C1 BSC L G904,+ BRANCH IF CK 88427480
02BE 0 3068 DC /3068 ERR ID + ERR WAIT 88427490
* SENSE/PROG SWS NOT 88427500
* EQUAL TO /0000 88427510
02C0 0 70FA MDX G903 REPEAT TEST 88427520
02C1 0 0808 L904 XIO F902 TEST DATA ENTRY SWS 88427530
02C2 0 C00F LD F917 * FOR /0000 88427540
02C3 00 4C1802C7 BSC L X003,+ BRANCH ON ZERO 88427550
02C5 0 3069 DC /3069 ERR ID + ERR WAIT 88427560
* DATA ENTRY SWITCHES 88427570
* NCT EQ /0000 88427580
02C6 0 70FA MDX G904 88427590
02C7 0 3002 X003 DC /3002 SET 8 BIT SWITCHES AS 88427600
* * DESIRED FOR RUN 88427610
* * AND PUSH START 88427620
* EXIT TO NEXT ROUTINE 88427630
02C8 0 7010 MDX A280 88427640
02CA 0000 BSS E 88427650
02CB 0 0202 F902 DC F917 88427660
02CC 0 0240 F903 DC /0240 EQUAL /3A00 IN 1130 88427670
02CD 0 020C F904 DC F904 88427680
02CE 0 02CE F911 DC F912 88427690
02CF 0 0000 F912 DC /0000 88427700
02D0 0 111F F913 DC /0000 88427710
02D1 0 0002 F916 DC /0002 88427720
02D2 0 0000 F917 DC /0000 88427730
02D3 0 0000 F918 DC /0000 88427740
02D4 0 0240 F919 DC /0240 1800 READ 8 BIT SWS CONSTANT 88427750
02D5 0 3A00 F920 DC /3A00 1130 READ 8 BIT SWS CONSTANT 88427760
02D6 0 0000 F922 DC 0 SENSE SENSE/PROG CON 88427770
02D7 0 0760 DC /0760 88427780
02D8 0 FF00 F923 DC /FF00 88427790
*****
* BEGINING OF SECTION OF 88427800
* PROGRAM USING COMMON ERROR 88427810
* CONTROL ROUTINE 88427820
* 88427830
* 88427840
* 88427850
* 88427860
* 88427870
* 88427880
* 88427890
* 88427900
* TEST OF SRA OPERATION
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
*
*
*****
CORE DATA OR *LA- OPER- 88427910
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88427920
***** 88427930
02D9 0 C039 A280 LD N280 88427940
02DA 0 1810 SRA 16 88427950
02DB 00 4C1802E0 BSC L G280,+ BRANCH ON ZERO 88427960
02DD 00 44000F83 BSI L F000 SRA 16 FAILED 88427970
02DF 0 306A DC /306A ERR ID 88427980
02E0 00 44000FDE G280 BSI L F005 CK LOCK ON ERROR 88427990
02E2 0 70F6 MDX A280 LOOP 88428000
***** 88428010
02E3 0 C030 A281 LO N281 LD /8000 88428020
02E4 0 180F SRA 15 NOW A=/0001 88428030
02E5 0 F02F EOR N282 ZERO WITH /0001 88428040
02E6 00 4C1802E8 BSC L G281,+ BRANCH ON ZERO 88428050
02E8 00 44000F85 BSI L F000 SRA 15 FAILED 88428060
02EA 0 306B DC /306B ERR ID 88428070
02EB 00 44000FDE G281 BSI L F005 CK LOCK ON ERROR 88428080
02ED 0 70F5 MDX A281 LOOP 88428090
***** 88428100
02EE 0 C027 A282 LD N283 LD /AAAA 88428110
02EF 0 1801 SRA 1 NOW A=/5555 88428120
02F0 0 F026 FOR N284 ZFRC WITH /5555 88428130
02F1 00 4C1802F6 BSC L G282,+ BRANCH ON ZERO 88428140
02F3 00 44000F83 BSI L F000 SRA 1 FAILED 88428150
02F5 0 306C DC /306C ERR ID 88428160
02F6 00 44000F0E G282 BSI L F005 CK LOCK ON ERROR 88428170
02F8 0 70F5 MDX A282 LOOP 88428180
***** 88428190
02F9 0 C01D A283 LO N284 LO /5555 88428200
02FA 0 1801 SRA 1 NOW A=/2AAA 88428210
02FB 0 F01C EOR N285 ZERO WITH /2AAA 88428220
02FC 00 4C180301 BSC L G283,+ BRANCH ON ZERO 88428230
02FE 00 44000F83 BSI L F000 SRA 1 FAILED 88428240
0300 0 306D DC /306D ERR ID 88428250
0301 00 44000FDE G283 BSI L F005 CK LOCK ON ERROR 88428260
0303 0 70F5 MDX A283 LOOP 88428270
***** 88428280
0304 0 C00F A284 LD N281 LD /8000 88428290
0305 0 1801 SRA 1 NOW A=/4000 88428300
0306 0 1802 SRA 2 A=/1000 88428310
0307 0 1804 SRA 4 A=/0100 88428320
0308 0 1808 SRA 8 A=/0001 88428330
0309 0 F008 EOR N282 ZERO WITH /0001 88428340
030A 00 4C18030F BSC L G284,+ BRANCH ON ZERO 88428350
030C 00 44000F83 BSI L F000 COMB SRA FAILED 88428360
030E 0 306E DC /306E ERR ID 88428370
030F 00 44000FDE G284 BSI L F005 CK LOCK ON ERROR 88428380
0311 0 70F2 MDX A284 LOOP 88428390
0312 0 7006 MDX A2C0 EXIT TO NEXT ROUTINE 88428400
0313 0 FFFF N280 DC /FFFF 88428410
0314 0 8000 N281 DC /8000 88428420
0315 0 0001 N282 DC /0001 88428430
0316 0 AAAA N283 DC /AAAA 88428440
0317 0 5555 N284 DC /5555 88428450
0318 0 2AAA N285 DC /2AAA 88428460
* 88428470
* 88428480
* 88428490
* 88428500
* TEST OF AND FUNCTION 88428510
***** 88428520
CORE DATA OR *LA- OPER- 88428530
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88428540
***** 88428550
0319 0 C029 A2C0 LD N2C0 LD /0000 88428560
***** 88428570
***** 88428580
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
031A 0 E02B      ANO      N2C0      ANO /0000      8B42B590
031B 00 4C1B032U  BSC L  G2C0,+-  BRANCH ON ZERO  8B42B600
031D 00 44000FB3  BSI L  F000      AND OF 0 AND FAILED  8B42B610
031F 0 306F      DC      /306F      ERR ID      8B42B620
0320 00 44000FDE  G2C0  BSI L  F005      CK LOCK ON ERROR  8B42B630
0322 0 70F6      MOX      A2C0      LOOP      8B42B640
*****
0323 0 C01F      A2C4  LD      N2C0      LO /0000      8B42B650
0324 0 E01F      AND      N2C2      LO /FFFF      8B42B660
0325 00 4C1B032A  BSC L  G2C4,+-  BRANCH ON ZERO  8B42B670
0327 00 44000FB3  BSI L  F000      ANO OF 0 AND 1 FAILED  8B42B680
0329 0 3070      DC      /3070      ERR ID      8B42B690
032A 00 44000FDE  G2C4  BSI L  F005      CK LOCK ON ERROR  8B42B700
032C 0 70F6      MDX      A2C4      LOOP      8B42B710
*****
032D 0 C016      A2C8  LD      N2C2      LO /FFFF      8B42B720
032E 0 E014      AND      N2C0      ANO /0000      8B42B730
032F 00 4C1B0334  BSC L  G2C8,+-  BRANCH ON ZERO  8B42B740
0331 00 44000FB3  BSI L  F000      AND OF 1 AND 0 FAILED  8B42B750
0333 0 3071      DC      /3071      ERR ID      8B42B760
0334 00 44000F0E  G2C8  BSI L  F005      CK LOCK ON ERROR  8B42B770
0336 0 70F6      MDX      A2C8      LOOP      8B42B780
*****
0337 0 C00C      A2CC  LD      N2C2      LD /FFFF      8B42B790
0338 0 E00B      ANO      N2C2      ANO /FFFF      8B42B800
0339 0 F00A      EOR      N2C2      ZERO WITH /FFFF  8B42B810
033A 00 4C1B033F  BSC L  G2CC,+-  BRANCH ON ZERO  8B42B820
033C 00 44000FB3  BSI L  F000      ANO OF 1 AND 1 FAILED  8B42B830
033E 0 3072      DC      /3072      ERR ID      8B42B840
033F 00 44000FDE  G2CC  BSI L  F005      CK LOCK ON ERROR  8B42B850
0341 0 70F5      MOX      A2CC      LOOP      8B42B860
0342 0 7002      MOX      A300      EXIT TO NEXT ROUTINE  8B42B870
0343 0 0000      N2C0  DC      /0000      8B42B880
0344 0 FFFF      N2C2  OC      /FFFF      8B42B890
*****
* TEST OF OR FUNCTION
*****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANOS + REMARKS IO+SEQ= AT RIGHT
*****
0345 0 C020      A300  LD      N300      LD /0000      8B42B900
0346 0 E81F      OR      N300      OR /0000      8B42B910
0347 00 4C1B034C  BSC L  G300,+-  BRANCH ON ZERO  8B42B920
0349 00 44000FB3  BSI L  F000      OR OF 0 AND 0 FAILED  8B42B930
034B 0 3073      DC      /3073      ERR ID      8B42B940
034C 00 44000FDE  G300  BSI L  F005      CK LOCK ON ERROR  8B42B950
034E 0 70F6      MOX      A300      LOOP      8B42B960
*****
034F 0 C016      A302  LD      N300      LD /0000      8B42B970
0350 0 E816      OR      N302      OR /FFFF      8B42B980
0351 0 F015      EOR      N302      ZERO WITH /FFFF  8B42B990
0352 00 4C1B0357  BSC L  G302,+-  BRANCH ON ZERO  8B429000
0354 00 44000FB3  BSI L  F000      OR OF 0 AND 1 FAILED  8B429010
0356 0 3074      DC      /3074      ERR ID      8B429020
0357 00 44000F0E  G302  BSI L  F005      CHECK LOOP SWITCH  8B429030
0359 0 70F5      MOX      A302      LOOP      8B429040
*****
035A 0 C00C      A304  LD      N302      LD /FFFF      8B429050
035B 0 E80B      JR      N302      OR /FFFF      8B429060
035C 0 F00A      EOR      N302      EOR IN /FFFF      8B429070
035D 00 4C1B0362  BSC L  G304,+-  BRANCH ON ZERO  8B429080
035F 00 44000FB3  BSI L  F000      OR OF 1 AND 1 FAILED  8B429090
0361 0 3075      DC      /3075      ERR ID      8B429100
0362 00 44000FDE  G304  BSI L  F005      CK LOCK ON ERROR  8B429110
0364 0 70F5      MDX      A304      LOOP      8B429120
0365 0 7002      MDX      A240      EXIT TO NEXT ROUTINE  8B429130
*****
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
0366 0 0000      N300  OC      /0000      8B429270
0367 0 FFFF      N302  DC      /FFFF      8B429280
*****
* TEST OF RTE 16 OPERATION
*****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANOS + REMARKS IO+SEQ= AT RIGHT
*****
0368 0 C016      A340  LO      N340      LO /0000      8B429290
0369 0 1800      RTE      16      PLACE /0000 IN Q REG  8B429300
036A 0 C015      LO      N341      LO /FFFF      8B429310
036B 0 1800      RTE      16      NOW A=/0000 Q=/FFFF  8B429320
036C 00 4C1B0371  BSC L  G340,+-  BRANCH ON ZERO  8B429330
036E 00 44000FB3  BSI L  F000      ALL 0 THRU Q FAILED  8B429340
0370 0 3076      OC      /3076      ERR ID      8B429350
0371 00 44000FB2  G340  BSI L  F00E      CK LOCK ON ERROR  8B429360
0373 0 70F4      MOX      A340      LOOP      8B429370
0374 0 1800      RTE      16      NOW A=/FFFF Q=/0000  8B429380
0375 0 F00A      EOR      N341      ZERO WITH /FFFF  8B429390
0376 00 4C1B0378  BSC L  G342,+-  BRANCH ON ZERO  8B429400
0378 00 44000FB3  BSI L  F000      ALL 1 THRU Q FAILED  8B429410
037A 0 3077      DC      /3077      ERR ID      8B429420
037B 00 44000FDE  G342  BSI L  F005      CK LOCK ON ERROR  8B429430
037C 0 70EA      MDX      A340      LOOP      8B429440
037E 0 7002      MDX      A380      EXIT TO NEXT ROUTINE  8B429450
037F 0 0000      N340  OC      /0000      8B429460
0380 0 FFFF      N341  OC      /FFFF      8B429470
*****
* TEST OF SRT OPERATION
*****
*****
0381 0 C055      A380  LO      N380      LD /8000      8B429480
0382 0 18A0      SRT      32      NOW A=/FFFF Q=/FFFF  8B429490
0383 0 F054      EOR      N381      EOR IN /FFFF      8B429500
0384 00 4C1B0389  BSC L  G380,+-  BRANCH ON ZERO  8B429510
0386 00 44000FB3  BSI L  F000      SRT 32-A REG FAILED  8B429520
0388 0 3078      OC      /3078      ERR ID      8B429530
0389 00 44000FB2  G380  BSI L  F00E      CK LOCK ON ERROR  8B429540
038B 0 70F5      MDX      A380      LOOP      8B429550
038C 0 1800      RTE      16      NOW A=/FFFF Q=/0000  8B429560
038D 0 F04A      EOR      N381      EOR IN /FFFF      8B429570
038E 00 4C1B0393  BSC L  G382,+-  BRANCH ON ZERO  8B429580
0390 00 44000FB3  BSI L  F000      SRT 32-Q REG FAILED  8B429590
0392 0 3079      DC      /3079      ERR ID      8B429600
0393 00 44000F0E  G382  BSI L  F005      CK LOCK ON ERROR  8B429610
0395 0 70EB      MOX      A380      LOOP      8B429620
*****
0396 0 C042      A384  LD      N382      LD /4000      8B429630
0397 0 18A0      SRT      32      NOW A=/0000 Q=/0000  8B429640
0398 00 4C1B039D  BSC L  G384,+-  BRANCH ON ZERO  8B429650
039A 00 44000FB3  BSI L  F000      SRT 32-A REG FAILED  8B429660
039C 0 307A      OC      /307A      ERR ID      8B429670
039D 00 44000F0E  G384  BSI L  F005      CK LOCK ON ERROR  8B429680
039F 0 70F6      MOX      A384      LOOP      8B429690
03A0 0 1800      RTE      16      NOW A=/0000 Q=/0000  8B429700
03A1 00 4C1B03A6  BSC L  G386,+-  BRANCH ON ZERO  8B429710
03A3 00 44000FB3  BSI L  F000      SRT 32-Q REG FAILED  8B429720
03A5 0 307B      OC      /307B      ERR ID      8B429730
03A6 00 44000F0E  G386  BSI L  F005      CK LOCK ON ERROR  8B429740
03A8 0 70ED      MDX      A384      LOOP      8B429750
*****
03A9 0 C030      A38B  LD      N383      LO /5555      8B429760
03AA 0 188F      SRT      15      NOW A=/0000 Q=/0000  8B429770
03AB 00 4C1B03BU  BSC L  G388,+-  BRANCH ON ZERO  8B429780
03AD 00 44000FB3  BSI L  F000      SRT 15-A REG FAILED  8B429790
03AF 0 307C      OC      /307C      ERR ID      8B429800
*****
```



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PROCESSOR-CONTROLLER FUNCTION TEST

```
0380 00 44000F82 G38B BSI L F00E CK LOCK ON ERROR
0382 0 70F6 MOX A388 LOOP
0383 0 1800 RTE 16 NOW A=/AAAA Q=/0000
0384 0 F026 EOR N384 ZERC WITH /AAAA
0385 00 4C1803BA BSC L G38A,+ BRANCH ON ZERO
0387 00 44000F83 BSI L F000 SRT 15-Q REG FAILED
0389 0 3070 OC /3070 ERR ID
038A 00 44000FDE G38A BSI L F005 CK LOCK ON ERROR
038C 0 70EC MDX A388 LOOP
```

```
*****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
038D 0 C01C A38C LD N383 LD /5555
038E 0 1880 SRT 0 NOW A=/5555 Q=/0000
038F 0 1882 SRT 2 NOW A=/1555 Q=/4000
03C0 0 1884 SRT 4 /0155 /5400
03C1 0 1886 SRT 6 /0005 /5530
03C2 0 1888 SRT 8 /0000 /0555
03C3 0 188A SRT 10 /0000 /0001
03C4 00 4C1803C9 BSC L G38C,+ BRANCH ON ZERO
03C6 00 44000F83 BSI L F000 SERIES SRT FAILED
03C8 0 307E DC /307E ERR ID
03C9 00 44000F82 G38C BSI L F00E CK LOCK ON ERROR
03CB 0 70F1 MOX A38C LOOP
03CC 0 18D0 RTE 16 NOW A=/0001 Q=/0000
03CD 0 F00E EOR N385 ZERC WITH /0001
03CE 00 4C1803D3 BSC L G38E,+ BRANCH ON ZERO
03D0 00 44000F83 BSI L F000 SERIES SRT FAILED
03D2 0 307F OC /307F ERR ID
03D3 00 44000FDE G38E BSI L F005 CK LOCK ON ERROR
03D5 0 70E7 MDX A38C LOOP
03D6 0 7006 MDX A3C0 EXIT TO NEXT ROUTINE
03D7 0 8000 N380 DC /8000
03D8 0 FFFF N361 DC /FFFF
03D9 0 4000 N382 DC /4000
03DA 0 5555 N383 DC /5555
03DB 0 AAAA N384 DC /AAAA
03DC 0 0001 N385 DC /0001
*
* TEST OF RTE OPERATION
*
*****
03DD 0 C035 A3C0 LO N3C1 LD /AAAA
03DE 0 18D0 RTE 16 NOW A=/0000 Q=/AAAA
03DF 0 C032 LD N3C0 NOW A=/5555 Q=/AAAA
03E0 0 18CF RTE 15 NOW A=/5554 Q=/AAAB
03E1 0 F034 EOR N3C4 ZERC WITH /5554
03E2 00 4C1803E7 BSC L G3C0,+ BRANCH ON ZERO
03E4 00 44000F83 BSI L F000 RTE 15-Q TO A FAILED
03E6 0 3080 OC /3080 ERR ID
03E7 00 44000F82 G3C0 BSI L F00E CK LOCK ON ERROR
03E9 0 70F3 MDX A3C0 LOOP
03EA 0 18D0 RTE 16 NOW A=/AAAB Q=/5554
03EB 0 F028 EOR N3C5 ZERC WITH /AAAB
03EC 00 4C1803F1 BSC L G3C2,+ BRANCH ON ZERO
03EE 00 44000F83 BSI L F000 RTE 15-A TO Q FAILED
03F0 0 3081 OC /3081 ERR ID
03F1 00 44000FDE G3C2 BSI L F005 CK LOCK ON ERROR
03F3 0 70E9 MDX A3C0 LOOP
*****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
03F4 0 C020 A3C4 LO N3C3 LD /8000
03F5 0 18D0 RTE 16 NOW A=/XXXX Q=/8000
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
03F6 0 C010 LO N3C2 LD /0000
03F7 0 18C0 RTE 0 NOW A=/0000 Q=/8000
03F8 0 18C1 RTE 1 /0000 /4000
03F9 0 18C2 RTE 2 /0000 /1000
03FA 0 18C3 RTE 3 /0000 /0200
03FB 0 18C4 RTE 4 /0000 /0020
03FC 0 18C5 RTE 5 /0000 /0002
03FD 0 18C6 RTE 6 /0400 /0000
03FE 0 18CA RTE 10 /0001 /0000
03FF 0 F018 EOR N3C6 ZERC WITH /0001
0400 00 4C180405 BSC L G3C4,+ BRANCH ON ZERO
0402 00 44000F83 BSI L F000 SERIES RTE FAILED
0404 0 3082 DC /3082 ERR ID
0405 00 44000F82 G3C4 BSI L F00E CK LOCK ON ERROR
0407 0 70EC MDX A3C4 LOOP
0408 0 1800 RTE 16 NOW A=/0000 Q=/0001
0409 00 4C18040E BSC L G3C6,+ BRANCH ON ZERO
040B 00 44000F83 BSI L F000 SERIES RTE FAILED
040D 0 3083 OC /3083 ERR ID
040E 00 44000FDE G3C6 BSI L F005 CK LOCK ON ERROR
0410 0 70E3 MDX A3C4 LOOP
0411 0 7007 MDX A400 EXIT TO NEXT ROUTINE
0412 0 5555 N3C0 DC /5555
0413 0 AAAA N3C1 OC /AAAA
0414 0 0000 N3C2 DC /0000
0415 0 8000 N3C3 OC /8000
0416 0 5554 N3C4 DC /5554
0417 0 AAA8 N3C5 OC /AAAB
0418 0 0001 N3C6 OC /0001
*
* TEST OF SLA OPERATION
*
*****
0419 00 C40004BD A400 LO L N400 LD /FFFF
041B 0 18D0 RTE 16 NOW A=/XXXX Q=/FFFF
041C 00 C40004BD LD L N400 LD /FFFF
041E 0 1010 SLA 16 NOW A=/0000 Q=/FFFF
041F 00 4C020424 BSC L G404,+ BR ON CARRY
0421 00 44000F83 BSI L F000 SLA 16-CARRY FAILED
0423 0 3085 DC /3085 ERR ID
0424 00 44000F82 G404 BSI L F00E CK LOCK ON ERROR
0426 0 70F2 MDX A400 LOOP
0427 00 4C18042C BSC L G400,+ BRANCH ON ZERO
0429 00 44000F83 BSI L F000 SLA 16-A REG FAILED
042B 0 3084 DC /3084 ERR ID
042C 00 44000F82 G400 BSI L F00E CK LOCK ON ERROR
042E 0 70EA MDX A400 LOOP
042F 0 18D0 RTE 16 NOW A=/FFFF Q=/0000
0430 00 F400048D EOR L N400 ZERC WITH /FFFF
0432 00 4C180437 BSC L G406,+ BRANCH ON ZERO
0434 00 44000F83 BSI L F000 SLA 16-AFFECTED Q REG
0436 0 3086 OC /3086 ERR ID
0437 00 44000FDE G406 BSI L F005 CK LOCK ON ERROR
0439 0 70DF MDX A400 LOOP
*****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
043A 00 C40004C2 A408 LD L N405 LD /0000
043C 0 18D0 RTE 16 NOW A=/XXXX Q=/0000
043D 00 C40004C3 LD L N406 /FFFF /0000
043F 0 1010 SLA 16 /0000 /0000
0440 00 4C020443 BSC L G407,+ BR ON CARRY
0442 0 7003 MDX G40C
0443 00 44000F83 G407 BSI L F000 SLA 16- CARRY FAILED
0445 0 3088 DC /3088 ERR ID
0446 00 44000F82 G40C BSI L F00E CK LOCK ON ERROR
```

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```
0448 0 70F1 MDX A408 LOOP 88431310
0449 00 4C18044E BSC L G408,+ BRANCH ON ZERO 88431320
044B 00 44000F83 BSI L F000 SLA 16-A REG FAILED 88431330
044D 0 3087 DC /3087 ERR 10 88431340
044E 00 44000FB2 G40B BSI L F00E CK LOCK ON ERROR 88431350
0450 0 70E9 MDX A408 LOOP 88431360
0451 0 1800 RTE 16 NOW A=/0000 Q=/0000 88431370
0452 00 4C180457 BSC L G40E,+ BRANCH ON ZERO 88431380
0454 00 44000F83 BSI L F000 SLA 16-AFFECTED Q REG 88431390
0456 0 3089 OC /3089 EPR 10 88431400
0457 00 44000FDE G40E BSI L F005 CK LOCK ON ERROR 88431410
0459 0 70E0 MDX A408 LOOP 88431420
*****
045A 0 C067 B400 LD N405 LD /0000 88431430
0458 0 1800 RTE 15 NOW A=/XXXX Q=/0000 88431440
045C 0 C063 LO N403 LO /AAAA 88431450
045D 0 1001 SLA 1 NOW A=/5554 Q=/0000 88431460
045E 00 4C020463 BSC L H402,C BRANCH ON CARRY 88431470
0460 00 44000F83 BSI L F000 SLA 1-CARRY FAILED 88431480
0462 0 3088 OC /3088 ERR 10 88431490
0463 00 44000F82 H402 BSI L F00E CK LOCK ON ERROR 88431500
0465 0 70F4 MDX 8400 LOOP 88431510
0466 0 F05A EOR N404 ZERO WITH /5554 88431520
0467 00 4C18046C BSC L H400,+ BRANCH ON ZERO 88431530
0469 00 44000FB3 BSI L F000 SLA 1-A REG FAILED 88431540
0468 0 308A DC /308A ERR 10 88431550
046C 00 44000F82 H400 BSI L F00E CK LOCK ON ERROR 88431560
046E 0 70E8 MDX 8400 LOOP 88431570
046F 0 1800 RTE 16 NOW A=/0000 Q=/5554 88431580
0470 00 4C180475 BSC L H404,+ BRANCH ON ZERO 88431590
0472 00 44000F83 BSI L F000 SLA 1-AFFECTED Q REG 88431600
0474 0 308C DC /308C ERR 10 88431610
0475 00 44000F0E H404 BSI L F005 CK LOCK ON ERROR 88431620
0477 0 70E2 MDX 8400 LOOP 88431630
*****
0478 0 C049 B406 LO N405 LO /0000 88431640
0479 0 18DD RTE 16 NOW A=/XXXX Q=/0000 88431650
047A 0 C044 LD N402 LD /5555 88431660
047B 0 1001 SLA 1 NOW A=/AAAA Q=/0000 88431670
047C 00 4C02047F BSC L H407,C BR ON CARRY 88431680
047E 0 7003 MDX H405 88431690
047F 00 44000F83 H407 BSI L F000 SLA 1-CARRY FAILED 88431700
0481 0 308E DC /308E ERR 10 88431710
0482 00 44000F82 H405 BSI L F00E CK LOCK ON ERROR 88431720
0484 0 70F3 MDX 8406 LOOP 88431730
0485 0 F03A EOR N403 ZERO WITH /AAAA 88431740
0486 00 4C180468 BSC L H406,+ BRANCH ON ZERO 88431750
0488 00 44000F83 BSI L F000 SLA 1-A REG FAILED 88431760
048A 0 30BD DC /30BD ERR 10 88431770
0488 00 44000FB2 H406 BSI L F00E CK LOCK ON ERROR 88431780
048D 0 70EA MDX 8406 LOOP 88431790
048E 0 18DD RTE 16 NOW A=/0000 Q=/AAAA 88431800
048F 00 4C180494 BSC L H408,+ BRANCH ON ZERO 88431810
0491 00 44000F83 BSI L F000 SLA 1-AFFECTED Q REG 88431820
0493 0 308F DC /308F ERR 10 88431830
0494 00 44000F0E H408 BSI L F005 CK LOCK ON ERROR 88431840
0496 0 70E1 MDX 8406 LOOP 88431850
*****
*****
CORE DATA OR *LA- OPER-
A00R INSTRUCTION *BEL ATION FT OPERANOS + REMARKS IO+SEQ= AT RIGHT
*****
0497 0 C02A B40A LD N405 LO /0000 88431860
0498 0 1800 RTE 16 NOW A=/XXXX Q=/0000 88431870
0499 0 C024 LO N401 LO /0001 88431880
049A 0 6101 LOX 1 1 88431890
049B 0 6204 LOX 2 4 88431900
049C 0 6303 LOX 3 3 88431910
88431920
88431930
88431940
88431950
88431960
88431970
88431980
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
0490 0 1000 SLA 0 NOW A=/0001 Q=/0000 88431990
049E 0 1100 SLA 1 0 /0002 /0000 88432000
049F 0 1002 SLA 2 /0008 /0000 88432010
04A0 0 1200 SLA 2 0 /0080 /0000 88432020
04A1 0 1006 SLA 6 /2000 /0000 88432030
04A2 0 1300 SLA 3 0 /0000 /0000 88432040
04A3 00 4C0204AB BSC L H400,C BRANCH ON CARRY 88432050
04A5 00 44000F83 BSI L F000 COMB SLA-CARRY FAILED 88432060
04A7 0 3091 OC /3091 ERR 10 88432070
04A8 00 44000FB2 H40D BSI L F00E CK LOCK ON ERROR 88432080
04AA 0 70EC MOX 840A LOOP 88432090
04AB 00 4C180480 BSC L H40A,+ BRANCH ON ZERO 88432100
04AD 00 44000F83 BSI L F000 COMB SLA-A REG FAILED 88432110
04AF 0 3090 OC /3090 ERR 10 88432120
04B0 00 44000F82 H40A BSI L F00E CK LOCK ON ERROR 88432130
04B2 0 70E4 MOX 840A LOOP 88432140
04B3 0 1800 RTE 16 88432150
04B4 00 4C180489 BSC L H40E,+ BRANCH ON ZERO 88432160
04B6 00 44000F83 BSI L F000 COMB SLA-AFFECTED Q 88432170
04B8 0 3092 DC /3092 ERR 10 88432180
04B9 00 44000F0E H40E BSI L F005 CK LOCK ON ERROR 88432190
04BB 0 70DB MDX 840A LOOP 88432200
04BC 0 7007 MOX A440 EXIT TO NEXT ROUTINE 88432210
04BD 0 FFFF N400 DC /FFFF 88432220
04BE 0 0001 N401 DC /0001 88432230
04BF 0 5555 N402 DC /5555 88432240
04C0 0 AAAA N403 DC /AAAA 88432250
04C1 0 5554 N404 DC /5554 88432260
04C2 0 0000 N405 DC /0000 88432270
04C3 0 FFFE N406 DC /FFFF 88432280
*
* TEST OF SLT OPERATION
*
*****
04C4 0 C07E A440 LD N440 LD /0001 88432320
04C5 0 18DD RTE 16 NOW A=/XXXX Q=Q=/0001 88432330
04C6 0 C070 LD N441 LO /0000 88432340
04C7 0 10A0 SLT 32 /0000 Q=/0000 88432350
04C8 00 4C0204C0 BSC L G442,C BRANCH ON CARRY 88432360
04CA 00 44000F83 BSI L F000 SLT 32-CARRY FAILED 88432370
04CC 0 3094 DC /3094 ERR 10 88432380
04CD 00 44000F82 G442 BSI L F00E CK LOCK ON ERROR 88432390
04CF 0 70F4 MOX A440 LOOP 88432400
04D0 00 4C1804D5 BSC L G440,+ BRANCH ON ZERO 88432410
04D2 00 44000F83 BSI L F000 SLT 32-A REG FAILED 88432420
04D4 0 3093 OC /3093 ERR 10 88432430
04D5 00 44000F82 G440 BSI L F00E CK LOCK ON ERROR 88432440
04D7 0 70EC MDX A440 LOOP 88432450
04D8 0 18DD RTE 16 NOW A=/0000 Q=/0000 88432460
04D9 00 4C18040E BSC L G443,+ BRANCH ON ZERO 88432470
04DB 00 44000F83 BSI L F000 SLT 32-Q REG FAILED 88432480
04DD 0 3095 OC /3095 ERR 10 88432490
04DE 00 44000F0E G443 BSI L F005 CK LOCK ON ERROR 88432500
04E0 0 70E3 MOX A440 LOOP 88432510
*****
*****
CORE DATA OR *LA- OPER-
A00R INSTRUCTION *BEL ATION FT OPERANOS + REMARKS IO+SEQ= AT RIGHT
*****
04E1 0 C063 A444 LD N442 LO /FFFF 88432520
04E2 0 1800 RTE 16 NOW A=/XXXX Q=/FFFF 88432530
04E3 0 C060 LO N441 LO /0000 88432540
04E4 0 109D SLT 16 NOW A=/FFFF Q=/0000 88432550
04E5 00 4C0204E8 BSC L G446,C BR ON CARRY 88432560
04E7 0 7003 MOX G447 88432570
04E8 00 44000F83 G446 BSI L F000 SLT 16-CARRY FAILED 88432580
04EA 0 3097 DC /3097 ERR 10 88432590
04EB 00 44000F82 G447 BSI L F00E CK LOCK ON ERROR 88432600
88432610
88432620
88432630
88432640
88432650
88432660
```

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04ED	0	70F3		MDX	A444	LOOP
04EE	0	F056		EOR	N442	ZERO WITH /FFFF
04EF	00	4C1804F4		BSC	L G444,+	BRANCH ON ZERO
04F1	00	44000FB3		BSI	L F000	SLT 16-A REG FAILED
04F3	0	3096		OC	/3096	ERR IO
04F4	00	44000FB2	G444	BSI	L F00E	CK LOCK ON ERROR
04F6	0	70FA		MDX	A444	LOOP
04F7	0	1800		RTE	16	NDW A=/0000 Q=/0000
04F8	00	4C1804FD		BSC	L G448,+	BRANCH ON ZERO
04FA	00	44000FB3		BSI	L F000	SLT 16-Q REG FAILED
04FC	0	3098		DC	/3098	ERR IO
04FD	00	44000FDE	G448	BSI	L F005	CK LOCK ON ERROR
04FF	0	70E1		MDX	A444	LOOP

```

0500 0 C045          A44A LD      N443          LD /5555
0501 0 18D0          RTE      16              NOW A=/XXXX Q=/5555
0502 0 C041          LO       N441            /0000      /5555
0503 0 108F          SLT      15              /2AAA      /8000
0504 00 4C020507     BSC L   G44C,C          8R ON CARRY
0506 0 70U3          MDX      G44D
0507 00 44000F83     G44C BSI L   F000          SLT 15-CARRY FAILED
0509 0 309A          DC       /309A          ERR ID
050A 00 44000F82     G44D BSI L   F00E          CK LOCK ON ERROR
050C 0 70F3          MDX      A44A          LOOP
050D 0 F039          EOR      N444          ZERO WITH /2AAA
050E 00 4C180513     8SC L   G44A,+-          BRANCH ON ZERO
0510 00 44000F83     BSI L   F000          SLT 15-A REG FAILED
0512 0 3099          DC       /3099          ERR ID
0513 00 44000F82     G44A BSI L   F00E          CK LOCK ON ERROR
0515 0 70EA          MDX      A44A          LOOP
0516 0 1800          RTE      16              NOW A=/8000 G=/0000
0517 0 F030          EOR      N445          ZERO WITH /8000
0518 00 4C180510     BSC L   G44E,+-          BRANCH ON ZERO
051A 00 44000F83     BSI L   F000          SLT 15-Q REG FAILED
051C 0 3098          DC       /3098          ERR ID
051D 00 44000FDE     G44E BSI L   F005          CK LOCK ON ERROR
051F 0 70E1          MDX      A44A          LOOP

```

CORE	DATA OR	*LA- OPER-			ID=SEQ= AT RIGHT
ADDR	INSTRUCTION	RELATION	FT	OF HANDS	REMARKS
0520 0	C022	B440	LD	N440	LD /0001
0521 0	1800		RTE	16	NOW A=/XXXX C=/0001
0522 0	C021		LD	N441	LD /0000
0523 0	1080		SLT	0	NOW A=/0000 Q=/0001
0524 0	1081		SLT	1	/0000 /0002
0525 0	1085		SLT	5	/0000 /0040
0526 0	1087		SLT	7	/0000 /2000
0527 0	1089		SLT	9	/0040 /0000
0528 0	108A		SLT	10	/0000 /0000
0529 00	4C02052E		BSC	L H443,C	BR ON CARRY
052B 00	44000F83		BSI	L F000	COMB SLT-CARRY FAILED
052D 0	3090		OC	/3090	ERR ID
052E 00	44000F82	H443	BSI	L F00E	CK LOCK ON ERROR
0530 0	70EF		MDX	B440	LOOP
0531 00	4C180536		BSC	L H440,+	BRANCH ON ZERO
0533 00	44000F83		BSI	L F000	COMB SLT-A REG FAILE
0535 0	309C		OC	/309C	ERR ID
0536 00	44000F82	H440	BSI	L F00E	CK LOCK ON ERROR
0538 0	70E7		MOX	B440	LOOP
0539 0	1800		RTE	16	NOW A=/0000 Q=/0000
053A 00	4C18053F		BSC	L H444,+	BRANCH ON ZERO
053C 00	44000F83		BSI	L F000	COMB SLT-Q REG FAILE
053E 0	309E		DC	/309E	ERR ID
053F 00	44000FDE	H444	BSI	L F005	CK LOCK ON ERROR
0541 0	700E		MOX	B440	LOOP
0542 0	7006		MDX	A480	EXIT TO NEXT ROUTINE

PROCESSOR-CONTROLLER FUNCTION TEST

0543	0	0001	N440	DC	/0001	88433350
0544	0	0000	N441	DC	/0000	88433360
0545	0	FFFF	N442	DC	/FFFF	88433370
0546	0	5555	N443	DC	/5555	88433380
0547	0	2AAA	N444	DC	/2AAA	88433390
0548	0	8000	N445	DC	/8000	88433400

TEST OF STO OPERATION

```

0549 0 C019          A480 LD      N480          LD /0000
054A 0 001A          STO      N482          STO /0000
054B 0 C018          LD      N481          LD /FFFF
054C 0 C018          LD      N482          LD /0000
054D 00 4C180552     BSC L   G480,+-- BRANCH ON ZERO
054F 00 44000F83     BSI L   F000          STO ZEROS FAILED
0551 0 309F          DC       /309F          EPR ID
0552 0 44000FDE     G480 BSI L   F005          CK LOCK ON ERROR
0554 0 70F4          MDX      A480          LOOP
*****
0555 0 C00E          A482 LD      N481          LD /FFFF
0556 0 000E          STO      N482
0557 0 C00B          LD      N480          LD /0000
0558 0 C00C          LD      N482          LD /FFFF
0559 0 F00A          EDR      N481          ZERO WITH /FFFF
055A 00 4C18055F     BSC L   G482,+-- BRANCH ON ZERO
055C 00 44000F83     BSI L   F000          STO ONES FAILED
055E 0 30A0          DC       /30A0          ERR ID
055F 00 44000FDE     G482 BSI L   F005          CK LOCK ON ERROR
0561 0 70F3          MDX      A482          LOOP
0562 0 7003          MDX      A4C0          EXIT TO NEXT ROUTINE
0563 0 0000          N480 OC      /0000
0564 0 FFFF          N481 DC      /FFFF
0565 0 FFFF          N482 DC      /FFFF

```

TEST OF STS OPERATION

CORE	DATA OR	*LA-	OPER-
ADDR	INSTRUCTION	*BEL	ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
0566 0	2000	A4C0	LDS 0 SET C AND CF OFF
0567 0	2858	STS	N4C0
0568 0	C05A	LD	N4C0
0569 00	4C18056E	BSC L	G4C0,+--
0568 00	44000F83	BSI L	F000 STS FAILED TO STORE
056D 0	30A1	DC	/30A1 ERR ID
056E 00	44000F0C	G4C0 BSI L	F005 CK LOCK ON ERROR
0570 0	70F5	MDX	A4C0 LOOP

0571 0	C0FF	A4C2 LD	A4C2
0572 0	2003	LDS	3
0573 0	284F	STS	N4C0
0574 0	F0FC	EOR	A4C2
0575 00	4C18057A	BSC L	H4C3,+-- BRANCH ON ZERO
0577 00	44000F83	BSI L	F000 ACC GONE AFT LDS-STS
0579 0	30A3	DC	/30A3 ERR ID
057A 00	4C02057D	H4C3 BSC L	H4C2,C BR IF CARRY IS NO
057L 0	7003	MDX	G4C2
057D 00	44000F83	H4C2 BSI L	F000 STS NOT CLEAR CARRY
057F 0	30A2	DC	/30A2 ERR ID
0580 00	44000F82	G4C2 BSI L	F00E CK LOCK ON ERROR
0582 0	70EE	MDX	A4C2 LOOP
0583 00	4C010586	BSC L	H4C4,C BR IF CARRY IS ON
0585 0	7003	MDX	G4C4
0586 00	44000F83	H4C4 BSI L	F000 STS NOT CLEAR OVERFLW
0588 0	30A4	DC	/30A4 ERR ID

PROCESSOR-CONTROLLER FUNCTION TEST

```
0589 00 44000FB2 G4C4 BSI L F00E CK LOCK ON ERROR 88434030
058B 0 70E5 MOX A4C2 LOOP 88434040
058C 0 C036 LO N4C0 88434050
058D 0 F036 EOK N4C1 88434060
058E 00 4C180593 BSC L G4C6,+ BRANCH ON ZERO 88434070
0590 00 44000FB3 BSI L F000 STS FAILED TO STORE 88434080
0592 0 30A5 DC /30A5 ERR IO 88434090
0593 00 44000FDE G4C6 BSI L F005 CK LOCK ON ERROR 88434100
0595 0 700B MOX A4C2 LOOP 88434110
***** 88434120
0596 0 2002 A4CB LOS 2 SET C ON OF OFF 88434130
0597 0 282B STS N4C0 SET /0002 IN N4C0 88434140
0598 0 2B2C STS N4C2 SET /0002 IN N4C2 88434150
0599 0 C029 LO N4C0 LO /0002 88434160
059A 0 F02B EOR N4C3 ZERO WITH /0002 88434170
059B 00 4C1805A0 BSC L G4C8,+ BRANCH ON ZERO 88434180
059D 00 44000FB3 BSI L F000 STS FAILED TO STORE 88434190
059F 0 30A6 OC /30A6 ERR IO 8843420C
05A0 00 44000FB2 G4C8 BSI L F00E CK LOCK ON ERROR 88434210
05A2 0 70F3 MDX A4C8 LOOP 88434220
05A3 0 C021 LO N4C2 LD /0002 88434230
05A4 00 4C1805A9 BSC L G4CA,+ BRANCH ON ZERO 88434240
05A6 00 44000FB3 BSI L F000 STS NOT CLEAR CARRY 88434250
05A8 0 30A7 DC /30A7 ERR IO 88434260
05A9 00 44000FDE G4CA BSI L F005 CK LOCK ON ERROR 88434270
05AB 0 70EA MOX A4C8 LOOP 88434280
***** 88434290
05AC 0 2031 A4CC LDS 1 SET C-OFF OF - ON 88434300
05AD 0 2B15 STS N4C0 SET /0001 IN N4C0 88434310
05AE 0 2B16 STS N4C2 SET /0001 IN N4C2 88434320
05AF 0 C013 LO N4C0 LO /0001 88434330
05B0 0 F016 EOR N4C4 ZERO WITH /0001 88434340
05B1 00 4C1805B6 BSC L G4CC,+ BRANCH ON ZERO 88434350
05B3 00 44000FB3 BSI L F000 STS FAILED TO STORE 88434360
05B5 0 30AB OC /30AB ERR IO 88434370
05B6 00 44000FB2 G4CC BSI L F00E CK LOCK ON ERROR 88434380
05B8 0 70F3 MDX A4CC LOOP 88434390
05B9 0 C00B LO N4C2 LO /0001 88434400
05BA 00 4C1805BF BSC L G4CD,+ BRANCH ON ZERO 88434410
05BC 00 44000FB3 BSI L F000 STS NOT CLEAR OVERFL 88434420
05BE 0 30A9 OC /30A9 ERR IO 88434430
05BF 00 44000FDE G4CD BSI L F005 CK LOCK ON ERROR 88434440
05C1 0 70EA MOX A4CC LOOP 88434450
05C2 0 7005 MDX A500 EXIT TO NEXT ROUTINE 88434460
05C3 0 0003 N4C0 OC /0003 88434470
05C4 0 0003 N4C1 DC /0003 88434480
05C5 0 0000 N4C2 OC /0000 88434490
05C6 0 0002 N4C3 DC /0002 88434500
05C7 0 0001 N4C4 DC /0001 88434510
***** 88434520
* 88434530
* 88434540
* 88434550
***** 88434560
CORE DATA OR *LA- OPER- 88434570
ADDR INSTRUCTION *BEL ATION FT OPERANOS + REMARKS IO+SEQ= AT RIGHT 88434580
***** 88434590
05CB 0 2003 A500 LDS 3 SET C AND OF ON 88434600
05C9 00 4400065C LO L N500 LO /8001 88434610
05CB 0 4B2F BSC O+EZC SK IF OF OFF, PLUS, EVEN, 88434620
* * ZERO OR CARRY OFF. 88434630
05CC 0 7003 MOX G500 88434640
05CD 00 44000FB3 BSI L F000 BSC SKPD-SHOULD NOT 88434650
05CF 0 30AA DC /30AA ERR IO 88434660
05D0 00 44000FDE G500 BSI L F005 CK LOCK ON ERROR 88434670
05D2 0 70F5 MOX A500 LOOP 88434680
***** 88434690
05D3 0 2003 A502 LDS 3 SET C + OF ON 88434700
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PROCESSOR-CONTROLLER FUNCTION TEST

```
0504 00 44000650 LD L N501 LO /0000 88434710
0506 0 481B BSC -OC+ SK IF MINUS, OF OFF, CARRY 88434720
* *OFF OR PLUS 88434730
0507 0 7003 MOX G502 88434740
0508 00 44000FB3 BSI L F000 BSC SKPD-SHOULD NOT 88434750
050A 0 30AB OC /30AB ERR IO 88434760
050B 00 44000FOE G502 BSI L F005 CK LOCK ON ERROR 88434770
050D 0 70F5 MOX A502 LOOP 88434780
***** 88434790
05DE 0 2003 A504 LOS 3 SET C AND OF ON 88434800
05DF 0 C07E LO N502 LO /8000 88434810
05E0 0 2809 STS N507 SET /0003 IN N507 88434820
05E1 0 4815 BSC O-E SK IF OF OFF, MUNIS OR EVEN 88434830
05E2 0 7001 MDX G504 88434840
05E3 0 7003 MOX G505 88434850
05E4 00 44000FB3 G504 BSI L F000 BSC FAILED TO SKIP 88434860
05E6 0 30AC OC /30AC ERR IO 88434870
05E7 00 44000FB2 G505 BSI L F00E CK LOCK ON ERROR 88434880
05E9 0 70F4 MOX A504 LOOP 88434890
05EA 0 2000 N507 LOS 0 SET C + OF OFF 88434900
05EB 0 4801 BSC 0 SKIP IF OVERFLOW IS OFF 88434910
05EC 0 4801 BSC 0 88434920
05ED 0 7001 MOX G506 88434930
05EE 0 7003 MOX G507 88434940
05EF 00 44000FB3 G506 BSI L F000 BSC NOT CLEAR OVERFLW 88434950
05F1 0 30AD OC /30AD ERR IO 88434960
05F2 00 44000FDE G507 BSI L F005 CK LOCK ON ERROR 88434970
05F4 0 70E9 MOX A504 LOOP 88434980
***** 88434990
05F5 0 2000 A508 LOS 0 SET C AND OF OFF 88435000
05F6 0 C068 LO N503 LD /0001 88435010
05F7 0 482A BSC C+Z SK IF CARRY OFF, PLUS 88435020
* * OR ZERO 88435030
05F8 0 7001 MOX G508 88435040
05F9 0 7003 MOX H508 88435050
05FA 00 44000FB3 G508 BSI L F000 BSC FAILED TO SKIP 88435060
05FC 0 30AE OC /30AE ERR IO 88435070
05FD 00 44000FDE H508 BSI L F005 CK LOCK ON ERROR 88435080
05FF 0 70F5 MOX A508 LOOP 88435090
***** 88435100
0600 0 2003 A50A LOS 3 SET C AND OF ON 88435110
0601 0 C05A LO N500 LO /8001 88435120
0602 00 4C0F0613 BSC L G50A,+OCE BR CN NCT PLUS, OF ON, 88435130
* * CARRY ON OR NOT EVEN 88435140
0604 0 7001 MOX H50A 88435150
0605 0 7007 MDX J50A 88435160
0606 00 44000FB3 H50A BSI L F000 BSC FELL THRU 88435170
0608 0 30AF OC /30AF ERR IO 88435180
0609 00 44000FB2 BSI L F00E CK LOCK ON ERROR 88435190
060B 0 70F4 MOX A50A LOOP 88435200
060C 0 7006 MDX G50A 88435210
060D 00 44000FB3 J50A BSI L F000 BSC SKPD-SHOULD BRNCH 88435220
060F 0 30B0 OC /30B0 ERR IO 88435230
0610 00 44000FB2 BSI L F00E CK LOCK ON ERROR 88435240
0612 0 70ED MDX A50A LOOP 88435250
0613 0 F048 G50A EOR N500 ZERO WITH /8001 88435260
0614 0 4820 BSC Z SK CN ZERO 88435270
0615 0 7001 MOX H50B 88435280
0616 0 7003 MDX K50B 88435290
0617 00 44000FB3 H50B BSI L F000 ACC OISTPOYED AFTER BSC 88435300
0619 0 3170 OC /3170 ERR IO 88435310
061A 00 44000FOE K50B BSI L F005 CK LOCK ON ERROR 88435320
061C 0 7000 MDX A50C EXIT TO NEXT ROUTINE 88435330
***** 88435340
CORE DATA OR *LA- OPER- 88435350
ADDR INSTRUCTION *BEL ATION FT OPERANOS + REMARKS IO+SEQ= AT RIGHT 88435360
***** 88435370
061D 0 7000 MDX A50C EXIT TO NEXT ROUTINE 88435380
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PROCESSOR-CONTROLLER FUNCTION TEST

061D 0	2003	A50C	LDS	3	SET C + OF ON	88435390
061E 0	C041		LD	N504	LD /0004	88435400
061F 00	4C300623		BSC	L G50C,-Z	BR NOT MINUS OR NOT ZERO	88435410
0621 0	7002		MDX	H50C		88435420
0622 0	7008		MDX	J50C		88435430
0623 0	700A	G50C	MOX	K50C		88435440
0624 00	44000F83	H50C	BSI	L F000	BSC FELL THRU	88435450
0626 0	30B1		OC	/30B1	ERR ID	88435460
0627 00	44000FDE		BSI	L F005	CK LOCK ON ERROR	88435470
0629 0	70F3		MDX	A50C	LOOP	88435480
062A 0	7006		MDX	A50E		88435490
062B 00	44000F83	J50C	BSI	L F000	BSC SKPD-SHOULD BRNC	88435500
0620 0	30B2		DC	/30B2	ERR ID	88435510
062E 00	44000FDE	K50C	BSI	L F005	CK LOCK ON ERROR	88435520
0630 0	70EC		MOX	A50C	LOOP	88435530
0631 0	200U	A50E	LDS	0	SET C AND OF OFF	88435540
*****						88435550
0632 0	2003		LDS	3	SET C AND OF ON	88435560
0633 0	C028		LD	N500	LD /8001	88435570
0634 00	4C3F0638		BSC	L G50E,+EOCZ-	BR ON NOT PLUS, NOT EVEN, *OF, CARRY, NOT ZERO OR *NOT MINUS	88435580
* *						88435590
0636 0	700B		MOX	H50E		88435600
0637 0	7007		MDX	J50E		88435610
0638 00	44000F83	G50E	BSI	L F000	BSC BRNCHEO-SHOULONT	88435630
063A 0	30B3		OC	/30B3	ERR ID	88435640
063B 00	44000FDE		BSI	L F005	CK LOCK ON ERROR	88435650
063D 0	70F3		MDX	A50E	LOOP	88435660
063E 0	7006		MDX	B500		88435670
063F 00	44000F83	J50E	BSI	L F000	BSC SKPD-SHOULDNT	88435680
0641 0	30B4		DC	/30B4	ERR ID	88435690
0642 00	44000FOE	H50E	BSI	L F005	CK LOCK ON ERROR	88435700
0644 0	70EC		MDX	A50E	LOOP	88435710
*****						88435720
0645 0	2003	B500	LDS	3	SET C AND OF ON	88435730
0646 0	C018		LD	N503	LD /0001	88435740
0647 0	4808		BSC	+	SK ON PLUS	88435750
0648 0	700C		MOX	S501		88435760
0649 0	2817		STS	N505	SET /0003 IN N505	88435770
064A 0	C0.6		LD	N505	LD /0003	88435780
064B 0	F016		EOR	N506	ZERO WITH /0003	88435790
064C 00	4C180058		BSC	L S503,+-	BRANCH ON ZERO	88435800
064E 00	44000F83		BSI	L F000	BSC + CLEAREO OVFLW	88435810
0650 0	30B5		OC	/30B5	ERR ID	88435820
0651 00	44000FDE		BSI	L F005	CK LOCK ON ERROR	88435830
0653 0	70F1		MOX	B500	LOOP	88435840
0654 0	700L		MOX	A540	EXIT TO NEXT ROUTINE	88435850
0655 00	44000F83	S501	BSI	L F000	BSC FAILED TO SKP	88435860
0657 0	30B6		DC	/30B6	ERR ID	88435870
0658 00	44000FOE	S503	BSI	L F005	CK LOCK ON ERROR	88435880
065A 0	70EA		MDX	B500	LOOP	88435890
065B 0	7007		MDX	A540	EXIT TO NEXT ROUTINE	88435900
065C 0	8001	N500	OC	/8001		88435910
065D 0	0000	N501	DC	/0000		88435920
065E 0	8000	N502	DC	/8000		88435930
065F 0	0001	N503	DC	/0001		88435940
0660 0	0004	N504	OC	/0004		88435950
0661 0	0000	N505	DC	/0000		88435960
0662 0	0003	N506	DC	/0003		88435970
* * * TEST OF BSI OPERATION						88435980
*****						88435990
*****						88436000
*****						88436010
*****						88436020
*****						88436030
*****						88436040

ADDRESS	DATA OR	INSTRUCTION	OPERANDS	REMARKS	ID+SEQ=	AT RIGHT
0664 00	C400070B	LD	N540	LD /0001		88436070
0666 00	442F0678	BSI	L G540	BR CN NOT EVEN, CARRY, OF,		88436080
0668 0	7001	MDX	H540	* NCT PLUS OR NOT ZERO		88436090
0669 0	7007	MOX	J540			88436100
066A 00	44000FB3	BSI	L F000	BSI FELL THRU		88436110
066C 0	30B7	DC	/30B7	ERR IO		88436120
066D 00	44000FB2	BSI	L F00E	CK LOCK ON ERROR		88436130
066F 0	70F3	MDX	A540	LOOP		88436140
0670 0	7016	MDX	A5 4	EXIT TO NEXT ROUTINE		88436150
0671 00	44000FB3	BSI	L F000	BSI SKPD-SHOULD BRNCH		88436160
0673 0	30B8	DC	/30B8	ERR ID		88436170
0674 00	44000FB2	BSI	L F00E	CK LOCK ON ERROR		88436180
0675 0	70EC	MDX	A540	LOOP		88436190
0677 0	7001	MDX	G540+1	SK TO WORD AFTER G540		88436200
0678 0	0000	DC	/0000			88436210
0679 00	2C00070C	STS	L N541	STORE /0002 IN N541		88436220
067B 00	C400070C	LD	L N541	LD /0002		88436230
067D 00	F400070D	EUR	L N542	ZERO WITH /0002		88436240
067F 00	4C180684	BSC	L G542,+	BRANCH ON ZERO		88436250
0681 00	44000FB3	BSI	L F000	BSI NOT CLEAR OVERFLOW		88436260
0683 0	30B9	DC	/30B9	ERR ID		88436270
0684 00	44000FDE	BSI	L F005	CK LOCK ON ERROR		88436280
0686 0	70DC	MDX	A540	LOOP		88436290

0687 00	C400070D	A544	LD L N542	LD /0002		88436300
0689 00	44300698	BSI	L G544,7-	SK CN NOT ZERO OR		88436310
068B 0	7001	MDX	H544	* NCT MINUS		88436320
068C 0	7007	MDX	J544			88436330
068D 00	44000FB3	BSI	L F000	BSI DID NOT BRANCH		88436340
068F 0	30BA	DC	/30BA	ERR ID		88436350
0690 00	44000FDE	BSI	L F005	CK LOCK ON ERROR		88436360
0692 0	70F4	MDX	A544	LOOP		88436370
0693 0	7008	MDX	A546	EXIT TO NEXT ROUTINE		88436380
0694 00	44000FB3	BSI	L F000	BSI SKPD-SHOULD BRNC		88436390
0696 0	30BB	DC	/30BB	ERR ID		88436400
0697 00	44000FDE	BSI	L F005	CK LOCK ON ERROR		88436410
0699 0	70ED	MDX	A544	LOOP		88436420
069A 0	7001	MDX	A546	EXIT TO NEXT ROUTINE		88436430
069B 0	0000	DC	/0000			88436440

069C 0	C071	A546	LO N543			88436450
069D 00	442006A1	BSI	L G546,Z	BR WHEN NOT ZERO		88436460
069F 0	700C	MDX	J546			88436470
06A0 0	7008	MDX	H546			88436480
06A1 0	0000	DC	/0000			88436490
06A2 00	44000FB3	BSI	L F000	BSI BRNCHD-SHOULD NO		88436500
06A4 0	30BC	DC	/30BC	ERR IO		88436510
06A5 00	44000FDE	BSI	L F005	CK LOCK ON ERROR		88436520
06A7 0	70F4	MDX	A546	LOOP		88436530
06A8 0	7006	MDX	A548	EXIT TO NEXT ROUTINE		88436540
06A9 00	44000FB3	BSI	L F000	BSI SKPD-SHOULD NOT		88436550
06AB 0	30BD	DC	/30BD	ERR ID		88436560
06AC 00	44000FDE	BSI	L F005	CK LOCK ON ERROR		88436570
06AE 0	70ED	MDX	A546	LOOP		88436580

CURE	DATA OR	*LA-	OPER-			88436590
ADDR	INSTRUCTION	*BEL	ATION FT	OPERANDS +	REMARKS	ID+SEQ= AT RIGHT

06AF 0	C05B	A54B	LD N540			8843

PROCESSOR-CONTROLLER FUNCTION TEST

```
06B8 00 44000F83      BSI L F000      BSI BRNCHO-SHOULD NOT
06B0 0 30BF            DC /30BF      ERR ID
06BE 00 44000F0E      H54B BSI L F005      CK LOCK ON ERROR
06C0 0 70EE            MOX A548      LOOP
*****
06C1 D C04B            A54A LO N542
06C2 00 440B06CC      BSI L G54A,+  BR WHEN NOT PLUS
06C4 0 700B            MOX H54A
06C5 00 44000F83      BSI L F000      BSI SKPD ON COND TRUE
06C7 0 30C0            DC /30C0      ERR ID
06C8 00 44000F0E      BSI L F005      CK LOCK ON ERROR
06CA 0 70F6            MOX A54A      LOOP
06CB 0 7007            MOX A54C      EXIT TO NEXT ROUTINE
06CC 0 0000            G54A DC /0000
06CD 00 44000F83      BSI L F000      BSI BRNCHO-SHOULD NOT
06CF 0 30C1            DC /30C1      ERR ID
06D0 00 44000F0E      H54A BSI L F005      CK LOCK ON ERROR
06D2 0 70EE            MOX A54A      LOOP
*****
06D3 0 C039            A54C LO N542
06D4 00 440406DE      BSI L G54C,E  BR WHEN NOT EVEN
06D6 0 700B            MOX H54C
06D7 00 44000F83      BSI L F000      BSI SKPD ON COND TRUE
06D9 0 30C2            DC /30C2      ERR ID
06DA 00 44000FDE      BSI L F005      CK LOCK ON ERROR
06DC 0 70F6            MOX A54C      LOOP
06DE 0 7007            MOX A54E      EXIT TO NEXT ROUTINE
06E0 0 0000            G54C DC /0000
06E1 00 44000F83      BSI L F000      BSI BRNCHO-SHOULD NOT
06E3 0 30C3            DC /30C3      ERR ID
06E4 00 44000FDE      H54C BSI L F005      CK LOCK ON ERROR
06E6 0 70EE            MOX A54C      LOOP
*****
06E5 0 2000            A54E LOS 0      SET C AND OF OFF
06E6 00 440206F0      BSI L G54F,C  BR IF CARRY IS ON
06E8 0 700B            MOX H54E
06E9 00 44000F83      BSI L F000      BSI SKPD ON COND TRUE
06EB 0 30C4            OC /30C4      ERR ID
06EC 00 44000FDE      BSI L F005      CK LOCK ON ERROR
06EE 0 70F6            MOX A54E      LOOP
06EF 0 7007            MOX A54F      EXIT TO NEXT ROUTINE
06F0 0 0000            G54E OC /0000
06F1 00 44000F83      BSI L F000      BSI BRNCHO-SHOULD NOT
06F3 0 30C5            OC /30C5      ERR ID
06F4 00 44000FDE      H54E BSI L F005      CK LOCK ON ERROR
06F6 0 70EE            MOX A54E      LOOP
*****
06F7 0 2000            A54F LOS 0      SET C AND OF OFF
06F8 00 4401C703      BSI L G54F,C  BR ON OVERFLOW
06FA 0 700C            MOX H54F
06FB 00 44000F83      BSI L F000      BSI SKPD ON COND TRUE
06FD 0 30C6            OC /30C6      ERR ID
06FE 0 30C6            OC /30C6      ERR ID
06FF 00 44000FDE      BSI L F005      CK LOCK ON ERROR
0701 0 70F5            MOX A54F      LOOP
0702 0 700C            MOX A580      EXIT TO NEXT ROUTINE
0703 0 0000            G54F OC /0000
0704 00 44000F83      BSI L F000      BSI BRNCHO-SHOULD NOT
0706 0 30C7            DC /30C7      ERR ID
0707 00 44000F0E      H54F BSI L F005      CK LOCK ON ERROR
0709 0 70ED            MOX A54F      LOOP
070A 0 7004            MOX A580      EXIT TO NEXT ROUTINE
070B 0 8001            N540 OC /8001
070C 0 0000            N541 OC /0000
070D 0 0002            N542 OC /0002
070E 0 0000            N543 OC /0000
*
```

TEST OF L00 OPERATION

```
88436750
88436760
88436770
88436780
88436790
88436800
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88436900
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88436980
88436990
88437000
88437010
88437020
88437030
88437040
88437050
88437060
88437070
88437080
88437090
88437100
88437110
88437120
88437130
88437140
88437150
88437160
88437170
88437180
88437190
88437200
88437210
88437220
88437230
88437240
88437250
88437260
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88437280
88437290
88437300
88437310
88437320
88437330
88437340
88437350
88437360
88437370
88437380
88437390
88437400
88437410
88437420
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS IO+SEQ= AT RIGHT
*****
070F 0 C838          A580 LOO N581      LOO A=/0000 Q=/0000
0710 00 4C180715      BSC L G580,+  BRANCH ON ZERO
0712 00 44000F83      BSI L F000      LOO-A REG INCORRECT
0714 0 30C8           OC /30C8      ERR ID
0715 00 44000F82      G580 BSI L F00E      CK LOCK ON ERROR
0717 0 70F7           MOX A580      LOOP
0718 0 1800           RTE 16
0719 00 4C18071E      BSC L G582,+  BRANCH ON ZERO
071B 00 44000F83      BSI L F000      LOO-Q REG INCORRECT
071D 0 30C9           OC /30C9      ERR ID
071E 00 44000F0E      G582 BSI L F005      CK LOCK ON ERROR
0720 0 70EE           MOX A580      LOOP
*****
0721 0 C828          A584 LOO N583      LO A=/FFFF Q=/FFFF
0722 0 F028           EOR N584      ZERC WITH /FFFF
0723 00 4C180728      BSC L G584,+  BRANCH ON ZERO
0725 00 44000F83      BSI L F000      LOO-A REG INCORRECT
0727 0 30CA           OC /30CA      ERR ID
0728 00 44000F82      G584 BSI L F00E      CK LOCK ON ERROR
072A 0 70F6           MOX A584      LOOP
072B 0 1800           RTE 16
072C 0 F01E           EOR N584      ZERC WITH /FFFF
072D 00 4C180732      BSC L G586,+  BRANCH ON ZERO
072F 00 44000F83      BSI L F000      LOO-Q REG INCORRECT
0731 0 30C8           OC /30C8      ERR ID
0732 00 44000F0E      G586 BSI L F005      CK LOCK ON ERROR
0734 0 70EC           MOX A584      LOOP
*****
0735 0 C813          A588 LOO N582      LO A=/0000 Q=/FFFF
0736 00 4C180738      BSC L G588,+  BRANCH ON ZERO
0738 00 44000F83      BSI L F000      LOO-000-A REG FAILEO
073A 0 30CC           OC /30CC      ERR ID
073B 00 44000F82      G588 BSI L F00E      CK LOCK ON ERROR
073D 0 70F7           MOX A588      LOOP
073E 0 18D0           RTE 16
073F 00 4C180744      BSC L G58A,+  BRANCH ON ZERO
0741 00 44000F83      BSI L F000      LOO-000-Q REG FAILEO
0743 0 30CD           OC /30CD      ERR ID
0744 00 44000F0E      G58A BSI L F005      CK LOCK ON ERROR
0746 0 70EE           MOX A588      LOOP
0747 0 7004           MOX A5C0      EXIT TO NEXT ROUTINE
0748 0 0000           BSS E
0748 0 0000           N581 OC /0000
0749 0 0000           N582 DC /0000
074A 0 FFFF           N583 OC /FFFF
074B 0 FFFF           N584 OC /FFFF
*
* TEST OF STO OPERATION
*
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS IO+SEQ= AT RIGHT
*****
074C 0 C848          A5C0 LOO N5C1      LO A=/0000 Q=/0000
074D 0 084E           STO N5C5
074E 0 C040           LO N5C5      LO A=/0000 Q=/0000
074F 00 4C180754      BSC L G5C0,+  BRANCH ON ZERO
0751 00 44000F83      BSI L F000      STO-EA INCORRECT
0753 0 30CF           OC /30CF      ERR ID
0754 00 44000F82      G5C0 BSI L F00E      CK LOCK ON ERROR
0756 0 70F5           MOX A5C0      LOOP
```

PROCESSOR-CONTROLLER FUNCTION TEST

88438110
88438120
88438130
88438140
88438150
88438160
88438170
88438180
88438190
88438200
8843821C
8843822C
88438230
88438240
88438250
88438260
88438270
88438280
88438290
88438300
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88438450
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88438500
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88438680
88438690
88438700
88438710
88438720
88438730
88438740
88438750
88438760
88438770
88438780

07A1	0	7003	MDX	H600			88438790
07A2	00	44000F83	G600	BSI	L	F000	88438800
07A4	0	30D4		DC		/30D4	88438810
07A5	00	44000FDE	H60D	BSI	L	F005	88438820
07A7	0	70F7	MDX	A600		LDDP	88438830
*****							88438840
07A8	00	660007AB	A602	LDX	L2	G602	88438850
07AA	0	7003	MDX	H602			88438860
07AB	00	44000F83	G602	BSI	L	F009	88438870
07AD	0	30D5		DC		/30D5	88438880
07AE	00	44000FDE	H602	BSI	L	F005	88438890
07B0	0	70F7	MDX	A602		LDDP	88438900
*****							88438910
07B1	0	6100	A604	LDX	1	0	88438920
07B2	00	C500081C		LD	L1	N601	88438930
07B4	0	F067		EOR		N601	88438940
07B5	00	4C18078A		BSL	L	G604,+	88438950
07B7	00	44000F83		BSI	L	F000	88438960
07B9	0	30D6		DC		/30D6	88438970
07BA	00	44000FDE	G604	BSI	L	F005	88438980
07BC	0	70F4	MDX	A604		LDDP	88438990
*****							88439000
07BD	0	6200	A606	LDX	2	0	88439010
07BE	0	C05F		LD		N603	88439020
07BF	00	C600081C		LD	L2	N601	88439030
07C1	0	F05A		EOR		N601	88439040
07C2	00	4C1807C7		BSL	L	G606,+	88439050
07C4	00	44000F83		BSI	L	F000	88439060
07C6	0	30D7		DC		/30D7	88439070
07C7	00	44000FDE	G606	BSI	L	F005	88439080
07C9	0	70F3	MDX	A606		LDDP	88439090
*****							88439100
07CA	0	6300	A608	LDX	3	0	88439110
07CB	0	C052		LD		N603	88439120
07CC	00	C700081C		LD	L3	N601	88439130
07CE	0	F04D		EOR		N601	88439140
07CF	00	4C1807D4		BSL	L	G608,+	88439150
07D1	00	44000F83		BSI	L	F000	88439160
07D3	0	30D8		DC		/30D8	88439170
07D4	00	44000FDE	G608	BSI	L	F005	88439180
07D6	0	70F3	MDX	A608		LDDP	88439190
*****							88439200
07D7	0	61FF	A60A	LDX	1	-1	88439210
07D8	0	C045		LD		N603	88439220
07D9	00	C500081C		LD	L1	N601	88439230
07DB	0	F03F		EOR		N600	88439240
07DC	00	4C1807E1		BSL	L	G60A,+	88439250
07DE	00	44000F83		BSI	L	F000	88439260
07E0	0	30D9		DC		/30D9	88439270
07E1	00	44000FDE	G60A	BSI	L	F005	88439280
07E3	0	70F3	MDX	A60A		LOOP	88439290
*****							88439300
07E4	0	62FF	A60C	LDX	2	-1	88439310
07E5	0	C038		LD		N603	88439320
07E6	00	C600081C		LD	L2	N601	88439330
07E8	0	F032		EOR		N600	88439340
07E9	00	4C1807EE		BSL	L	G60C,+	88439350
07EB	00	44000F83		BSI	L	F000	88439360
07ED	0	30DA		DC		/30DA	88439370
07EE	00	44000FDE	G60C	BSI	L	F005	88439380
07F0	0	70F3	MDX	A60C		LOOP	88439390
*****							88439400
07F1	0	63FF	A60E	LDX	3	-1	88439410
07F2	0	C02B		LD		N603	88439420
07F3							

PROCESSOR-CONTROLLER FUNCTION TEST

```
07FA 0 300B OC /300B ERR IO 88439470
07FB 00 4400FDE G60E BSI L F005 CK LOCK ON ERROR 88439480
07FD 0 70F3 MOX A60E LOOP 88439490
*****
07FE 00 65000001 B600 LDX L1 1 LD XR 3 WITH +1 88439500
C800 0 C01D LO N603 LD /FFFF 88439510
0801 00 C500081C LD L1 N601 LD ADDR OF N601 + XR 1 88439520
0803 0 F019 EUR N602 ZERO WITH ADDR OF N602 88439530
0804 00 4C180809 BSC L J600,+~ BRANCH ON ZERO 88439540
0806 00 44000F83 BSI L F000 LONG FORM LDX-FAILED 88439550
0808 0 300C DC /300C ERR IO 88439560
0809 00 44000F0E J600 BSI L F005 CK LOCK ON ERROR 88439570
080B 0 70F2 MDX B600 LOOP 88439580
*****
080C 00 6780081E B602 LDX J3 N603 LD XR J WITH /FFFF 88439590
080E 0 C010 LD N604 LD /0001 88439600
080F 00 C700081C LD L3 N601 LD ADDR OF N601 + XR 3 88439610
0811 0 F009 EOR N600 ZERO WITH ADDR OF N600 88439620
0812 00 4C180817 BSC L J602,+~ BRANCH ON ZERO 88439630
0814 00 44000F83 BSI L F000 INDIRECT LDX FAILED 88439640
0816 0 300D DC /300D ERR IO 88439650
0817 00 44000FDE J602 BSI L F005 CK LOCK ON ERROR 88439660
0819 0 70F2 MOX B602 LOOP 88439670
081A 0 7005 MDX A640 EXIT TO NEXT ROUTINE 88439680
081B 0 081B N600 DC N600 88439690
081C 0 081C N601 DC N601 88439700
081D 0 081D N602 DC N602 88439710
081E 0 FFFF N603 DC /FFFF 88439720
081F 0 0001 N604 DC /0001 88439730
* 88439740
* 88439750
* 88439760
* 88439770
* 88439780
* 88439790
* 88439800
* 88439810
* 88439820
* 88439830
* 88439840
* 88439850
* 88439860
* 88439870
* 88439880
* 88439890
* 88439900
* 88439910
* 88439920
* 88439930
* 88439940
* 88439950
* 88439960
* 88439970
* 88439980
* 88439990
* 88440000
* 88440010
* 88440020
* 88440030
* 88440040
* 88440050
* 88440060
* 88440070
* 88440080
* 88440090
* 88440100
* 88440110
* 88440120
* 88440130
* 88440140

*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS IO+SEQ= AT RIGHT
*****
0820 0 C060 A640 LD N644 LD /FFFF 88439830
0821 0 D069 STO N640 SAVE 88439840
0822 0 C0FF N640 LD H640 LD /COFF 88439850
0823 0 6867 STX N640 STORE INST REG AT N640 88439860
0824 0 F0F0 N640 EOR H640 CK THAT ACC WAS NOT 88439870
* * RESET BY STX 88439880
0825 00 4C180820 BSC L G640,+~ BRANCH ON ZERO 88439890
0827 00 44000F83 BSI L F000 ACC GONE AFTER STX 88439900
0829 0 3167 OC /3167 ERR IO 88439910
082A 00 44000F82 BSI L F00E CK LOCK ON ERROR 88439920
082C 0 70F3 MOX A640 88439930
082D 0 C05D G640 LD N640 CK THAT STX STORED CORECT 88439940
082E 0 F050 EOR N642 88439950
082F 00 4C180834 BSC L G641,+~ BRANCH ON ZERO 88439960
0831 00 44000F83 BSI L F000 I CTR NOT STORED 88439970
0833 0 30DE DC /30DE ERR IO 88439980
0834 00 44000FDE G641 BSI L F005 CK LOCK ON ERROR 88440000
0836 0 70E9 MOX A640 LOOP 88440010
*****
0837 0 C056 A642 LD N644 LD /FFFF 88440020
0838 0 0052 STO N640 SAVE 88440030
0839 0 6100 LOX 1 0 LD XR 1 WITH /0000 88440040
083A 0 6950 STX 1 N640 STORE C(XR 1) AT N640 88440050
083B 0 C04F LO N640 LD C(N640) 88440060
083C 00 4C180841 BSC L G642,+~ BRANCH ON ZERO 88440070
083E 00 44000F83 BSI L F000 XR 1 NOT STORED 88440080
0840 0 300F DC /300F ERR IO 88440090
0841 00 44000FDE G642 BSI L F005 CK LOCK ON ERROR 88440100
0843 0 70F3 MDX A642 LOOP 88440110
*****
0844 0 C049 A644 LO N644 LD /FFFF 88440120
*****
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
0845 0 0045 STO N640 SAVE 88440150
0846 0 6200 LDX 2 0 LD XR 2 WITH /0000 88440160
0847 0 6A43 STY 2 N640 STORE C(XR 2) AT N640 88440170
0848 0 C042 LD N640 LD C(N640) 88440180
0849 00 4C18084E BSC L G644,+~ BRANCH ON ZERO 88440190
084B 00 44000F83 BSI L F000 XR 2 NOT STORED 88440200
084D 0 30E0 DC /30E0 ERR IO 88440210
084E 00 44000FDE G644 BSI L F005 CK LOCK ON ERROR 88440220
0850 0 70F3 MDX A644 LOOP 88440230
*****
0851 0 C03C A646 LO N644 LD /FFFF 88440240
0852 0 D038 STO N640 SAVE 88440250
0853 0 6300 LDX 3 0 LD XR 3 WITH /0000 88440260
0854 0 6836 STX 3 N640 STORE C(XR 3) AT N640 88440270
0855 0 C035 LD N640 LD C(N640) 88440280
0856 00 4C18085B BSC L G646,+~ BRANCH ON ZERO 88440290
0858 00 44000F93 BSI L F000 XR 3 NOT STORED 88440300
085A 0 30E1 DC /30E1 ERR IO 88440310
085B 00 44000F0E G646 BSI L F005 CK LOCK ON ERROR 88440320
085D 0 70F3 MDX A646 LOOP 88440330
*****
085E 0 C02E A648 LD N643 LD /0000 88440340
085F 0 D028 STO N640 SAVE 88440350
0860 0 61FF LOX 1 -1 LD XR 1 WITH /FFFF 88440360
0861 0 6929 STX 1 N640 STORE C(XR 1) AT N640 88440370
0862 0 C028 LD N640 LD C(N640) 88440380
0863 0 F02A EOR N644 ZERO WITH /FFFF 88440390
0864 00 4C180869 BSC L G648,+~ BRANCH ON ZERO 88440400
0866 00 44000F83 BSI L F000 XR 1 NOT STORED 88440410
0868 0 30E2 DC /30E2 ERR IO 88440420
0869 00 44000FDE G648 BSI L F005 CK LOCK ON ERROR 88440430
086B 0 70F2 MDX A648 LOOP 88440440
*****
086C 0 C020 A64A LD N643 LD /0000 88440450
086D 0 D01D STO N640 SAVE 88440460
086E 0 62FF LDX 2 -1 LD XR 2 WITH /FFFF 88440470
086F 0 6A18 STX 2 N640 STORE C(XR 2) AT N640 88440480
0870 0 C01A LD N640 LD C(N640) 88440490
0871 0 F01C EOR N644 ZERO WITH /FFFF 88440500
0872 00 4C180877 BSC L G64A,+~ BRANCH ON ZERO 88440510
0874 00 44000F83 BSI L F000 XR 2 NOT STORED 88440520
0876 0 30E3 DC /30E3 ERR IO 88440530
0877 00 44000FDE G64A BSI L F005 CK LOCK ON ERROR 88440540
0879 0 70F2 MDX A64A LOOP 88440550
*****
087A 0 C012 A64C LD N643 LD /0000 88440560
087B 0 D00F STO N640 SAVE 88440570
087C 0 63FF LOX 3 -1 LD XR 3 WITH /FFFF 88440580
087D 0 680D STX 3 N640 STORE C(XR 3) AT N640 88440590
087E 0 C00C LD N640 LD C(N640) 88440600
087F 0 F00E EOR N644 ZERO WITH /FFFF 88440610
0880 00 4C180885 BSC L G64C,+~ BRANCH ON ZERO 88440620
0882 00 44000F83 BSI L F000 XR 3 NOT STORED 88440630
0884 0 30E4 DC /30E4 ERR IO 88440640
0885 00 44000FDE G64C BSI L F005 CK LOCK ON ERROR 88440650
0887 0 70F2 MDX A64C LOOP 88440660
0888 0 C004 LO N643 LD /0000 88440670
0889 0 0001 STO N640 RESTORE N640 TO /0000 88440680
088A 0 7004 MDX A660 EXIT TO NEXT ROUTINE 88440690
088B 0 0000 N640 DC /0000 88440700
088C 0 0824 N642 DC K640 88440710
088D 0 0000 N643 DC /0000 88440720
088E 0 FFFF N644 DC /FFFF 88440730
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS IO+SEQ= AT RIGHT
*****
88440740
88440750
88440760
88440770
88440780
88440790
88440800
88440810
88440820
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
088F 0 6100      A660 LDX 1 0      LD XR 1 WITH /0000      88440830
0890 0 6200      LDX 2 0      LD XR 2 WITH /0000      88440840
0891 0 6300      LOX 3 0      LD XR 3 WITH /0000      88440850
0892 0 61FF      LDX 1 -1      LD XR 1 WITH /FFFF      88440860
0893 0 6A44      STX 2 N660      CK FOR OISTRUCTION OF      88440870
0894 0 C043      LD N660      OTHER INDEXES      88440880
0895 00 4C18089A  BSC L G660,+--      BRANCH ON ZERO      88440890
0897 00 44000FB3  BSI L F000      XR 2 CHANGED      88440900
0899 0 3157      OC /3157      ERR ID      88440910
089A 00 44000FB2  G660 BSI L F00E      CK LOCK ON ERROR      88440920
089C 0 70F2      MOX A660      LOOP      88440930
089D 0 6B3A      STX 3 N660      STORE C(XR 3) AT N660      88440940
089E 0 C039      LD N660      LD C(N660)      88440950
089F 00 4C1808A4  BSC L G661,+--      BRANCH ON ZERO      88440960
08A1 00 44000FB3  BSI L F000      XR 3 CHANGED      88440970
08A3 0 3158      OC /3158      ERR ID      88440980
08A4 00 44000FDE  G661 BSI L F005      CK LOCK ON ERROR      88440990
08A6 0 70E8      MOX A660      LOOP      88441000
*****
08A7 0 6100      A662 LDX 1 0      LD XR 1 WITH /0000      88441010
08A8 0 6200      LDX 2 0      LD XR 2 WITH /0000      88441020
08A9 0 6300      LOX 3 0      LD XR 3 WITH /0000      88441030
08AA 0 62FF      LDX 2 -1      LD XR 2 WITH /FFFF      88441040
08AB 0 692C      STX 1 N660      STORE C(XR 1) AT N660      88441050
08AC 0 C02B      LD N660      LD C(N660)      88441060
08AD 00 4C1808B2  BSC L G662,+--      BRANCH ON ZERO      88441070
08AF 00 44000FB3  BSI L F000      XR 1 CHANGED      88441080
08B1 0 3159      DC /3159      ERR ID      88441090
08B2 00 44000FB2  G662 BSI L F00E      CK LOCK ON ERROR      88441100
08B4 0 70F2      MDX A662      LOOP      88441110
08B5 0 6B22      STX 3 N660      STORE C(XR 3 AT N660      88441120
08B6 0 C021      LD N660      LD C(N660)      88441130
08B7 00 4C1808BC  BSC L G663,+--      BRANCH ON ZERO      88441140
08B9 00 44000FB3  BSI L F000      CK LOCK ON ERROR      88441150
08BB 0 315A      DC /315A      ERR ID      88441160
08BC 00 44000FOE  G663 BSI L F005      CK LOCK ON ERROR      88441170
08BE 0 70E8      MDX A662      LOOP      88441180
*****
08BF 0 6100      A664 LOX 1 0      CK DISTRUCTION OF      88441200
08C0 0 6200      LOX 2 0      OTHER INDEXES      88441210
08C1 0 6300      LOX 3 0      XR'S HAVE /0000      88441220
08C2 0 63FF      LOX 3 -1      LD XR 3 WITH /FFFF      88441230
08C3 0 6914      STX 1 N660      STORE C(XR 1) AT N660      88441240
08C4 0 C013      LD N660      LD C(N660)      88441250
08C5 00 4C1808CA  BSC L G664,+--      BRANCH ON ZERO      88441260
08C7 00 44000FB3  BSI L F000      XR 1 CHANGED      88441270
08C9 0 315B      OC /315B      ERR ID      88441280
08CA 00 44000FB2  G664 BSI L F00E      CK LOCK ON ERROR      88441290
08CC 0 70F2      MOX A664      LOOP      88441300
08CD 0 6A0A      STX 2 N660      STORE C(XR 2) AT N660      88441310
08CE 0 C009      LD N660      LD C(N660)      88441320
08CF 00 4C1808D4  BSC L G665,+--      BRANCH ON ZERO      88441330
08D1 00 44000FB3  BSI L F000      XR 2 CHANGED      88441340
08D3 0 315C      OC /315C      ERR ID      88441350
08D4 00 44000FDE  G665 BSI L F005      CK LOCK ON ERROR      88441360
08D6 0 70E8      MDX A664      LOOP      88441370
08D7 0 7001      MOX A670      EXIT TO NEXT ROUTINE      88441380
08D8 0 0000      N660 OC 0      88441390
*****
08D9 0 6110      A670 LDX 1 16      LD XR 1 WITH /0010      88441400
08DA 0 C010      LD N670      LOAD ONE      88441410
08DB 00 4C1808E4  G671 BSC L G670,+--      NOT BR FOR CORRECT OP      88441420
08DD 0 1001      G672 SLA 1      88441430
08DE 0 71FF      MOX 1 -1      -1 FROM C(XR 1)      88441440
08DF 0 70FB      MDX G671      88441450
08E0 00 44000FDE  BSI L F005      CK LOCK ON ERROR      88441460
08E2 0 70F6      MOX A670      LOOP      88441470
08E3 0 7008      MOX A680      EXIT TO NEXT ROUTINE      88441480
88441490
88441500
```

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```
08E4 00 44000FB3  G670 BSI L F000      WRONG DECODE OF ZERO ACC      88441510
08E6 0 3169      OC /3169      ERR ID      88441520
08E7 00 44000FB2  BSI L F00E      CK LOCK ON ERROR      88441530
08E9 0 70EF      MOX A670      LOOP      88441540
08EA 0 70F2      MOX G672      88441550
08EB 0 0001      N670 DC 1      88441560
*****
***** TEST CF ACC OPERATION *****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *8EL ATION FT OPERANDS + REMARKS IO+SEQ= AT RIGHT
*****
08EC 0 2002      A680 LOS 2      SET CARRY ON      88441600
08ED 0 C06E      LO N680      LD /FFFF      88441610
08EE 0 806E      A N681      A /0000      88441620
08EF 00 4C0108F2  BSC L G680,C      CK FOR OVERFLOW CN      88441630
08F1 0 7003      MDX H680      OVERFLOW IS OFF      88441640
08F2 0 44000FB3  G680 BSI L F000      OVERFLOW IS ON      88441650
08F4 0 30E5      OC /30E5      ERR ID      88441660
08F5 00 44000FB2  H680 BSI L F00E      CK LOCK ON ERROR      88441670
08F7 0 70F4      MDX A680      LOOP      88441680
08F8 0 F063      EOR N680      CK IF ADD ZERO      88441690
08F9 00 4C1808FE  BSC L G682,+--      * CHANGED ACC      88441700
08FB 00 44000FB3  BSI L F000      AOD 1 AND 0 FAILED      88441710
08FD 0 30E6      DC /30E6      ERR ID      88441720
08FE 00 44000FOE  G682 BSI L F005      CK LOCK ON ERROR      88441730
0900 0 70EB      MOX A680      LOOP      88441740
*****
0901 0 2000      A684 LOS 0      SET C AND OF OFF      88441750
0902 0 C059      LD N680      LD /FFFF      88441760
0903 0 805A      A N682      A /0001      88441770
0904 00 4C020909  BSC L G684,C      CK IF CARRY OCCURED      88441780
0906 00 44000FB3  BSI L F000      CARRY NOT ON      88441790
0908 0 30E7      OC /30E7      ERR ID      88441800
0909 00 44000FB2  G684 BSI L F00E      CK LOCK ON ERROR      88441810
090B 0 70F5      MDX A684      LOOP      88441820
090C 00 4C180911  BSC L G686,+--      BRANCH ON ZERO      88441830
090E 00 44000FB3  BSI L F000      AOD FFFF+0001 FAILED      88441840
0910 0 30E8      DC /30E8      ERR ID      88441850
0911 00 44000FDE  G686 BSI L F005      CK LOCK ON ERROR      88441860
0913 0 70E0      MDX A684      LOOP      88441870
*****
0914 0 2000      A688 LOS 0      SET C AND OF OFF      88441880
0915 0 C046      LD N680      LD /FFFF      88441890
0916 0 8045      A N680      A /FFFF      88441900
0917 00 4C02091C  BSC L G688,C      BR ON CARRY      88441910
0919 00 44000FB3  BSI L F000      CARRY NOT ON      88441920
091B 0 30E9      OC /30E9      ERR ID      88441930
091C 00 44000FB2  G688 BSI L F00E      CK LOCK ON ERROR      88441940
091E 0 70F5      MOX A688      LOOP      88441950
091F 0 F042      EOR N687      ZERC WITH /FFFF      88441960
0920 00 4C180925  BSC L G68A,+--      BRANCH ON ZERO      88441970
0922 00 44000FB3  BSI L F000      AOD FFFF+FFFF FAILED      88441980
0924 0 30FA      OC /30FA      ERR ID      88441990
0925 00 44000FDE  G68A BSI L F005      CK LOCK ON ERROR      88442000
0927 0 70EC      MOX A688      LOOP      88442010
*****
0928 0 2000      A68C LOS 0      SET C AND OF OFF      88442020
0929 0 C035      LD N683      LD /4000      88442030
092A 0 8034      A N683      A /4000      88442040
092B 00 4C010930  BSC L G68C,C      BR IF CF NOT ON      88442050
092D 00 44000FB3  BSI L F000      OVERFLOW NOT ON      88442060
092F 0 30EB      DC /30EB      ERR ID      88442070
0930 00 44000FB2  G68C BSI L F00E      CK LOCK ON ERROR      88442080
0932 0 70F5      MOX A68C      LOOP      88442090
0933 0 F02C      EOR N684      ZERO WITH /8000      88442100
88442110
88442120
88442130
88442140
88442150
88442160
88442170
88442180
```

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```
0934 00 4C180939      BSC L G68E,+-  BRANCH ON ZERO
0936 00 44000F83      BSI L F000      ADD 4000+4000 FAILED
0938 0 30EC           DC /30EC      ERR ID
0939 00 44000FDE      G68E BSI L F005  CK LOCK ON ERROR
0938 0 70EC           MDX A6BC      LOOP
*****
093C 0 2000           B680 LDS 0      SET C AND OF OFF
093D 0 C022           LD N684      LO /8000
093E 0 8021           A N684      A /8000
093F 0 2B23           STS N688      STORE C AND OF COND
0940 00 4C180945      BSC L J680,+-  BRANCH ON ZERO
0942 00 44000F83      BSI L F000      ADD 8000+8000 FAILED
0944 0 30ED           DC /30ED      ERR ID
0945 00 44000F82      J680 BSI L F00E  CK LOCK ON ERROR
0947 0 70F4           MDX B680      LOOP
0948 0 C01A           LD N688      LO C AND OF COND
0949 0 F017           EOR N686      ZERO WITH /0003
094A 00 4C180958      BSC L J682,+-  BRANCH ON ZERO
094C 00 4C040955      BSC L K682,E  BR ON NOT EVEN
094E 00 44000F83      BSI L F000      CARRY NOT ON
0950 0 30EF           DC /30EF      ERR ID
0951 00 44000FDE      BSI L F005  CK LOCK ON ERROR
0953 0 70E8           MDX B680      LOOP
0954 0 700F           MDX A6C0      EXIT TO NEXT ROUTINE
0955 00 44000F83      K682 BSI L F000  OVERFLOW NOT ON
0957 0 30EE           DC /30EE      ERR ID
0958 00 44000FDE      J682 BSI L F005  CK LOCK ON ERROR
095A 0 70E1           MDX B680      LOOP
095B 0 7008           MDX A6C0      EXIT TO NEXT ROUTINE
095C 0 FFFF           N680 DC /FFFF
095D 0 0000           N681 DC /0000
095E 0 0001           N682 DC /0001
095F 0 4000           N683 DC /4000
0960 0 8000           N684 DC /8000
0961 0 0003           N686 DC /0003
0962 0 1FFE           N687 DC /FFFE
0963 0 0000           N688 DC /0000
*
* INDEXING TEST
*
```

```
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0964 0 61FC           A6C0 LDX 1 -4  LD XR 1 WITH -4
0965 00 C50009F0      LD L1 N6C4  LD C(N6C4+XR 1)
0967 0 F074           EOR N6C0  ZERO ACC IF CORRECT OP
0968 00 4C200976      BSC L H6C0,Z  BR IF NOT ZERO
096A 0 697A           STX 1 N6C9  STORE C(XR 1) AT N6C9
096B 0 C079           LD N6C9  GET XR 1 VALUE
096C 0 F079           FOR N6CA  ZERO ACC IF CORRECT
096D 00 4C180979      BSC L G6C0,+-  BRANCH ON ZERO
096F 00 44000F83      BSI L F000      XR 1 LOADED WRONG
0971 0 30F0           DC /30F0  ERR ID
0972 00 44000FDE      BSI L F005  CK LOCK ON ERROR
0974 0 70EF           MDX A6C0      LOOP
0975 0 7006           MDX A6C2      EXIT TO NEXT ROUTINE
0976 00 44000F83      H6C0 BSI L F000  WRONG LOCATION
0978 0 30F1           DC /30F1  ERR ID
0979 00 44000FDE      G6C0 BSI L F005  CK LOCK ON ERROR
097B 0 70E8           MDX A6C0      LOOP
*****
097C 0 6204           A6C2 LDX 2 4  LD XR 2 WITH +4
097D 00 C60009F0      LD L2 N6C4  LD C(N6C4+XR 2)
097F 0 F064           EOR N6C8  ZERO ACC IF CORRECT
0980 00 4120098E      BSC L H6C2,Z  BR IF NOT ZERO
0982 0 6A62           STX 2 N6C9  STORE XR 2 AT N6C9
```

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```
0983 0 C061           LD N6C9  GET XR 2 VALUE
0984 0 F062           EOR N6C8  ZERO ACC IF CORRECT
0985 00 4C180991      BSC L G6C2,+-  BRANCH ON ZERO
0987 00 44000F83      BSI L F000      XR 2 LOADED WRONG
0989 0 30F2           DC /30F2  ERR ID
098A 00 44000FDE      BSI L F005  CK LOCK ON ERROR
098C 0 70EF           MDX A6C2      LOOP
098D 0 7006           MDX A6C4      EXIT TO NEXT ROUTINE
098E 00 44000F83      H6C2 BSI L F000  WRONG LOCATION
0990 0 30F3           DC /30F3  ERR ID
0991 00 44000FDE      G6C2 BSI L F005  CK LOCK ON ERROR
0993 0 70E8           MDX A6C2      LOOP
*****
0994 0 6300           A6C4 LDX 3 0  SET XR 3 TO ZERO
0995 00 C70009E0      LD L3 N6C4  LD C(N6C4+XR 3)
0997 0 F048           EOR N6C4  ZERO ACC IF CORRECT
0998 00 4C2009A5      BSC L H6C4,Z  BR IF NOT ZERO
099A 0 684A           STX 3 N6C9  STORE XR 3 AT N6C9
099B 0 C049           LD N6C9  LD /0000
099C 00 4C1809AB      BSC L G6C4,+-  BRANCH ON ZERO
099E 00 44000F83      BSI L F000      XR 3 LOADED WRONG
09A0 0 30F4           DC /30F4  ERR ID
09A1 00 44000FDE      BSI L F005  CK LOCK ON ERROR
09A3 0 70F0           MDX A6C4      LOOP
09A4 0 7006           MDX A6C6      EXIT TO NEXT ROUTINE
09A5 00 44000F83      H6C4 BSI L F000  WRONG LOCATION
09A7 0 30F5           DC /30F5  ERR ID
09A8 00 44000FDE      G6C4 BSI L F005  CK LOCK ON ERROR
09AA 0 70E9           MDX A6C4      LOOP
*****
09AB 0 6301           A6C6 LDX 3 1  SET XR 3 TO +1
09AC 00 C70009E0      LD L3 N6C4  LD C(N6C4+XR 3)
09AE 0 F032           EOR N6C5  ZERO FOR CORRECT OP
09AF 00 4C2009BD      BSC L H6C5,Z  BR IF NOT ZERO
09B1 0 6833           STX 3 N6C9  STORE XR 3 AT N6C9
09B2 0 C032           LD N6C9  LD C(N6C9)
09B3 0 F034           EOR N6C0  ZERO ACC FOR CORRECT OP
09B4 00 4C1809C0      BSC L G6C6,+-  BRANCH ON ZERO
09B6 00 44000F83      BSI L F000      XR 3 LOADED WRONG
09B8 0 30F6           DC /30F6  ERR ID
09B9 00 44000FDE      BSI L F005  CK LOCK ON ERROR
09BB 0 70EF           MDX A6C6      LOOP
09BC 0 7006           MDX A6C8      EXIT TO NEXT ROUTINE
09BD 00 44000F83      H6C6 BSI L F000  WRONG LOCATION
09BF 0 30F7           DC /30F7  ERR ID
09C0 00 44000FDE      G6C6 BSI L F005  CK LOCK ON ERROR
09C2 0 70E8           MDX A6C6      LOOP
*****
09C3 0 63F7           A6C8 LDX 3 -1  SET XR 3 TO -1
09C4 00 C74009EA      LD L3 N6C4  LD C(N6C4+XR 3)
09C6 0 F019           EOR N6C4  ACC NOW ZERO
09C7 00 4C2009D5      BSC L H6C8,Z  BR IF NOT ZERO
09C9 0 681D           STX 3 N6C9  STORE XR 3 AT N6C9
09CA 0 C01A           LD N6C9  LD C(N6C9)
09CB 0 F01E           LOR N6C8  ZERO WITH /FFFF
09CC 00 4C1809DB      BSC L G6C8,+-  BRANCH ON ZERO
09CE 00 44000F83      BSI L F000      XR 3-LOADED WRONG
09D0 0 30F8           DC /30F8  ERR ID
09D1 00 44000FDE      BSI L F005  CK LOCK ON ERROR
09D3 0 70EF           MDX A6C8      LOOP
09D4 0 7017           MDX A6D0      EXIT TO NEXT ROUTINE
09D5 00 44000F83      H6C8 BSI L F000  WRONG LOCATION
09D7 0 30F9           DC /30F9  ERR ID
09D8 00 44000FDE      G6C8 BSI L F005  CK LOCK ON ERROR
09DA 0 70E8           MDX A6C8      LOOP
09DB 0 7010           MDX A6D0      EXIT TO NEXT ROUTINE
09DC 0 09DC           N6C0 DC N6C0
09DD 0 09DD           N6C1 DC N6C1
```

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```
09DE 0 09DE      N6C2 DC      N6C2      88443550
09DF 0 09DF      N6C3 DC      N6C3      88443560
09E0 0 09E0      N6C4 DC      N6C4      88443570
09E1 0 09E1      N6C5 DC      N6C5      88443580
09E2 0 09E2      N6C6 DC      N6C6      88443590
09E3 0 09E3      N6C7 DC      N6C7      88443600
09E4 0 09E4      N6C8 DC      N6C8      88443610
09E5 0 0000      N6C9 DC      /0000      88443620
09E6 0 FFFC      N6CA DC      /FFFC      88443630
09E7 0 0004      N6C8 DC      /0004      88443640
09E8 0 0001      N6CD DC      /0001      88443650
09E9 0 09E0      OC      N6C4      88443660
09EA 0 FFFF      N6CF DC      /FFFF      88443670
09EB 0 70D7      MDX      A6C8      LOOP      88443680
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
09EL 00 650009D0 A6D0 LOX L1 N6C1 LD XR 1 WITH ADDRESS 88443740
* * OF N6C1 88443750
09EE 0 C1FF LD 1 -1 SHORT FCRM INDEXING 88443760
09EF 0 FOEC EOR N6C0 ZERO IF CORRECT 88443770
09F0 00 4C1809F5 BSC L H600,+-- BRANCH ON ZERO 88443780
09F2 00 44000F83 BSI L F000 INDEXED LD INST. FAILED 88443790
09F4 0 315D DC /315D ERR ID 88443800
09F5 00 44000FDE H6D0 BSI L F005 CK LOCK ON ERROR 88443810
09F7 0 70F4 MDX A6D0 LOOP 88443820
*****
09F8 00 6600090D A6D2 LOX L2 N6C1 LD XR 2 WITH ADDRESS 88443830
* * OF N6C1 88443840
09FA 0 C201 LD 2 1 LD C(OF ADDRESS IN XR 1+1) 88443860
09FB 0 FOE2 EOR N6C2 ZERO IF CORRECT 88443870
09FC 00 4C180A01 BSC L H6D2,+-- BRANCH CN ZERO 88443880
09FE 00 44000F83 BSI L F000 INDEXED LD INST. FAILED 88443890
0A00 0 315E OC /315E ERR ID 88443900
0A01 00 44000FDE H6D2 BSI L F005 CK LOCK ON ERROR 88443910
0A03 0 70F4 MDX A6D2 LOOP 88443920
*****
0A04 00 670009D0 A6D3 LOX L3 N6C1 LD XR 3 WITH ADD OF N6C1 88443940
0A06 0 C300 LD 3 0 LD C(OF ADD IN XR 3 + 0) 88443950
0A07 0 F005 EOR N6C1 ZERO IF CORRECT 88443960
0A08 00 4C180A0D BSC L H6D3,+-- BRANCH ON ZERO 88443970
0A0A 00 44000F83 BSI L F000 INDEXED LD INST. FAILED 88443980
0A0C 0 315F DC /315F ERR ID 88443990
0A0D 00 44000FDE H6D3 BSI L F005 CK LOCK ON ERROR 88444000
0A0F 0 70F4 MDX A6D3 LOOP 88444010
*****
0A10 0 6102 A6D5 LOX 1 2 LD XR 1 WITH +2 88444020
0A11 0 C006 LD N6C0 LD /0001 88444030
0A12 0 1101 SLA 1 1 NOW A=/0004 88444040
0A13 0 F0D3 EOR N6C8 NOW A=/0000 88444050
0A14 00 4C180A19 BSC L H6D5,+-- BRANCH CN ZERO 88444060
0A16 00 44000F83 BSI L F000 INDEXED SLA FAILED 88444070
0A18 0 3163 DC /3163 ERR ID 88444080
0A19 00 44000FDE H6D5 BSI L F005 CK LOCK ON ERROR 88444090
0A1B 0 70F4 MDX A6D5 LOOP 88444100
*****
0A1C 0 6202 A6D6 LOX 2 2 LD /00004 88444110
0A1D 0 C0C9 LO N6C8 NOW A=/0001 88444120
0A1E 0 1A01 SRA 2 1 ZERO ACC 88444130
0A1F 0 F0C8 EOR N6CD ZERO WITH /0001 88444140
0A20 00 4C180A25 BSC L H6D6,+-- BRANCH ON ZERO 88444150
0A22 00 44000F83 BSI L F000 INDEXED SRA FAILED 88444160
0A24 0 3164 OC /3164 ERR ID 88444170
0A25 00 44000FDE H6D6 BSI L F005 CK LOCK ON ERROR 88444180
0A27 0 70F4 MDX A6D6 LOOP 88444190
*****
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
*****
*
* TEST INDEXED BSC
*
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0A28 0 6301 A6F0 LDX 3 1 LD XR 3 WITH +1 88444230
0A29 0 C00E LD N6F1 LD C(OF LABEL N6F1) 88444240
0A2A 00 4F000A2D BSC L3 N6F0 BR TO C(N6F0+XR 3) 88444250
0A2C 0 3000 WAIT INDEXED BSC FAILED 88444260
0A2D 0 3000 N6F0 WAIT INDEXED BSC FAILED 88444270
0A2E 0 F009 EOR N6F1 CK FOR DISTROYED ACC 88444280
0A2F 00 4C180A34 BSC L H6F0,+-- BRANCH CN ZERO 88444290
0A31 00 44000F83 BSI L F000 ACC DISTROYED 88444300
0A33 0 3165 DC /3165 ERR ID 88444310
0A34 00 44000FDE H6F0 BSI L F005 CK LOCK ON ERROR 88444320
0A36 0 70F1 MDX A6F0 LOOP 88444330
0A37 0 7001 MOX A6F1 EXIT TC NEXT ROUTINE 88444340
0A38 0 0A38 N6F1 DC N6F1 88444350
*****
0A39 0 6201 A6F1 LDX 2 1 LD XR 2 WITH +1 88444360
0A3A 00 4E800A3D BSC L2 N6F2 BR TO N6F2+1 INDIRECT 88444370
0A3C 0 7005 MDX H6F1 BSC FAILED 88444380
0A3D 0 7004 N6F2 MDX H6F1 BSC FAILED 88444390
0A3E 0 0A41 DC N6F3 88444400
0A3F 0 7002 MOX H6F1 BSC FAILED 88444410
0A40 0 7001 MDX H6F1 BSC FAILED 88444420
0A41 0 7003 N6F3 MDX H6F2 88444430
0A42 00 44000F83 H6F1 BSI L F000 BSC DID NOT BRANCH 88444440
0A44 0 3166 OC /3166 ERR ID 88444450
0A45 00 44000FDE H6F2 BSI L F005 CK LOCK ON ERROR 88444460
0A47 0 70F1 MDX A6F1 LOOP 88444470
*****
*
* TEST OF SUBTRACT OPERATION
*
*****
0A48 0 2000 A700 LDS 0 SET C AND CF OFF 88444480
0A49 0 C066 LD N700 LD /0000 88444490
0A4A 0 9066 S N701 S /0001 A NOW /FFFF 88444500
0A4B 0 2866 STS N702 STORE CARRY IND. TO N702 88444510
0A4C 0 F066 EOR N703 ZERO ACC IF CORRECT 88444520
0A4D 00 4C180A52 BSC L G700,+-- BRANCH ON ZERO 88444530
0A4F 00 44000F83 BSI L F000 0000 MINUS 0001 FAILED 88444540
0A51 0 30FA DC /30FA ERR ID 88444550
0A52 00 44000F82 G700 BSI L F00F CK LOCK ON ERROR 88444560
0A54 0 70F3 MDX A700 LOOP 88444570
0A55 0 C05C LO N702 LD CARRY INDICATION 88444580
0A56 0 F05D EOR N704 ZERO IF CORRECT 88444590
0A57 00 4C180A5C BSC L G702,+-- BRANCH ON ZERO 88444600
0A59 00 44000F83 BSI L F000 CARRY NOT ON 88444610
0A5B 0 30FB DC /30FB ERR ID 88444620
0A5C 00 44000FDE G702 BSI L F005 CK LOCK ON ERROR 88444630
0A5E 0 70E9 MOX A700 LDOP 88444640
*****
0A5F 0 2000 A704 LDS 0 SET C AND CF OFF+ 88444650
0A60 0 C04F LD N700 LD /0000 88444660
0A61 0 9051 S N703 S /FFFF 88444670
0A62 0 284F STS N702 STORE CARRY ON CONDITION 88444680
0A63 0 F04D EOR N701 ZERO WITH /0001 88444690
0A64 00 4C180A69 BSC L G704,+-- BRANCH CN ZERO 88444700
0A66 00 44000F83 BSI L F000 0000 MINUS FFFF FAILED 88444710
0A68 0 30FC DC /30FC ERR ID 88444720
0A69 00 44000F82 G704 BSI L F00E CK LOCK ON ERROR 88444730
0A6B 0 70F3 MOX A704 LOOP 88444740
0A6C 0 C045 LO N702 LD CARRY COND FROM N702 88444750
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PROCESSOR-CONTROLLER FUNCTION TEST

PROCESSOR-CONTROLLER FUNCTION TEST

```
0A6D 0 F046      EDR      N704      ZERO ACC IF CORRECT      88444910
0A6E 00 4C180A73 BSC L  G70C,+-- BRANCH ON ZERO          88444920
0A70 00 44000F83 BSI L  F000      CARRY NOT SET          88444930
0A72 0 30FD      DC        /30FD      ERR ID              88444940
0A73 00 44000FDE G706 BSI L  F005      CK LOCK ON ERROR      88444950
0A75 0 70E9      MDX      A704      LOOP                  88444960
                        *****
CORE      DATA OR      *LA- OPER-
ADDR      INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0A76 0 2000      A70C LDS      0          SET C AND OF OFF      88445000
0A77 0 C03D      LD        N705      LD /8000              88445010
0A78 0 9028      S          N701      S /0001              88445020
0A79 0 2838      STS      N702      SAVE C + OF CONDITION      88445030
0A7A 0 F03C      EDR      N707      ZERO ACC IF CORRECT UP      88445040
0A7B 00 4C180A80 BSC L  G70B,+-- BRANCH ON ZERO          88445050
0A7D 00 44000F83 BSI L  F000      8000 MINUS 0001 FAILED      88445060
0A7E 0 30FE      DC        /30FE      ERR ID              88445070
0A80 00 44000FB2 G708 BSI L  F00E      CK LOCK ON ERROR      88445080
0A82 0 70F3      MDX      A708      LOOP                  88445090
0A83 0 C02E      LD        N702      LD STORE CARRY CONDITION      88445100
0A84 0 F02C      EDR      N701      ZERO IF CORRECT          88445110
0A85 00 4C180A8A BSC L  G70A,+-- BRANCH ON ZERO          88445120
0A87 00 44000F83 BSI L  F000      OVERFLOW NOT SET          88445130
0A89 0 30FF      DC        /30FF      ERR ID              88445140
0A8A 00 44000FDE G70A BSI L  F005      CK LOCK ON ERROR      88445150
0A8C 0 70E9      MDX      A708      LOOP                  88445160
                        *****
0A8D 0 2000      A70C LDS      0          SET C AND OF OFF      88445170
0A8E 0 C021      LD        N700      LD /0000              88445180
0A8F 0 9025      S          N705      S /8000              88445190
0A90 0 2821      STS      N702      STORE C + OF CONDITION      88445200
0A91 0 F023      EDR      N705      ZERO ACC IF CORRECT          88445210
0A92 00 4C180A97 BSC L  G70C,+-- BRANCH ON ZERO          88445220
0A94 00 44000F83 BSI L  F000      0000 MINUS 8000 FAILED      88445230
0A96 0 3100      DC        /3100      ERR ID              88445240
0A97 00 44000FB2 G70C BSI L  F00E      CK LOCK ON ERROR      88445250
0A99 0 70F3      MDX      A70C      LOOP                  88445260
0A9A 0 C017      LD        N702      LD CON CF C+OF          88445270
0A9B 0 F01A      EDR      N706      ZERO ACC IF CORRECT          88445280
0A9C 00 4C180AAC BSC L  G70E,+-- BRANCH ON ZERO          88445290
0A9E 0 C013      LD        N702      LD CON OF C + OF          88445300
0A9F 0 E011      AND      N701      AND IN /0001              88445310
0AA0 00 4C200AA9 BSC L  J70E,+-- BR IF NOT ZERO          88445320
0AA2 00 44000F83 BSI L  F000      OVERFLOW NOT ON          88445330
0AA4 0 3101      DC        /3101      ERR ID              88445340
0AA5 00 44000FDE BSI L  F005      CK LOCK ON ERROR      88445350
0AA7 0 70E5      MDX      A70C      LOOP                  88445360
0AA8 0 70DF      MDX      A70C      EXIT TO NEXT ROUTINE      88445370
0AA9 00 44000F83 J70E BSI L  F000      CARRY NOT ON          88445380
0AAB 0 3102      DC        /3102      ERR ID              88445390
0AAC 00 44000FDE G70E BSI L  F005      CK LOCK ON ERROR      88445400
0AAE 0 70DE      MDX      A70C      LOOP                  88445410
0AAF 0 7008      MDX      A740      EXIT TO NEXT ROUTINE      88445420
0AB0 0 0000      N700 DC        /0000              88445430
0AB1 0 0001      N701 DC        /0001              88445440
0AB2 0 0000      N702 DC        /0000              88445450
0AB3 0 FFFF      N703 DC        /FFFF              88445460
0AB4 0 0002      N704 DC        /0002              88445470
0AB5 0 8000      N705 DC        /8000              88445480
0AB6 0 0003      N706 DC        /0003              88445490
0AB7 0 7FFF      N707 DC        /7FFF              88445500
                        *
                        *          TEST OF ADD DOUBLE          *
                        *
*****
*****
88445580
```

```
CORE      DATA OR      *LA- OPER-
ADDR      INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0AB8 0 2000      A740 LDS      0          SET C AND OF OFF      88445590
0AB9 00 C0000B80 LOD L  N742      LD A=/FFFF Q=/FFFF      88445600
0ABB 00 8C000B82 AD L  N744      A /0000 /0000      88445610
0ABD 00 2C000B7E STS L  N740      STORE CON. OF C + OF      88445620
0ABF 00 F4000B80 EOR L  N742      BR ON ZERO          88445630
0AC1 00 4C180AC6 BSC L  G740,+-- AD FFFF+0000 A FAILED      88445640
0AC3 00 44000F83 BSI L  F000      ERR ID              88445650
0AC5 0 3103      DC        /3103      CK LOCK ON ERROR      88445660
0AC6 00 44000FB2 G740 BSI L  F00E      LOOP                  88445670
0AC8 0 70EF      MDX      A740      RTE 16              88445680
0AC9 0 1800      RTE 16              88445690
0ACA 00 F4000B80 EOR L  N742      BR ON ZERO          88445700
0ACC 00 4C180AD1 BSC L  G742,+-- AD FFFF+0000 Q FAILED      88445710
0ACE 00 44000F83 BSI L  F000      ERR ID              88445720
0AD0 0 3104      DC        /3104      CK LOCK ON ERROR      88445730
0AD1 00 44000FB2 G742 BSI L  F00E      LOOP                  88445740
0AD3 0 70E4      MDX      A740      CONDITION OF C + OF      88445750
0AD4 00 C4000B7E LD L  N740      BRANCH ON ZERO          88445760
0AD6 00 4C180AE4 BSC L  G744,+-- BR IF NOT EVEN          88445770
0AD8 00 4C040AE1 BSC L  H744,E      CARRY ON          88445780
0ADA 00 44000F83 BSI L  F000      ERR ID              88445790
0ADC 0 3105      DC        /3105      CK LOCK ON ERROR      88445800
0ADD 00 44000FDE BSI L  F005      LOOP                  88445810
0ADF 0 70D8      MDX      A740      OVFLO ON          88445820
0AE0 0 7003      MDX      G744      ERR ID              88445830
0AE1 00 44000F83 H744 BSI L  F000      CK LOCK ON ERROR      88445840
0AE3 0 3106      DC        /3106      LOOP                  88445850
0AE4 00 44000FDE G744 BSI L  F005      OVFLO ON          88445860
0AE6 0 70D1      MDX      A740      ERR ID              88445870
                        *****
0AE7 0 2000      A746 LOS      0          SET C AND OF OFF      88445880
0AE8 00 C0000B84 LOD L  N746      LD A=/0000 Q=/0001      88445890
0AEA 00 8C000B80 AD L  N742      A /FFFF /FFFF      88445900
0AEC 00 2C000B7E STS L  N740      STORE COND OF C AND OF      88445910
0AEE 00 4C180AE3 BSC L  G746,+-- BRANCH ON ZERO          88445920
0AEF 00 44000F83 BSI L  F000      AD 0000+FFFF A FAILED      88445930
0AF2 0 3107      DC        /3107      ERR ID              88445940
0AF3 00 44000FB2 G746 BSI L  F00E      CK LOCK ON ERROR      88445950
0AF5 0 70F1      MDX      A746      LOOP                  88445960
0AF6 0 1800      RTE 16              88445970
0AF7 00 4C180AFC BSC L  G748,+-- INTERCHANGE A AND Q      88445980
0AF9 00 44000F83 BSI L  F000      BRANCH ON ZERO          88445990
0AFB 0 3108      DC        /3108      AD 0001+FFFF Q FAILED      88460000
0AFC 00 44000FB2 G748 BSI L  F00E      CK LOCK ON ERROR      88460010
0AFE 0 70E8      MDX      A746      LOOP                  88460020
0AFF 00 C4000B7E LD L  N740      LD COND OF C AND OF      88460030
0B01 00 F4000B86 EOR L  N748      CHECK FOR CARRY          88460040
0B03 00 4C180B11 BSC L  G74A,+-- ZERO= C AND OF OK          88460050
0B05 00 4C040B0E BSC L  H74A,E      CHECK FOR OVERFLOW (B15) 88460060
0B07 00 44000F83 BSI L  F000      CARRY NOT ON          88460070
0B09 0 3109      DC        /3109      ERR ID              88460080
0B0A 00 44000FDE BSI L  F005      CK LOCK ON ERROR      88460090
0B0C 0 70DA      MDX      A746      LOOP                  88460100
0B0D 0 7003      MDX      G74A      LD COND OF C AND OF      88460110
0B0E 00 44000F83 H74A BSI L  F000      CHECK FOR CARRY          88460120
0B10 0 310A      DC        /310A      ZERO= C AND OF OK          88460130
0B11 00 44000FDE G74A BSI L  F005      CHECK FOR OVERFLOW (B15) 88460140
0B13 0 70D3      MDX      A746      CARRY NOT ON          88460150
                        *****
CORE      DATA OR      *LA- OPER-
ADDR      INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0B14 0 2000      A74C LDS      0          SET C AND OF OFF      88460160
0B15 0 C864      LOD L  N742      LD A=/FFFF Q=/FFFF      88460170
                        *****
88460200
88460210
88460220
88460230
88460240
88460250
88460260
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
0816 0 8865      AD      N742      A /FFFF      /FFFF      88446270
0817 0 2866      STS      N740      STORE C AND OF COND      88446280
0818 0 F067      EOR      N742      ZERO WITH /FFFF      88446290
0819 00 4C180B1E BSC L G74C,+- BRANCH ON ZERO      88446300
081B 00 44000FB3 BSI L F000 AD FFFF+FFFF ACC FAILED      88446310
081D 0 3108      OC      /3108      ERR ID      88446320
081E 00 44000FB2 G74C BSI L F00E CK LOCK ON ERROR      88446330
0820 0 7JF3      MDX      A74C      LOOP      88446340
0821 0 1800      RTE      16      INTERCHANGE A AND Q      88446350
0822 0 F065      EOR      N74A      ZERO WITH /FFFF      88446360
0823 00 4C180B28 BSC L G74E,+- BRANCH ON ZERO      88446370
0825 00 44000FB3 BSI L F000 AD FFFF+FFFF Q FAILED      88446380
0827 0 310C      DC      /310C      ERR ID      88446390
0828 00 44000FB2 G74E BSI L F00E CK LOCK ON ERROR      88446400
082A 0 70E9      MDX      A74C      LOOP      88446410
082B 0 C052      LD      N740      CONDITION OF C AND OF      88446420
082C 0 F059      EOR      N748      CHECK FOR OVERFLOW      88446430
082D 00 4C180B3B BSC L J74D,+- BRANCH ON ZERO      88446440
082F 00 4C040B38 BSC L K74D,E CHECK FOR CARRY      88446450
0831 00 44000FB3 BSI L F000 CARRY NOT ON      88446460
0833 0 310E      DC      /310E      ERR ID      88446470
0834 00 44000FDE BSI L F005 CK LOCK ON ERROR      88446480
0836 0 70DD      MDX      A74C      LOOP      88446490
0837 0 7003      MDX      J740      88446500
0838 00 44000FB3 K740 BSI L F000 OVFLD ON      88446510
083A 0 310D      DC      /310D      ERR ID      88446520
083B 00 44000FDE J740 BSI L F0D5 CK LOCK ON ERROR      88446530
083D 0 70D6      MDX      A74C      LOOP      88446540
*****
083E 0 2000      B742 LDS      0      SET C AND OF OFF      88446560
083F 0 C84A      LDD      N74C      LD A=/FFFF Q=/7FFF      88446570
0840 0 883F      AD      N742      A /FFFF /FFFF      88446580
0841 0 283C      STS      N740      STORE CONDITION OF C + OF      88446590
0842 0 F03D      EOR      N742      88446600
0843 00 4C180B48 BSC L J742,+- BRANCH ON ZERO      88446610
0845 00 44000FB3 BSI L F000 AD FFFF+FFFF A FAILED      88446620
0847 0 310F      DC      /310F      ERR ID      88446630
0848 00 44000FB2 J742 BSI L F00E CK LOCK ON ERROR      88446640
084A 0 70F3      MDX      B742      LOOP      88446650
084B 0 1800      RTE      16      INTERCHANGE A AND Q      88446660
084C 0 F03C      EOR      N748      88446670
084D 00 4C180B52 BSC L J744,+- BRANCH ON ZERO      88446680
084F 00 44000FB3 BSI L F000 AD /7FFF+FFFF Q /FAILED      88446690
0851 0 3110      DC      /3110      ERR ID      88446700
0852 00 44000FB2 J744 BSI L F00L CK LOCK ON ERROR      88446710
0854 0 70E9      MDX      B742      LOOP      88446720
0855 0 C02E      LD      N740      LD C AND OF CONDITION      88446730
0856 0 F02F      EOR      N748      ZERO IF CARRY WAS ON      88446740
0857 00 4C180B52 BSC L J746,+- BRANCH ON ZERO      88446750
0859 00 4C040B62 BSC L K746,E CHECK FOR CARRY      88446760
085B 00 44000FB3 BSI L F000 CARRY NOT ON      88446770
085D 0 3112      DC      /3112      ERR ID      88446780
085E 00 44000FDE BSI L F005 CK LOCK ON ERROR      88446790
0860 0 70DD      MDX      B742      LOOP      88446800
0861 0 7003      MDX      J746      88446810
0862 00 44000FB3 K746 BSI L F000 OVFLD ON      88446820
0864 0 3111      DC      /3111      ERR ID      88446830
0865 00 44000FDE J746 BSI L F005 CK LOCK ON ERROR      88446840
0867 0 70D6      MDX      B742      LOOP      88446850
*****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0868 0 C81B      B747 LDD      N746      LD A=/0000 Q=/0001      88446910
0869 0 881B      AD      N747      A /0001 /0001      88446920
086A 0 F01A      EOR      N747      ZERO ACC IF CORRECT DP      88446930
086B 00 4C180B70 BSC L J748,+- BRANCH ON ZERO      88446940
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
086D 00 44000FB3 BSI L F000 AD-000 A REG FAILED      88446950
086F 0 3113      DC      /3113      ERR ID      88446960
0870 00 44000FB2 J748 BSI L F00E CK LOCK ON ERROR      88446970
0872 0 70F5      MDX      B747      LOOP      88446980
0873 0 1800      RTE      16      NOW A=/0002 Q=/0000      88446990
0874 0 F011      EOR      N748      ZERO ACC IF CORRECT OP      88447000
0875 00 4C180B7A BSC L J74A,+- BRANCH ON ZERO      88447010
0877 00 44000FB3 BSI L F000 AD-000 Q REG FAILED      88447020
0879 0 3114      DC      /3114      ERR ID      88447030
087A 00 44000FDE J74A BSI L F005 CK LOCK ON ERROR      88447040
087C 0 70E8      MDX      B747      LOOP      88447050
087D 0 700E      MDX      A780      EXIT TO NEXT ROUTINE      88447060
087E 0 C000      N740 DC      /0000      88447070
0880 0000      BSS      E      88447080
0880 0 FFFF      N742 DC      /FFFF      88447090
0881 0 FFFF      DC      /FFFF      88447100
0882 0 0000      N744 DC      /0000      88447110
0883 0 0000      DC      /0000      88447120
0884 0 0000      N746 DC      /0000      88447130
0885 0 0001      N747 DC      /0001      88447140
0886 0 0002      N748 DC      /0002      88447150
0887 0 0000      DC      /0000      88447160
0888 0 FFFE      N74A DC      /FFFE      88447170
0889 0 7FFE      N74A DC      /7FFE      88447180
088A 0 FFFF      N74C DC      /FFFF      88447190
088B 0 7FFF      DC      /7FFF      88447200
*****
*****
TEST SUB DOUBLE
*****
088C 0 2000      A780 LDS      0      SET C AND OF OFF      88447250
088D 0 C868      LDD      N782      LD A=/0000 Q=/0000      88447260
088E 0 9869      SD      N784      S /0000 /0001      88447270
088F 0 2864      STS      N780      STORE C AND OF CONDITION      88447280
0890 0 F069      EOR      N786      ZERO WITH /FFFF      88447290
0891 00 4C180B96 BSC L G760,+- BRANCH ON ZERO      88447300
0893 00 44000FB3 BSI L F000 SD 0000-0000 ACC FAILED      88447310
0895 0 3115      DC      /3115      ERR ID      88447320
0896 00 44000FB2 G780 BSI L F00E CK LOCK ON ERROR      88447330
0898 0 70F3      MDX      A780      LOOP      88447340
0899 0 1800      RTE      16      NOW A=/FFFF Q=/0000      88447350
089A 0 F05F      EOR      N786      ZERO WITH /FFFF      88447360
089B 00 4C180BA0 BSC L G782,+- BR ON ZERO      88447370
089D 00 44000FB3 BSI L F000 SD 0000-0001 Q FAILED      88447380
089F 0 3116      DC      /3116      ERR ID      88447390
08A0 00 44000FB2 G7B2 BSI L F00E CK LOCK ON ERROR      88447400
08A2 0 70E9      MDX      A780      LOOP      88447410
08A3 0 C050      LD      N780      LD C AND OF CONDITION      88447420
08A4 0 F057      EOR      N788      ZERO IF CARRY WAS ON      88447430
08A5 00 4C180BB3 BSC L G784,+- BRANCH ON ZERO      88447440
08A7 00 4C040B80 BSC L H784,E CHECK FOR CARRY      88447450
08A9 00 44000FB3 BSI L F000 CARRY NOT ON      88447460
08AB 0 3117      DC      /3117      ERR ID      88447470
08AC 00 44000FDE BSI L F005 CK LOCK ON ERROR      88447480
08AE 0 70DD      MDX      A780      LOOP      88447490
08AF 0 7003      MDX      G784      88447500
08B0 00 44000FB3 H7B4 BSI L F000 OVFLD ON      88447510
08B2 0 3118      DC      /3118      ERR ID      88447520
08B3 00 44000FDE G7B4 BSI L F005 CK LOCK ON ERROR      88447530
08B5 0 70D6      MDX      A780      LOOP      88447540
*****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
08B6 0 2000      A786 LDS      0      SET C AND OF OFF      88447580
08B7 0 C83E      LDD      N782      LD A=/0000 Q=/0000      88447590
08B8 0 9841      SD      N786      /FFFF /FFFF      88447600
```


PROCESSOR-CONTROLLER FUNCTION TEST

```
08B9 00 4C180B8E      BSC L G786,+--  BRANCH ON ZERO      88447630
08BB 00 44000F83      BSI L F000      SD 0000-FFFF A FAILED  88447640
08B0 0 3119           DC /3119      ERR ID      88447650
08BE 00 44000F82      G786 BSI L F00E      CK LOCK ON ERROR  88447660
08C0 0 70F5           MDX A786      LOOP      88447670
08C1 0 18D0           RTE 16      NOW A=/0001 Q=/0000  88447680
08C2 0 F036           EOR N785      ZERO WITH /0001  88447690
08C3 00 4C180BC8      BSC L G788,+--  BRANCH ON ZERO      88447700
08C5 00 44000F83      BSI L F000      SO 0000-FFFF Q FAILED  88447710
08C7 0 311A           DC /311A      ERR ID      88447720
08C8 00 44000F0E      G788 BSI L F005      CK LOCK ON ERROR  88447730
08CA 0 70EB           MDX A786      LOOP      88447740
*****
08CB 0 C832           A78A LDD N78A      LD A=/0000 Q=/C000  88447750
08CC 0 982D           SD N786      S /FFFF /FFF  88447760
08CD 00 4C180B02      BSC L G78A,+--  BRANCH ON ZERO      88447770
08CF 00 44000F83      BSI L F000      SD 0000-FFFF A FAILED  88447780
08D1 0 311B           DC /311B      ERR ID      88447790
08D2 00 44000F82      G78A BSI L F00E      CK LOCK ON ERROR  88447800
08D4 0 70F6           MDX A78A      LOOP      88447810
08D5 0 18D0           RTE 16      NOW A=/C001 Q=/0000  88447820
08D6 0 F029           EOR N78D      ZERO WITH /C001  88447830
08D7 00 4C180B0C      BSC L G78C,+--  BRANCH ON ZERO      88447840
08D9 00 44000F83      BSI L F000      SD C000-FFFF Q FAILED  88447850
08DB 0 311C           DC /311C      ERR ID      88447860
08DC 00 44000FDE      G78C BSI L F005      CK LOCK ON ERROR  88447870
08DE 0 70EC           MDX A78A      LOOP      88447880
*****
08DF 0 C816           A78E LDD N782      LD A=/0000 Q=/0000  88447890
08E0 0 981A           SD N787      S /FFFF /FFFF  88447900
08E1 00 4C180BE6      BSC L G78E,+--  BRANCH ON ZERO      88447910
08E3 00 44000F83      BSI L F000      SO-0DD A FAILED  88447920
08E5 0 311D           DC /311D      ERR ID      88447930
08E6 00 44000F82      G78E BSI L F00E      CK LOCK ON ERROR  88447940
08E8 0 70F6           MDX A78E      LOOP      88447950
08E9 0 18D0           RTE 16      NOW A=/0001 Q=/0000  88447960
08EA 0 F00E           EOP N785      ZERO WITH /0001  88447970
08EB 00 4C180BF0      BSC L G780,+--  BRANCH ON ZERO      88447980
08ED 00 44000F83      BSI L F000      SO-0DD Q FAILED  88447990
08EF 0 311E           DC /311E      ERR ID      88448000
08F0 00 44000F0E      H780 BSI L F005      CK LOCK ON ERROR  88448010
08F2 0 70EC           MDX A78E      LOOP      88448020
08F3 0 70D0           MDX A7C0      EXIT TO NEXT ROUTINE  88448030
08F4 0 0000           N780 DC /000D      88448040
08F6 0 0000           BSS E      88448050
08F6 0 0000           N782 DC /0000      88448060
08F7 0 0000           OC /0000      88448070
08F8 0 0000           N784 DC /0000      88448080
08F9 0 0001           N785 OC /0001      88448090
08FA 0 FFFF           N786 OC /FFFF      88448100
08FB 0 FFFF           N787 OC /FFFF      88448110
08FC 0 0002           N788 DC /0002      88448120
08FD 0 0000           OC /0000      88448130
08FE 0 0000           N78A DC /0000      88448140
08FF 0 C000           DC /C000      88448150
0C00 0 C001           N78D OC /C001      88448160
*****
```

TEST OF MULTIPLY OPERATION

```
*****
CORE DATA OR *LA- DPER-
ADDR INSTRUCTION *DEL ATION FT OPERANOS + REMARKS ID+SEQ= AT RIGHT
*****
0C01 0 C04F      A7C0 LO N7C0      LO /5555      88448200
0C02 0 A04F      M N7C1      M /2AAA      88448210
0C03 0 F04F      EOR N7C2      ZERD WITH /0E38  88448220
0C04 00 4C180C09      BSC L G7C0,+--  BRANCH ON ZERO  88448230
*****
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
0C06 00 44000F83      BSI L F000      M /5555X/2AAA ACC FAILED  88448310
0C08 0 311F          OC /311F      ERR ID      88448320
0C09 00 44000FB2      G7C0 BSI L F00E      CK LOCK ON ERROR  88448330
0C0B 0 70F5          MOX A7C0      LOOP      88448340
0C0C 0 1800          RTE 16      NOW A=/9C72 Q-/0000  88448350
0C0D 0 F046          EOR N7C3      ZERO WITH /9C72  88448360
0C0E 00 4C180C13      BSC L G7C2,+--  BRANCH ON ZERO      88448370
0C10 00 44000FB3      BSI L F000      MULT 5555X2AAA C FAILED  88448380
0C12 0 3120          DC /3120      ERR ID      88448390
0C13 00 44000FDE      G7C2 BSI L F005      CK LOCK ON ERROR  88448400
0C15 0 70EB          MOX A7C0      LOOP      88448410
*****
0C16 0 C03E          A7C4 LO N7C4      LD /FFFF      88448420
0C17 0 A03D          M N7C4      M /FFFF      88448430
0C18 00 4C180C1D      BSC L G7C4,+--  BRANCH ON ZERO      88448440
0C1A 00 44000F83      BSI L F000      M /FFFFX/FFFF ACC FAILED  88448450
0C1C 0 3121          OC /3121      ERR ID      88448460
0C1D 00 44000FB2      G7C4 BSI L F00E      CK LOCK ON ERROR  88448470
0C1F 0 70F6          MDX A7C4      LOOP      88448480
0C20 0 1800          RTE 16      NOW A=/0001 Q=/0000  88448490
0C21 0 F034          EOR N7C5      ZERO WITH /0001  88448500
0C22 00 4C180C27      BSC L G7C6,+--  BRANCH ON ZERO      88448510
0C24 00 44000F83      BSI L F000      M /FFFFX/FFFF Q REG FAILED  88448520
0C26 0 3122          DC /3122      ERR ID      88448530
0C27 00 44000FDE      G7C6 BSI L F005      CK LOCK ON ERROR  88448540
0C29 0 70EC          MOX A7C4      LOOP      88448550
*****
0C2A 0 C02C          A7C8 LO N7C6      LD /0000      88448560
0C2B 0 A029          M N7C6      M /FFFF      88448570
0C2C 00 4C180C31      BSC L G7C8,+--  BRANCH ON ZERO      88448580
0C2E 00 44000F83      BSI L F000      M /FFFFX/0000 ACC FAILED  88448590
0C30 0 3123          DC /3123      ERR ID      88448600
0C31 00 44000FB2      G7C8 BSI L F00E      CK LOCK ON ERROR  88448610
0C33 0 70F6          MOX A7C8      LOOP      88448620
0C34 0 18D0          RTE 16      NOW A=/0000 Q=/0000  88448630
0C35 00 4C180C3A      BSC L G7CA,+--  BRANCH ON ZERO      88448640
0C37 00 44000F83      BSI L F000      M /FFFFX/0000 Q REG FAILED  88448650
0C39 0 3124          DC /3124      ERR ID      88448660
0C3A 00 44000FDE      G7CA BSI L F005      CK LOCK ON ERROR  88448670
0C3C 0 70ED          MOX A7C8      LOOP      88448680
*****
0C3D 0 C017          A7CC LO N7C4      LD /FFFF      88448690
0C3E 0 A018          M N7C6      M /0000      88448700
0C3F 00 4C180C44      BSC L G7CC,+--  BRANCH ON ZERO      88448710
0C41 00 44000F83      BSI L F000      M /0000X/FFFF ACC FAILED  88448720
0C43 0 3125          OC /3125      ERR ID      88448730
0C44 00 44000FB2      G7CC BSI L F00E      CK LOCK ON ERROR  88448740
0C46 0 70F6          MOX A7CC      LOOP      88448750
0C47 0 1800          RTE 16      NOW A=/0000 Q=/0000  88448760
0C48 00 4C180C40      BSC L G7CE,+--  BRANCH ON ZERO      88448770
0C4A 00 44000FB3      BSI L F000      M /0000X/FFFF Q REG FAILED  88448780
0C4C 0 3126          DC /3126      ERR ID      88448790
0C40 00 44000F0E      G7CE BSI L F005      CK LOCK ON ERROR  88448800
0C4F 0 70ED          MOX A7CC      LOOP      88448810
0C50 0 7007          MDX A800      EXIT TO NEXT ROUTINE  88448820
0C51 0 5555          N7C0 OC /5555      88448830
0C52 0 2AAA          N7C1 OC /2AAA      88448840
0C53 0 0E38          N7C2 DC /0E38      88448850
0C54 0 9C72          N7C3 DC /9C72      88448860
0C55 0 FFFF          N7C4 OC /FFFF      88448870
0C56 0 0001          N7C5 OC /0001      88448880
0C57 0 0000          N7C6 DC /0000      88448890
*****
```

TEST OF DIVIOE OPERATION

```
*****
CORE DATA OR *LA- OPER-
*****
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
ADDR INSTRUCTION *BEL ATION FT DPERANDS + REMARKS ID+SEQ= AT RIGHT 88448990
*****
OC58 0 2000 A800 LDS 0 SET C AND OF OFF 88449000
OC59 00 CC000CF6 LDD L N802 LO A=/400C Q=/7FFF 88449010
OC5B 00 AC000D06 D L N812 D /8000 88449020
OC5D 00 2C000CF5 STS L N800 STORE C AND OF CONDITION 88449030
OC5F 00 F4000D06 EDR L N812 ZERO WITH /8000 88449040
OC61 00 4C180C66 BSC L G800,+ BR ON ZERO 88449050
OC63 00 44000F83 BSI L F000 DVD-A-REG INCORRECT 88449060
OC65 0 3127 DC /3127 ERR IO 88449070
OC66 00 44000FB2 G800 BSI L F00E CK LOCK ON ERROR 88449080
OC68 0 70EF MDX A800 LOOP 88449090
OC69 0 1800 RTE 16 NOW A=/7FFF Q=/0000 88449100
OC6A 00 F4000D05 EDR L N811 ZERO WITH /7FFF 88449110
OC6C 00 4C180C71 BSC L G802,+ BR ON ZERO 88449120
OC6E 00 44000F83 BSI L F000 OVD-Q REG INCORRECT 88449130
OC70 0 3128 DC /3128 ERR IO 88449140
OC71 00 44000FB2 G802 BSI L F00E CK LOCK ON ERROR 88449150
OC73 0 70E4 MDX A800 LOOP 88449160
OC74 00 C4000CF5 LO L N800 LD /0000 88449170
OC76 00 4C180C84 BSC L G804,+ BR ON ZERO 88449180
OC78 00 4C040C81 BSC L H804,E BR ON NOT EVEN 88449190
OC7A 00 44000F83 BSI L F000 CARRY ON 88449200
OC7C 0 3129 DC /3129 ERR ID 88449210
OC7D 00 44000F0E BSI L F005 CK LOCK ON ERROR 88449220
OC7F 0 7008 MDX A800 LOOP 88449230
OC80 0 7006 MDX A806 EXIT TO NEXT ROUTINE 88449240
OC81 00 44000F83 H804 BSI L F000 OVFLO ON 88449250
OC83 0 312A DC /312A ERR ID 88449260
OC84 00 44000F0E G804 BSI L F005 CK LOCK ON ERROR 88449270
OC86 0 70D1 MDX A800 LODP 88449280
***** 88449290
OC87 0 C870 A806 LOO N804 LD A=/1C71 Q=/B8E3 88449300
OC88 00 AC000D07 D L N813 D /5555 88449310
OC8A 0 286A STS N800 STORE C AND OF CONDITION 88449320
OC8B 00 F4000D07 EDR L N813 ZERO WITH /5555 88449330
OC8D 00 4C180C92 BSC L G806,+ BR ON ZERO 88449340
OC8F 00 44000F83 BSI L F000 OVD-A REG INCORRECT 88449350
OC91 0 312B DC /312B ERR IO 88449360
OC92 00 44000FB2 G806 BSI L F00E CK LOCK ON ERRDR 88449370
OC94 0 70F2 MDX A806 LODP 88449380
OC95 0 1800 RTE 16 NOW A=/B8E3 Q=/0000 88449390
OC96 00 F4000D08 EDR L N816 ZERO WITH /2DAA 88449400
OC98 00 4C180C9D BSC L G808,+ BR ON ZERO 88449410
OC9A 00 44000F83 BSI L F000 DVD-Q REG INCORRECT 88449420
OC9C 0 312C DC /312C ERR ID 88449430
OC9D 00 44000FB2 G808 BSI L F00E CK LOCK ON ERROR 88449440
OC9F 0 70E7 MDX A806 LODP 88449450
OCA0 0 C054 LO N800 LD C AND OF CONDITION 88449460
OCA1 00 4C1B0CAF BSC L G80A,+ BR ON ZERO 88449470
OCA3 00 4C040CAC BSC L H80A,E BR IF NOT EVEN 88449480
OCA5 00 44000F83 BSI L F000 CARRY ON 88449490
OCA7 0 3120 DC /3120 ERR ID 88449500
OCA8 00 44000FDE BSI L F005 CK LOCK ON ERROR 88449510
OCA9 0 70DC MDX A806 LODP 88449520
OCAB 0 7006 MDX A80C EXIT TO NEXT ROUTINE 88449530
OCAC 00 44000F83 H80A BSI L F000 OVFLO CN 88449540
OCAE 0 312E DC /312E ERR IO 88449550
OCAF 00 44000F0E G80A BSI L F005 CK LOCK ON ERROR 88449560
OCB1 0 70D5 MDX A806 LOOP 88449570
***** 88449580
CORE DATA OR *LA- OPER- 88449590
ADDR INSTRUCTION *BEL ATION FT DPERANDS + REMARKS ID+SEQ= AT RIGHT 88449600
***** 88449610
OCB2 0 2000 A80C LOS 0 SET C AND OF OFF 88449620
OCB3 0 C846 LDO N806 SET A=/0000 Q=/0001 88449630
OCB4 0 A840 0 N80E 0 /0000 88449640
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
OCB5 00 4C010CBA BSC L G80C,D BRANCH CN OVERFLOW 88449670
OCB7 00 44000F83 BSI L F000 DVD BY 0-DVRFLW CFF 88449680
OCB9 0 312F DC /312F ERR IO 88449690
OCBA 00 44000FDE G80C BSI L F005 CK LOCK ON ERROR 88449700
OCBC 0 70F5 MDX A80C LODP 88449710
***** 88449720
OCBD 0 2000 A80E LDS 0 SET C AND OF OFF 88449730
OCBE 0 C83D LDO N808 LO A=/4000 Q=/0000 88449740
OCBF 0 A838 D N807 D /0001 88449750
OCC0 00 4C010CC5 BSC L G80E,D BRANCH DN OVERFLOW 88449760
OCC2 00 44000F83 BSI L F000 DVD-BY 1-DVRFLW CFF 88449770
OCC4 0 3130 DC /3130 ERR IO 88449780
OCC5 00 44000FDE G80E BSI L F005 CK LOCK ON ERRDR 88449790
OCC7 0 70F5 MDX A80E LODP 88449800
***** 88449810
OCC8 0 2000 B800 LDS 0 SET C AND OF OFF 88449820
OCC9 0 C834 LDD N80A LO A=/A000 Q=/0000 88449830
OCCA 0 A831 D N808 D /4000 88449840
OCCB 00 4C010CDB BSC L J800,D BRANCH CN OVERFLOW 88449850
OCCD 00 44000F83 BSI L F000 DVD/4000-DVRFLW CFF 88449860
OCCF 0 3131 DC /3131 ERR IO 88449870
OCD0 00 44000FDE J800 BSI L F005 CK LOCK ON ERRDR 88449880
OCD2 0 70F5 MDX B800 LODP 88449890
***** 88449900
OCD3 0 2000 B802 LDS 0 SET C AND OF OFF 88449910
OCD4 0 C82B LDD N80C LD A=/C000 Q=/0000 88449920
OCD5 0 A830 D N812 D /8000 88449930
OCD6 00 4C010CDB BSC L J802,D BR ON OF 88449940
OCD8 00 44000F83 BSI L F000 OVD/8000-DVRFLW OFF 88449950
OCD9 0 3132 DC /3132 ERR ID 88449960
OCD8 00 44000FDE J802 BSI L F005 CHECK LODP SWITCH 88449970
OCD0 0 70F5 MDX B802 LODP 88449980
***** 88449990
OCDE 0 2000 B804 LDS 0 SET C AND OF OFF 88450000
OCDF 0 C822 LDD N80E LO A=/0000 Q=/FFFF 88450010
OCE0 0 A81A D N807 D /0001 88450020
OCE1 00 4C010CE6 BSC L J804,D BR ON OF 88450030
OCE3 00 44000F83 BSI L F000 DVD/0001-DVRFLW OFF 88450040
OCE5 0 3133 DC /3133 ERR IO 88450050
OCE6 00 44000FDE J804 BSI L F005 CK LOCK ON ERROR 88450060
OCE8 0 70F5 MDX B804 LODP 88450070
***** 88450080
OCE9 0 2000 B806 LDS 0 SET C AND OF OFF 88450090
OCEA 0 C819 LDO N810 LD A=/FFFF Q=/7FFF 88450100
OCEB 0 A80F D N807 D /0001 88450110
OCEC 00 4C010CF1 BSC L J806,D BR ON OF 88450120
OCEE 00 44000F83 BSI L F000 DVD/0001-DVRFLW CFF 88450130
OCF0 0 3134 DC /3134 ERR IO 88450140
OCF1 00 44000FDE J806 BSI L F005 CK LOCK ON ERROR 88450150
OCF3 0 70F5 MDX B806 LODP 88450160
OCF4 0 7023 MDX B807 EXIT TO NEXT ROUTINE 88450170
OCF5 0 0000 N800 OC /0000 88450180
OCF6 0 0000 BSS E 88450190
OCF6 0 4000 N802 OC /4000 88450200
OCF7 0 7FFF DC /7FFF 88450210
OCF8 0 1C71 N804 OC /1C71 88450220
OCF9 0 B8E3 OC /B8E3 88450230
OCFA 0 0000 N806 DC /0000 88450240
OCFB 0 0001 N807 DC /0001 88450250
OCFC 0 4000 N808 DC /4000 88450260
OCFD 0 0000 OC /0000 88450270
OCFE 0 A000 N80A DC /A000 88450280
OCFF 0 0000 OC /0000 88450290
OD00 0 C000 N80C OC /C000 88450300
OD01 0 0000 OC /0000 88450310
OD02 0 0000 N80E DC /0000 88450320
OD03 0 FFFF N80F DC /FFFF 88450330
OD04 0 FFFF N810 OC /FFFF 88450340
```


PROCESSOR-CONTROLLER FUNCTION TEST

PROCESSOR-CONTROLLER FUNCTION TEST

0005 0 7FFF	N811 DC	/7FFF	88450350	
0006 0 8000	N812 DC	/8000	88450360	
0007 0 5555	N813 DC	/5555	88450370	
0008 0 2DAA	N816 DC	/2DAA	88450380	
0009 0 C000	N817 DC	/C000	88450390	
000A 0 6100	N818 DC	/6100	88450400	
000B 0 0000	DC	/0000	88450410	
000C 0 8000	N819 DC	/8000	88450420	
000D 0 0000	DC	/0000	88450430	
000E 0 0002	N820 DC	/0002	88450440	
000F 0 0000	N821 DC	0	88450450	
0010 0 2001	DC	/2001	88450460	
0011 0 4000	DC	/4000	88450470	
0012 0 C000	DC	/C000	88450480	
0013 0 8000	N822 DC	/8000	88450490	
0014 0 FFFF	N823 DC	/FFFF	88450500	
0015 0 FFFF	DC	/FFFF	88450510	
0016 0 0000	BSS E 0		88450520	
0016 0 0000	N824 DC	0	88450530	
0017 0 0000	DC	0	88450540	

CORE DATA OR *LA- OPER-				88450550
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT				88450560
*****				88450570
0018 0 2000	B807 LDS	0	SET C AND CF OFF	88450580
0019 0 C8F0	LDD	N818	LD A=/6100 Q=/0000	88450590
001A 0 A8EE	D	N817	D /C000	88450600
001B 00 4C010D20	BSC L	J808,C	BR ON DF	88450610
001D 00 4400DF83	BSI L	F000	OVERFLOW OFF	88450620
001F 0 316A	DC	/316A	ERR ID	88450630
0020 00 4400CFDE	J808 BSI L	F005	CK LOCK ON ERROR	88450640
0022 0 70F5	MDX	B807	LOOP	88450650
*****				88450660
0023 0 2000	B808 LDS	0	SET C AND CF OFF	88450670
0024 0 C8F7	LDD	N819	LD A=/8000 Q=/0000	88450680
0025 0 A8D0	D	N80F	D /FFFF	88450690
0026 00 4C010D2B	BSC L	J809,C	BR ON DF	88450700
0028 00 4400DF83	BSI L	F000	OVERFLOW OFF	88450710
002A 0 316B	DC	/316B	ERR ID	88450720
002B 00 4400CFDE	J809 BSI L	F005	CK LOCK ON ERROR	88450730
002D 0 70F5	MDX	B808	LOOP	88450740
*****				88450750
002E 0 2000	B809 LDS	0	SET C AND CF OFF	88450760
002F 0 C8E4	LDD	N823	LD A=/FFFF Q=/FFFF	88450770
0030 0 A8D0	D	N820	D /0002	88450780
0031 00 4C010D34	BSC L	J815,C	BR ON DF	88450790
0033 0 7003	MDX	J810	OVERFLOW OFF	88450800
0034 00 4400DFB3	J815 BSI L	F000		88450810
0036 0 316C	DC	/316C	ERR ID	88450820
0037 00 4400CFDE	J810 BSI L	F005	CK LOCK ON ERROR	88450830
0039 0 70F4	MDX	B809	LOOP	88450840
*****				88450850
*****				88450860
*****				88450870
*****				88450880
*****				88450890
*****				88450900
MULTIPLY-DIV TEST (B810I)				88450910
*****				88450920
*****				88450930
THIS TEST TAKES 4 NUMBERS				88450940
/8000, /C000, /4000 AND				88450950
/2001 AND MULTIPLIES AND				88450960
DIVIDES THE RESULT OF THE				88450970
MULTIPLICATION BY ALL				88450980
VALUES OF NEGATIVE AND				88450990
POSITIVE NUMBERS. THIS				88451000
PROCEDURE IS REPEATED				88451010
UNTIL ALL FOUR NUMBERS				88451020

			HAVE BEEN USED.	88451030
				88451040
			STEP1 SET MULTIPLICAND AND	88451050
			DIVISOR TO LARGEST NEG.	88451060
			NUMBER.	88451070
			STEP2 TAKE ONE OF FOUR NUMBERS	88451080
			AND USE IT AS THE	88451090
			MULTIPLIER	88451100
			STEP3 MULTIPLY	88451110
			STEP4 STORE RESULTS IN SYMBOLIC	88451120
			LOCATION N824	88451130
			STEP5 DIVIDE	88451140
			STEP6 CHECK RESULT	88451150
			STEP7 INCREMENT MULTIPLICAND	88451160
			AND DIVISOR BY 1.	88451170
			STEP8 GO TO STEP 2 IF ALL	88451180
			VALUES HAVE NOT BEEN	88451190
			USED AS MULTIPLICANDS AND	88451200
			DIVISORS.	88451210
			STEP9 SET UP FOR NEXT ONE OF 4	88451220
			MULTIPLIERS.	88451230
			STEP10 GO TO STEP 2 IF ALL 4	88451240
			NUMBERS HAVE NOT BEEN	88451250
			USED.	88451260
				88451270
				88451280
				88451290
			NOTE -- THREE WORD LOCATIONS ARE AVAILABLE FOR	88451300
			MANUAL INSERTION OF ANY VALUE DESIRED.	88451310
			THEY ARE AT LABEL ADDRESS N821+1, N821+2	88451320
			AND N821+3.	88451330
				88451340
			CAUTION ** DO NOT CHANGE THE WORD AT LABEL	88451350
			LOCATION N822 (/8000).	88451360
				88451370
			*****	88451380
			*LA- OPER-	88451390
			DEFINITION OF OPERANDS + REMARKS ID+SEQ= AT RIGHT	88451400
			*****	88451410
			DX 1 4 LD XR 1 WITH /0C04	88451420
J814	X10 L F003		CK BYPASS MPY/DIV SW	88451430
	LD L Z000		LD SWITCH SETTINGS	88451440
	SRA 8		SHIFT BIT 7 TO BIT PDS 15	88451450
	BSC E		SK IF BIT 15=0	88451460
	MDX A840		SW BIT 6 DN (BYPASS)	88451470
	LD N822		CONST /8000	88451480
	STO N821		STORE /8000 AT N821	88451490
J811	LD N821		LD C(N821) /8000	88451500
	M L1 N821			88451510
	STD N824		STORE A AND Q	88451520
	LDS 0		SET C AND OF DFF	88451530
	D N821		D /8000	88451540
	EOR L1 N821		ZERO WITH /8000	88451550
	BSC L J812,+-		BRANCH ON ZERO	88451560
	BSI L F000		ACC NOT ZERO	88451570
	DC /316D		ERR ID	88451580
J812	BSI L F00E		CK LOCK ON EKRDR	88451590
	MDX J811		LDDP ON MPL/DIV	88451600
	RTE 16		NDW A=/0000 C=/0000	88451610
	BSC L J813,+-		BRANCH ON ZERO	88451620
	BSI L F000		REMAINDER IN Q REG	88451630
	DC /316E		ERR ID	88451640
J813	BSI L F00E		CK LOCK ON FRRDR	88451650
	MDX J811		LDDP ON MPL/DIV	88451660
J816	LD N821		LD /8000	88451670
	A N807		ADD ONE	88451680
	STO N821			88451690
	BSC L J816,+-		BRANCH ON ZERO	88451700



PROCESSOR-CONTROLLER FUNCTION TEST

```
0062 0 F080      EOR      NB22
0063 00 4C200D44  BSC L J811,+2  BR IF NOT ZERO
0065 0 71FF      MOX      1 -1
0066 0 7004      MDX      J814      LOOP TO CK SWITCHES
0067 00 44000FDE BSI L F005      CK LOCK ON ERROR
0069 0 7000      MOX      B810      LOOP
*****
TEST OF MDX OPERATION
*****
006A 0 6100      AB40 LDX      1 0      LO XR 1 WITH ZERO
006B 0 71FF      MDX      1 -1      SK IF SIGN CHANGES
006C 0 3000      WAIT
006D 0 696E      STX      1 NB40      STORE C(XR 1) AT NB40
006E 0 C060      LD       NB40      LO VALUE OF XR 1
006F 0 F06D      EOR      NB41      ZERO ACC WITH /FFFF
0070 00 4C180D75 BSC L G840,+-- BRANCH ON ZERO
0072 00 44000F83 BSI L F000      MDX XR 1 FAILED
0074 0 3135      DC       /3135      ERR ID
0075 00 44000FDE BSI L F005      CK LOCK ON ERROR
0077 0 70F2      MDX      AB40      LOOP
*****
0078 0 C069      AB42 LD       NB45      LD WITH ADDR OF
*                *                * LABEL NB44
0079 00 740100DE MDX L NB42,+1 BR TO LABEL ADDR NB42 +1
007B 0 F066      EOR      NB45
007C 00 4C180D81 BSC L H842,+-- BRANCH ON ZERO
007E 00 44000F83 BSI L F000      ACC DISTROYED AFTER MDX
0080 0 316F      DC       /316F      ERR ID
0081 0 C05C      H842 LD       NB42      LO A=/3000
0082 0 F060      EOR      NB46      ACC NOW /0001
0083 00 4C180D88 BSC L G842,+-- BRANCH ON ZERO
0085 00 44000F83 BSI L F000      ADD TO MEM FAILED
0087 0 3136      DC       /3136      ERR ID
0088 0 C057      G842 LD       NB43      LD /3000
0089 0 0054      STC      NB42
008A 00 44000FDE BSI L F005      CK LOCK ON ERROR
008C 0 70EB      MDX      AB42      LOOP
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0080 00 6600FFFE AB44 LDX L2 -2      LD XR 2 WITH -2
008F 00 76000001 MDX L2 1      ADD ONE TO XR 2
0091 0 6A4A      STX      2 NB40      STORE XR 2
0092 0 C049      LD       NB40      LO WITH XR 2 VALUE
0093 0 F049      FOR      NB41      ZERO ACC WITH /FFFF
0094 00 4C180D99 BSC L G844,+-- BRANCH ON ZERO
0096 00 44000F83 BSI L F000      MDX LONG XR 2 FAILED
0098 0 3137      DC       /3137      ERR ID
0099 00 44000FDE G844 BSI L F005      CK LOCK ON ERROR
009B 0 70F1      MDX      AB44      LOOP
*****
009C 0 63FF      AB46 LDX      3 -1      LD XR 3 WITH -1
009D 0 7301      MDX      3 1      ADD ONE TO XR 3
009E 0 7001      MOX      G846      OIO NOT SK ON MDX
009F 0 7003      MOX      H846
00A0 00 44000F83 G846 BSI L F000      XR 3 NO SKIP AT 0
00A2 0 3138      DC       /3138      ERR ID
00A3 00 44000FDE BSI L F005      CK LOCK ON ERROR
00A5 0 70F6      MOX      AB46      LOOP
*****
00A6 0 61FF      AB48 LDX      1 -1      LO XR 1 WITH -1
00A7 0 7104      MDX      1 4      ADD 4 TO XR 1
00A8 0 7001      MOX      G848      DID NOT SK ON MDX
00A9 0 7J03      MOX      H848
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
00AA 00 44000F83 G848 BSI L F000      SIGN CHANGE-NO SKIP
00AC 0 3139      DC       /3139      ERR ID
00AD 00 44000FDE H848 BSI L F005      CK LOCK ON ERROR
00AF 0 70F6      MOX      AB48      LOOP
*****
00B0 0 6500FFFE AB49 LDX LI -2      LD XR 1 WITH -2
00B2 0 C0FF      H849 LD       H849
00B3 00 75800DE2 MDX      11 NB45
00B5 0 6926      STX      1 NB40      STORE C(XR 1) AT NB40
00B6 0 F0F9      EOR      H849
00B7 00 4C180DBC BSC L K849,+-- BRANCH ON ZERO
00B9 00 44000F83 BSI L F000      ACC GONE AFTER MDX INDEXED
00CB 0 3168      DC       /3168      ERR ID
00CC 0 C01F      K849 LD       NB40      LD VALUE OF XR 1 AFTER
*                *                * MDX OP
00BD 0 F01F      EOR      NB41      ZERO ACC WITH /FFFF
00BE 00 4C180DC3 BSC L G849,+-- BRANCH ON ZERO
00C0 00 44000F83 BSI L F000      INDIRECT MDX FAILED
00C2 0 313A      DC       /313A      ERR ID
00C3 00 44000FDE G849 BSI L F005      CK LOCK ON ERROR
00C5 0 70EA      MDX      AB49      LOOP
*****
00C6 00 7400CDDA AB4A MDX      1 NB4A,C      TEST SKIP IF ZERO
00C8 0 7001      MOX      G84A      BYPASS IF CORRECT OP
00C9 0 7003      MDX      H84A
00CA 00 44000F83 G84A BSI L F000      MDX L FAILED TO SKIP
00CC 0 3171      DC       /3171      ERR ID
00CD 00 44000FDE H84A BSI L F005      CK LOCK ON ERROR
00CF 0 70F6      MDX      AB4A      LOOP
*****
00D0 00 7400CDD8 AB5A MDX L NB5A,C      TEST ACC SKIP
00D2 0 7003      MDX      H85A
00D3 00 44000F83 BSI L F000      MDX L SKIPPED
00D5 0 3172      DC       /3172      ERR ID
00D6 00 44000FDE H85A BSI L F005      CK LOCK ON ERROR
00D8 0 70F7      MDX      AB5A      LOOP
00D9 0 700A      MDX      AB80      EXIT TO NEXT ROUTINE
00DA 0 0000      NB4A DC       0      CONSTANT ZERO
00DB 0 0001      NB5A DC       1      NON ZERO CONSTANT
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
00DC 0 0000      NB40 DC       /0000
00DD 0 FFFF      NB41 DC       /FFFF
00DE 0 3000      NB42 WAIT      ADD TO MEM FAILED
00DF 0 3000      WAIT      ADD TO MEM FAILED
00E0 0 3000      NB43 WAIT      ADD TO MEM FAILED
00E1 0 0001      NB44 DC       /0001
00E2 0 00E1      NB45 DC       NB44
00E3 0 3001      NB46 DC       /3001
*
TEST OF SLC OPERATION
*****
00E4 0 610A      AB80 LDX      1 10      LD XR 1 WITH +10
00E5 00 CC000E0E LDO L NB82      LD A=/0000 Q=/FFFF
00E7 0 2002      LDS      2      SET C ON
00E8 0 1140      SLCA      1 0      NOW A=/0000 Q=/FFFF
00E9 00 60000E0C STX LI NB80      STORE C(XR 1)
00EB 0 2B12      STS      G881      STORE CARRY CONDITION
00EC 00 4C180DF1 BSC L G880,+-- BRANCH ON ZERO
00EE 00 44000F83 BSI L F000      ACC NOT=ZERO
00F0 0 3138      DC       /3138      ERR ID
00F1 00 44000F82 G880 BSI L F00E      CK LOCK ON ERROR
00F3 0 70F0      MDX      AB80      LOOP
00F4 00 C4000E0C LD L NB80      LD PREVIOUS C(XR 1)
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
UDF6 00 4C18UDF8      8SC L G882,+--  BRANCH DN ZERO      88453070
UDF8 00 4400UDF83      BSI L F000      XR 1 NOT=ZERO      88453080
UDFA 0 313C            DC /313C      ERR ID      88453090
UDFB 00 4400UDF82      G882 BSI L F00E      CK LOCK ON ERROR  88453100
UDFO 0 70E6            MDX A880      LOOP      88453110
UDFE 0 2000            G881 LDS G      SAVED BY STS ABOVE    88453120
UDFF 0 4802            8SC C      SK IF CARRY OFF      88453130
UE00 0 7004            MDX G883      CARRY ON      88453140
UE01 00 4400UDFDE      8SI L F005      CK LOCK ON ERROR  88453150
UE03 0 70E0            MDX A880      LOOP      88453160
UE04 0 7006            MDX A884      EXIT TO NEXT ROUTINE  88453170
UE05 00 4400UDF83      G883 8SI L F000      CARRY ON (SHOULD NOT BE) 88453180
UE07 0 3160            DC /3160      ERR ID      88453190
UE08 00 4400UDFDE      8SI L F005      CK LOCK ON ERROR  88453200
UE0A 0 70D9            MDX A880      LOOP      88453210
*****
UE0B 00 6580UEE3      A884 LDX I1 N887      LD XR 1 WITH /FFD0    88453220
UE0D 00 CC00UEF0      LDD L N884      LD A=/0001 Q=/0010    88453230
UE0F 0 2000            LDS 0      SET C AND OF OFF      88453240
UE10 0 1140            SLCA 1 0      ACC NOW /8000      88453250
UE11 0 2816            STS G885      STORE C AND OF CONDITION 88453260
UE12 00 F400UEE2      EOR L N886      ZERO WITH /8000      88453270
UE14 00 4C18UE19      8SC L G884,+--  BRANCH DN ZERO      88453280
UE16 00 4400UDF83      BSI L F000      ACC NOT=/8000      88453290
UE18 0 313D            DC /313D      ERR ID      88453300
UE19 00 4400UDF82      G884 8SI L F00E      CHECK LOOP SWITCH  88453310
UE1B 0 70EF            MDX A884      LOOP      88453320
UE1C 00 6000UEDC      STX L1 N880      STORE C(XR 1) AT N880 88453330
UE1E 00 C400UEDC      LD L N880      LD C(N880)      88453340
UE20 00 F400UEEA      EOR L N88E      ZERO WITH /FF01      88453350
UE22 00 4C18UE27      8SC L G886,+--  BRANCH DN ZERO      88453360
UE24 00 4400UDF83      BSI L F000      XR-1 NOT FF01      88453370
UE26 0 313E            DC /313E      ERR ID      88453380
UE27 00 4400UDF82      G886 8SI L F00E      CK LOCK ON ERROR  88453390
UE29 0 70E1            MDX A884      LOOP      88453400
UE2A 0 2000            G885 LDS 0      SAVED BY STS ABOVE  88453410
UE2B 0 4802            8SC C      SK IF CARRY OFF      88453420
UE2C 0 7003            MDX G887      CARRY OFF (SHOULD BE ON) 88453430
UE2D 00 4C00UDF83      8SC L F000      CARRY OFF (SHOULD BE ON) 88453440
UE2F 0 3161            DC /3161      ERR ID      88453450
UE30 00 4400UDFDE      G887 8SI L F005      CK LOCK ON ERROR  88453460
UE32 0 70D8            MDX A884      LOOP      88453470
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL AT IDN FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
UE33 00 6580UEE1      A884 LDX I1 N885      LD XR 1 WITH /0010    88453480
UE35 00 CC00UEE2      LDD L N886      LD A=/8000 Q=/FF00    88453490
UE37 0 1140            SLCA 1 0      ACC NOW /8000      88453500
UE38 00 F400UEE2      EOR L N886      ZERO WITH /8000      88453510
UE3A 00 4C18UE3F      8SC L G888,+--  BRANCH DN ZERO      88453520
UE3C 00 4400UDF83      BSI L F000      ACC NOT=8000      88453530
UE3E 0 313F            DC /313F      ERR ID      88453540
UE3F 00 4400UDF82      G884 8SI L F00E      CK LOCK ON ERROR  88453550
UE41 0 70FI            MDX A888      LOOP      88453560
UE42 00 6000UEDC      STX L1 N880      STORE C(XR 1) IN N880 88453570
UE44 00 C400UEDC      LD L N880      LD C(N880)      88453580
UE46 00 F400UEE1      EOR L N885      ZERO WITH /0010      88453590
UE48 00 4C18UE4D      8SC L G88A,+--  BRANCH DN ZERO      88453600
UE4A 00 4400UDF83      BSI L F000      XR 1 NOT=0010      88453610
UE4C 0 3140            DC /3140      ERR ID      88453620
UE4D 00 4400UDFDE      G884 8SI L F005      CK LOCK ON ERROR  88453630
UE4F 0 70F3            MDX A888      LOOP      88453640
*****
UE50 0 6110            A889 LDX 1 16      LD XR 1 WITH /0010    88453650
UE51 0 6210            LDX 2 16      LD XR 2 WITH /0010    88453660
UE52 0 6310            LDX 3 16      LD XR 3 WITH /0010    88453670
```

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```
OE53 00 C400UEE0      LD L N884      LD A=/0001      88453750
OE55 0 1041            SLCA 1      ACC NOW /0002      88453760
OE56 00 F400UEE7      EOR L N888      ZERO WITH /0002      88453770
OE58 00 4C18UE50      8SC L G889,+--  BRANCH DN ZERO      88453780
OE5A 00 4400UDF83      BSI L F000      NON INDEXED SLCA FAILED 88453790
OE5C 0 3162            DC /3162      ERR ID      88453800
OE5D 00 4400UDFDE      G889 8SI L F005      CK LOCK ON ERROR  88453810
OE5F 0 70F0            MDX A889      LOOP      88453820
*****
OE60 0 6110            A88A LDX 1 16      LD XR 1 WITH /0010    88453830
OE61 0 6210            LDX 2 16      LD XR 2 WITH /0010    88453840
OE62 0 6310            LDX 3 16      LD XR 3 WITH /0010    88453850
OE63 00 CC00UEDE      LDD L N882      LD A=/0000 Q=/FFFF    88453860
OE65 0 10CF            SLC 15      NOW A-/7FFF Q=/1000  88453870
OE66 00 F400UEE8      EOR L N88F      ZERO WITH /7FFF      88453880
OE68 00 4C18UE68      8SC L G88S,+--  NON INDEXED SLC FAILED 88453890
OE6A 0 3173            DC /3173      ERR ID      88453900
OE6B 00 4400UDFDE      G888 8SI L F005      CK LOCK ON ERROR  88453910
OE6D 0 70F2            MDX A88A      LOOP      88453920
*****
OE6E 00 6580UEE8      A88C LDX I1 N88C      LD XR 1 WITH /0020    88453930
OE70 0 C873            LDD N88E      LD A=/0000 Q=/0000    88453940
OE71 0 11C0            SLC 1 0      ACC NOW A=/0000 Q=/0000 88453950
OE72 00 4C18UE77      8SC L G88C,+--  BRANCH DN ZERO      88453960
OE74 00 4400UDF83      BSI L F000      ACC NOT=0000      88453970
OE76 0 3141            DC /3141      ERR ID      88453980
OE77 00 4400UDF82      G88C 8SI L F00E      CK LOCK ON ERROR  88453990
OE79 0 70F4            MDX A88C      LOOP      88454000
OE7A 0 18D0            RTE 16      ACC NOW A=/0000 Q=/0000 88454010
OE7B 00 4C18UE80      8SC L G88F,+--  BRANCH DN ZERO      88454020
OE7D 00 4400UDF83      BSI L F000      Q REG NOT=0000      88454030
OE7F 0 3142            DC /3142      ERR ID      88454040
OE80 00 4400UDF82      G88E 8SI L F00E      CK LOCK ON ERROR  88454050
OE82 0 70E8            MDX A88C      LOOP      88454060
OE83 0 6958            STX 1 N880      STORE C(XR 1) IN N880 88454070
OE84 0 C057            LD N880      LD C(N880)      88454080
OE85 00 4C18UE8A      8SC L J880,+--  BRANCH DN ZERO      88454090
OE87 00 4400UDF83      BSI L F000      XR 1 NOT=0000      88454100
OE89 0 3143            DC /3143      ERR ID      88454110
OE8A 00 4400UDFDE      J880 8SI L F005      CK LOCK ON ERROR  88454120
OE8C 0 70E1            MDX A88C      LOOP      88454130
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL AT IDN FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OE8D 00 6580UEE9      B882 LDX I1 N88D      LD XR 1 WITH /FFDF    88454140
OE8F 0 C856            LDD N88A      LD A=/0000 Q=/0002    88454150
OE90 0 11C0            SLC 1 0      NOW A=/0000 Q=/0000    88454160
OE91 0 0F05            EOR N886      ZERO WITH /8000      88454170
OE92 00 4C18UE97      8SC L J882,+--  BRANCH DN ZERO      88454180
OE94 00 4400UDF83      BSI L F000      ACC NOT=/8000      88454190
OE96 0 3144            DC /3144      ERR ID      88454200
OE97 00 4400UDF82      J882 8SI L F00E      CK LOCK ON ERROR  88454210
OE99 0 70F3            MDX 8882      LOOP      88454220
OE9A 0 18D0            RTE 16      NOW A=/0000 Q=/8000    88454230
OE9B 00 4C18UEA0      8SC L J884,+--  BRANCH DN ZERO      88454240
OE9D 00 4400UDF83      BSI L F000      Q REG NOT=0000      88454250
OE9F 0 3145            DC /3145      ERR ID      88454260
OEAO 00 4400UDF82      J884 8SI L F00E      CK LOCK ON ERROR  88454270
OEAA 0 70EA            MDX 8882      LOOP      88454280
OEAB 0 6938            STX 1 N880      STORE C(XR 1) AT N880 88454290
OEAC 0 C037            LD N880      LD C(N880)      88454300
OEAD 00 F400UEEA      EOR L N88E      ZERO WITH /FF01      88454310
OEA7 00 4C18UEAC      8SC L J886,+--  BRANCH DN ZERO      88454320
OEA9 00 4400UDF83      BSI L F000      XR-1 NOT FF01      88454330
OEAB 0 3146            DC /3146      ERR ID      88454340
```

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```
UFAC 0J 4400FDE  J886 BSI L F005  CK LOCK DN ERROR  88454430
OEAE 0 70DE      MDX  8882  LOOP  88454440
*****
OEAF 0 C836      LOD  N88A  LD A=/0000 Q=/00D2  88454450
OEBO 0 611F      LDX  1 31  LD XR 1 WITH /001F  88454460
OEB1 0 11C0      SLC  1 0   NOW A=/8000 Q=/0000  88454470
OEB2 0 4802      BSC  C     SK IF CARRY OFF  88454480
OEB3 0 7003      MDX  J887  CARRY ON  88454490
OEB4 0J 4400F83  BSI L F000  CARRY NOT ON  88454500
OEB5 0 3147      DC  /3147  ERR ID  88454510
OEB6 0J 4400F82  J887 BSI L F00E  CK LOCK ON ERROR  88454520
OEB7 0 70F5      MDX  8884  LOOP  88454530
OEB8 0 F027      EOR  N886  ZERC WITH /8000  88454540
OEB9 0 4C180ECO  BSC L J888,+  BRANCH ON ZERO  88454550
OEBD 0 4400F83  BSI L F000  ACC NOT EQUAL 8000  88454560
OEBF 0 3148      DC  /3148  ERR ID  88454570
OEC0 0J 4400F82  J888 BSI L F0DE  CK LOCK ON ERROR  88454580
OEC2 0 70EC      MDX  8884  LOOP  88454590
OEC3 0J 6D000EDC STX LI N880  STORE XR 1 WITH C(N880)  88454600
OEC5 0 C016      LD  N880  LD C(N880)  88454610
OEC6 0 F019      EOR  N884  ZERO WITH /0001  88454620
OEC7 0 4C180ECC BSC L J889,+  BRANCH ON ZERO  88454630
OEC9 0J 4400F83  BSI L F000  XR 1 NOT EQUAL 0001  88454640
OECB 0 3149      DC  /3149  ERR ID  88454650
OEC C 0J 4400FDE J889 BSI L F005  CK LOCK ON ERROR  88454660
OEE 0 70E0      MDX  8884  LDOP  88454670
*****
OECF 0 611C      LDX  1 28  LD XR 1 WITH /001C  88454680
OED0 0 C815      LOD  N88A  LD A=/0000 Q=/0002  88454690
OED1 0 1100      SLA  1 0   NOW A=/2000 Q=/0000  88454700
OED2 0 4802      BSC  C     SKIP IF CARRY OFF  88454710
OED3 0 7001      MDX  J88A  88454720
OED4 0 7003      MDX  J88B  88454730
OED5 0J 4400F83  J88A BSI L F000  CARRY IS ON  88454740
OED7 0 314A      DC  /314A  ERR ID  88454750
OED8 0J 4400FOE  J88B BSI L F005  CK LOCK DN ERROR  88454760
OEDA 0 70F4      MDX  8885  LOOP  88454770
OEDB 0 701D      MDX  88A0  EXIT TO NEXT ROUTINE  88454780
OEDC 0 0000      N880  DC  /000D  88454790
OEE 0 0000      BSS  E     88454800
OEE6 0 0000      N882  DC  /0000  88454810
OEEF 0 FFFF      DC  /FFFF  88454820
OEE0 0 0001      N884  DC  /0001  88454830
OEE1 0 0010      N885  DC  /0010  88454840
OEE2 0 8000      N886  DC  /8000  88454850
OEE3 0 FF00      N887  DC  /FF00  88454860
OEE4 0 0000      N888  DC  /0000  88454870
OEE5 0 D000      DC  /D00D  88454880
OEE6 0 3000      N88A  DC  /000D  88454890
OEE7 0 0002      N88B  DC  /0002  88454900
OEE8 0 0020      N88C  DC  /0020  88454910
OEE9 0 FFDF      N88D  DC  /FFDF  88454920
OEEA 0 FF01      N88E  DC  /FF01  88454930
OEEB 0 7FFF      N88F  DC  /7FFF  88454940
*****
*****
TEST COMPARE INSTRUCTION
*****
A = ACCUMULATOR
Q = ACCUMULATOR EXTENTION
M = WORD BEING COMPARED
M+1 = 2ND WORD ON OCM
*****
THE 1800 HAS A COMPARE INSTRUCTION
BUT THE 1130 DOES NOT. THIS ROUTINE
DETERMINES WHICH MACHINE IS BEING
TESTED BEFORE ATTEMPTING A COMPARE
*****
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
***** INSTRUCTION. 88455110
***** INDEX REGISTERS ARE HARDWARE IN 1800 88455120
***** AND CORE STORAGE LOCATIONS IN 1130. 88455130
***** 88455140
***** 88455150
***** 88455160
CORE DATA OR *LA- OPER- 88455170
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ- AT RIGHT 88455180
***** 88455190
OEEC 0 1810      B8A0 SRA 16 CK FOR 1130 DR 1800 88455200
OEEED 0J D4000D1 STO L /0001 STUPE /0000 AT ADDR /0001 88455210
OEEF 0 61FF      LDX 1 -1 LD XR 1 WITH /FFFF 88455220
OEEF0 0 C4000001 LD L /0001 LD C(/0001) 88455230
OEEF2 0 4C200F76 BSC L W8CO,2 BRANCH IF 1130 88455240
OEEF4 0 C075      LD N8A2 LO C(N8A2) /4000 88455250
OEEF5 0 8072      CMP N8A0 A GREATER THAN M 88455260
OEEF6 0 7004      MDX J8A0 A GREATER THAN M 88455270
OEEF7 0 1000      SIA 0 A LESS THAN M 88455280
OEEF8 0J 4400F83 BSI L F000 A GREATER THAN M FAILED 88455290
OEEFA 0 3148      DC /3148 ERR ID 88455300
OEEFB 0J 4400F82 J8A0 BSI L F00E CK LOCK ON ERRDR 88455310
OEEFD 0 70EE      MDX 88A0 LOOP 88455320
OEEFE 0 F068      EOR N8A2 ZERC WITH /4000 88455330
OEEFF 0J 40180F07 BSC L B8A1,+ BRANCH ON ZERO 88455340
OEF0 0 4400F83 BSI L F00D ACC CHANGED ERRDR 88455350
OEF03 0 314C      DC /314C EPR ID 88455360
OEF04 0J 4400FDE BSI L F005 CK LOCK ON ERROR 88455370
OEF06 0 70E5      MDX 88A0 LOOP 88455380
***** 88455390
OEF07 0 C060      B8A1 LD N8A0 N8A0 =/0000 88455400
OEF08 0 8060      CMP N8A1 N8A1 =/1000 88455410
OEF09 0 7001      MDX J8A2 A LESS THAN M FAILED 88455420
OEF0A 0 7003      MDX J8A1 A LESS THAN M 88455430
OEF0B 0J 4400F83 J8A2 BSI L F000 A LESS THAN M FAILED 88455440
OEF0D 0 314D      DC /314D ERR ID 88455450
OEF0E 0J 4400FDE J8A1 BSI L F005 CK LOCK ON ERROR 88455460
OEF10 0 70F6      MDX 88A1 LOOP 88455470
***** 88455480
OEF11 0 C056      B8A2 LD N8A0 N8A0 =/0000 88455490
OEF12 0 8058      CMP N8A3 N8A3 =/2000 88455500
OEF13 0 7001      MDX J8A4 A LESS THAN M FAILED 88455510
OEF14 0 7003      MDX J8A3 A LESS THAN M 88455520
OEF15 0J 4400F83 J8A4 BSI L F000 A LESS THAN M FAILED 88455530
OEF17 0 314E      DC /314E ERR ID 88455540
OEF18 0J 4400FDE J8A3 BSI L F005 CK LOCK ON ERROR 88455550
OEF1A 0 70F6      MDX 88A2 LDOP 88455560
***** 88455570
OEF1B 0 C04C      B8A3 LD N8A0 N8A0 =/0000 88455580
OEF1C 0 8040      CMP N8A2 N8A2 =/4000 88455590
OEF1D 0 7001      MDX J8A6 A LESS THAN M FAILED 88455600
OEF1E 0 7003      MDX J8A5 A LESS THAN M 88455610
OEF1F 0J 4400F83 J8A6 BSI L F000 A LESS THAN M FAILED 88455620
OEF21 0 314F      DC /314F ERR ID 88455630
OEF22 0J 4400FDE J8A5 BSI L F005 CK LOCK ON ERROR 88455640
OEF24 0 70F6      MDX 88A3 LDOP 88455650
***** 88455660
OEF25 0 C046      B8A4 LD N8A4 LD /8000 88455670
OEF26 0 8041      CMP N8A0 COMPARE C(N8A0) /0000 88455680
OEF27 0 7001      MDX J8A8 A LESS THAN M FAILED 88455690
OEF28 0 7003      MDX J8A7 A LESS THAN M 88455700
OEF29 0J 4400F83 J8A8 BSI L F000 A LESS THAN M FAILED 88455710
OEF2B 0 3150      DC /3150 ERR ID 88455720
OEF2C 0J 4400FDE J8A7 BSI L F005 CK LOCK ON ERROR 88455730
OEF2E 0 70F6      MDX 88A4 LOOP 88455740
***** 88455750
OEF2F 0 C039      B8A5 LD N8A1 LD /1000 88455760
OEF30 0 8038      CMP N8A1 CMP /1000 88455770
OEF31 0 7002      MDX J8AA A EQUAL M FAILED 88455780
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
OF32 0 7001      MOX      J8AA      A EQUAL M FAILED      88455790
OF33 0 7003      MDX      J8A9      A=M                    88455800
OF34 00 4400F83  J8AA 8S1 L F000    A=M FAILED            88455810
OF36 0 3151      OC       /3151    ERR 10                 88455820
OF37 00 4400FDE  J8A9 8S1 L F005    CK LOCK ON ERROR      88455830
OF39 0 70F5      MOX      88A5      LOOP                  88455840
*****
*
* TEST DOUBLE COMPARE
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OF3A 0 C835      B8C0 L00 N8C6      LO A=/8000 Q=/0001    88455920
OF3B 0 B832      DCM N8C5          AQ GREATER THAN M, M+1 88455930
OF3C 0 7003      MOX J8C0          88455940
OF3D 0 1000      SLA 0              NO-OP                 88455950
OF3E 0 4044      BS1 F000          FAILED A,Q NOT GREATER 88455960
OF3F 0 3152      DC /3152          ERR 10                 88455970
OF40 00 4400F82  J8C0 BS1 L F00E    CK LOCK ON ERROR      88455980
OF42 0 70F7      MOX 88C0          LOOP                  88455990
OF43 0 F02C      EOR N8C6          ZERO WITH /8000        88456000
OF44 00 4C180F48 BSC L J8C1,+-- BRANCH ON ZERO           88456010
OF46 0 403C      BS1 F000          ACC CHANGE0           88456020
OF47 0 3153      DC /3153          ERR 10                 88456030
OF48 00 4400FB2  J8C1 BS1 L F00E    CK LOCK ON ERROR      88456040
OF4A 0 70EF      MOX 88C0          LOOP                  88456050
OF4B 0 18D0      RTE 16            NOW A=/0001 Q=/0000    88456060
OF4C 0 F024      EOR N8C6+1        ZERO WITH /0001        88456070
OF4D 00 4C180F51 BSC L J8C2,+-- BRANCH ON ZERO           88456080
OF4F 0 4033      BS1 F000          Q REG CHANGE0         88456090
OF50 0 3154      DC /3154          ERR 10                 88456100
OF51 00 4400FDE  J8C2 BS1 L F005    CK LOCK ON ERROR      88456110
OF53 0 70E6      MOX 88C0          LOOP                  88456120
*****
OF54 0 C81D      B8C1 L00 N8C7      LO A=/0000 Q=/8000    88456130
OF55 0 881E      DCM N8C8          A,Q LESS THAN M, M+1    88456140
OF56 0 7001      MDX J8C3          A,Q GREATER THAN M,M+1 88456150
OF57 0 7002      MDX J8C4          A,Q LESS THAN M,M+1    88456160
OF58 0 402A      J8C3 8S1 F000      FAILED A,Q GREATER    88456170
OF59 0 3155      OC /3155          ERR 10                 88456180
OF5A 00 4400FDE  J8C4 8S1 L F005    CK LOCK ON ERROR      88456190
OF5C 0 70F7      MDX 88C1          LOOP                  88456200
*****
OF50 0 C814      B8C2 L00 N8C7      LO A=/0000 Q=/8000    88456210
OF5E 0 8813      OCM N8C7          A,Q EQUQL M,M+1        88456220
OF5F 0 7002      MDX J8C5          A,Q GREATER            88456230
OF60 0 7004      MDX J8C5          A,Q LESS              88456240
OF61 0 7012      MOX J8C6          A,Q = M,M+1            88456250
OF62 0 4020      J8C5 BS1 F000      A,Q = M,M+1 FAILED    88456260
OF63 0 3156      OC /3156          ERR 10                 88456270
OF64 00 4400FDE  J8C6 BS1 L F005    CK LOCK ON ERROR      88456280
OF66 0 70F6      MOX 88C2          LOOP                  88456290
OF67 0 700E      MOX 88C0          EXIT TO NEXT ROUTINE    88456300
OF68 0 0000      BSS E 0          88456310
OF69 0 0000      N8A0 OC /0000      88456320
OF6A 0 1000      N8A1 OC /1000      88456330
OF6B 0 4000      N8A2 DC /4000      88456340
OF6C 0 2000      N8A3 DC /2000      88456350
OF6D 0 8000      N8A4 OC /8000      88456360
OF6E 0 0000      BSS E 0          88456370
OF6F 0 8000      N8C5 DC /8000      88456380
OF70 0 0000      DC /0000          88456390
OF71 0 0001      DC /0001          88456400
OF72 0 0000      N8C6 DC /0000      88456410
OF73 0 8000      DC /8000          88456420
```

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```
OF74 0 0000      N8C8 OC /0000      88456470
OF75 0 8001      DC /8001          88456480
*****
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OF76 0 0809      W8C0 X10 N8C1      READ SWITCHES      88456490
OF77 0 C00A      LD N8C3          LD SW BITS          88456500
OF78 0 1804      SRA 4            PLACE SW 11 AT BIT 15 POS. 88456510
OF79 0 4804      BSC E            IS SWITCH 11 ON      88456520
OF7A 0 7002      MDX W8C4          SWITCH 11 ON        88456530
OF7B 0 C003      LD Z020          SWITCH 11 IS OFF-WAIT 88456540
OF7C 0 3003      DC /3003          PROGRAM FINISHED    88456550
OF7D 00 4C000154 W8C4 BSC L A140    88456560
OF7E 0 0003      Z020 OC /0003      88456570
OF80 0 0000      BSS E            88456580
OF81 0 0F82      N8C1 DC N8C3      EQUAL /3A00 IN 1130 88456590
OF82 0 0240      N8C2 DC /0240      88456600
OF83 0 0000      N8C3 OC /0000      88456610
*****
*****
* ERROR CONTROL ROUTINE
*****
OF83 0 0000      F000 UC 0          REENTER ADDRESS      88456620
OF84 0 2816      STS F00X          SAVE STATUS          88456630
OF85 0 0063      STO U000          SAVE A REG           88456640
OF86 0 18D0      RTE 16            88456650
OF87 0 0062      STU U001          SAVE Q REG           88456660
OF88 0 0863      X10 F003          READ SWITCHES        88456670
OF89 0 C066      LD Z000          LD SW READINGS        88456680
OF8A 0 1807      SRA 7            PLACE SW B AT BIT POS 15 88456690
OF8B 0 4804      BSC E            CK LOOP ON INSTRUCTION 88456700
OF8C 0 7012      MDX F00A          * BEING TESTED SW    88456710
OF8D 00 4C800F83 LD I F000          GET WAIT ERROR 10    88456720
OF8E 0 D00C      STU F002          STORE ERROR 10 AT F002 88456730
OF8F 0 C0F2      LD F000          GET RETURN ADDR        88456740
OF90 0 D01F      STU U00B          STURE AT U00B         88456750
OF91 0 805C      A U006          ADD ONE                88456760
OF92 0 D0EF      STG F000          STORE NEW RETURN ADDRESS 88456770
OF93 0 C05B      LD Z000          CK BYPASS EPKOR SW      88456780
OF94 0 1801      SRA 1            PLACE SW 14 AT BIT POS 15 88456790
OF95 0 4804      BSC E            SKIP IF SW 14 OFF      88456800
OF96 0 700D      MDX F00F          CK FOR 8 OR 12 ON ALSO 88456810
OF97 0 C051      LD U001          RESTORE REG AND WAIT    88456820
OF98 0 18D0      RTE 16            PLACE IN Q REG        88456830
OF99 0 C04E      LD U000          RESTORE A REG          88456840
OF9A 0 2000      F00X LDS 0          RESTORE C AND OF IND. 88456850
OF9B 0 3000      F002 WAIT 0        ERROR WAIT B REG      88456860
OF9C 0 4C800F83 F00B BSC 1 F000    * SHOWS ERROR 10 88456870
*****
*****
* C(F000) IS NOW ONE
* GREATER THAN AT THE
* BEGINNING OF ROUTINE
*****
*****
* LOOP ON INSTRUCTION BEING
* TESTED
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OF9F 0 C0E3      F00A LD F000          GET RETURN ADDR AT F000 88457100
```

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PROCESSOR-CONTROLLER FUNCTION TEST

```
OFA0 0 D010      STD  U008      STORE RETURN ADDRESS      88457150
OFA1 0 800D      A    U003      A00 3                      88457160
OFA2 0 D0E0      STO  F000      UPDATE RETURN ADDRESS     88457170
OFA3 00 4C800F83 BSC  I  F000      BR TO UPDATAD ADDRESS  88457180
                                CK FOR SW 8 OP 12          88457190
OFA5 0 1802      F00F SRA  2      PLACE SW 12 AT BIT POS 15 88457200
OFA6 0 4804      BSC  F          SKIP IF SW 12 OFF          88457210
OFA7 0 70F5      MOX  F00B      BR TO EXIT IF SW 12 ON     88457220
OFA8 0 1804      SPA  4          PLACE SW 8 AT BIT POS 15  88457230
OFA9 0 4804      BSC  E          SKIP IF SW 8 OFF           88457240
OFAA 0 70F2      MOX  F00B      BR TO EXIT IF SW 8 ON     88457250
OFAB 0 C044      LO    Z000      LD SWITCH READINGS       88457260
OFAC 0 0000      OC          IMPROPER BIT SWS, 14 ON       88457270
OFA0 0 083E      XIO  F003      *WITHOUT 8 OR 12 CN       88457280
OFAE 0 70E5      MDX  F00L      88457290
OFAF 0 0003      U003 OC  3      CONSTANT 3                88457300
OFB0 0 FFFD      U00A DC  -3      CONSTANT -3              88457310
OFB1 0 0000      U00B DC  0      ERROR OCCURED CONTROL    88457320
```

LOCK ON ERRDR RT

```
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OFB2 0 0000      F00E DC  0      CONTAINS RETURN ADDRESS  88457420
OFB3 0 281A      STS  F00H      SAVE REGS C AND OF        88457430
OFB4 0 D040      RTE  U00X      ACCUMULATOR              88457440
OFB5 0 18D0      STO  U00X+1    ACC EXTENTION             88457450
OFB6 0 D03F      *          88457460
```

* SET UP FOR RESTART *

* TO RESTART -- PRESS STOP, RESET AND START. *

```
OFB7 0 C03F      *      LD      RST1      LO /6004          *
```

```
OFB8 00 D4000000 *      STO L /0000      STO IN WORD ZERO          *
```

```
OFBA 0 C03D      *      LO      RST2      LO /4C00          *
```

```
OFBB 00 D4000004 *      STO L /0004      STO IN WORD FOUR          *
```

```
OFBD 0 C03B      *      LO      RST2+1    LO /012C          *
```

```
OFBE 00 D4000005 *      STO L /0005      STO IN WORD FIVE          *
```

```
OFEO 0 0828      XIO  F003      READ SWITCHES            88457660
OFC1 0 C02E      LO    Z000      CK LOOP ON INST BEING    88457670
OFC2 0 1807      SRA  7          * TESTED SW             88457680
OFC3 0 4804      BSC  E          SKIP IF EVEN             88457690
OFC4 0 700A      MOX  F00B      EXIT TO LOOP INST         88457700
OFC5 0 C0EB      LD    U00B      CK IF ERROR HAS          88457710
OFC6 0 4820      BSC  Z          * OCCURRED              88457720
OFC7 0 7009      MOX  F009      88457730
OFC8 0 C0E9      F00K LO  F00E      GOT RETURN ADDR       88457740
OFC9 0 8025      A    U006      A00 ONE                  88457750
OFCA 0 00E7      STO  F00E      STORE RETURN ADDRESS     88457760
OFCB 0 C02A      LO    U00X+1    RESTORE REGS            88457770
OFCC 0 18D0      RTE  16          88457780
OFCD 0 C027      LO    U00X      88457790
OFCE 0 2000      F00H LOS  0      SET C AND OF OFF        88457800
OFCF 00 4C800F82 F00B BSC  I  F00E      BR TO RETURN ADDRESS 88457810
OFD1 0 C01E      F009 LO  Z000      CHECK LOCK ON ERROR SW 88457820
```

PROCESSOR-CONTROLLER FUNCTION TEST

```
OFD2 0 1803      SRA  3          SHIFT BIT 12 TO POS 15 88457830
OFD3 0 4804      BSC  E          SKIP IF OFF            88457840
OFD4 0 7003      MOX  F00C      ERRLR SW (8 12) ON       88457850
OFD5 0 1810      SRA  16         RESET ERROR OCCURRED 88457860
OFD6 0 000A      STO  U00B      * CONTROL              88457870
OFD7 0 70F0      MOX  F00K      BR TO GET RETURN ADDRESS 88457880
OFD8 0 C0D9      F00C LD  F00E      GOT ADDR             88457890
OFD9 0 80D6      A    U00A      ADD MINUS THREE          88457900
OFDA 0 F006      EOR  U008      COMPARE TO ERR CNTR      88457910
                                * ACOR                  88457920
OFDB 0 4820      BSC  Z          SKIP ON ZERO            88457930
OFDC 0 70E8      MDX  F00K      BR TO GET RETURN ADDRESS 88457940
OFDD 0 70F1      MDX  F00B      EXIT                      88457950
```

PROCESSOR-CONTROLLER FUNCTION TEST

CROSS REFERENCE LISTING

SYMBOL	VALUE	REFERENCES
A0C0	013F	300F, 3010, 3011, 3012, 013A
A080	012E	3004, 3005, 3006, 3007, 3008, 3009, 300A, 300B, 300C, 3000, 300E, 0FF8
A1C0	01E8	303D, 303E, 01E9
A1D0	01F5	303F, 3040, 3041, 3042, 3043, 3044, 3045, 01F2
A1E0	0214	3046, 3047, 0210
A1F0	0220	3048, 3049, 0210
A100	014C	3013, 3014, 3015
A140	0154	3016, 3017, 3018, 3019, 301A, 3018, 301C, 3010, 301E, 301F, 3020, 3021, 3022, 3023, 3024, 3025, 3026, 3027, 3028, 3029, 302E, 0F7D
A180	01A0	302A, 302B, 302C, 3020, 302F, 3030, 3031, 3032, 3033, 3034, 3035, 3036, 3037, 3038, 3039, 303A, 3038, 303C, 019E
A2C0	0337	3072, 0341
A2C0	0319	306F, 0312, 0322
A2C4	0323	3070, 032C
A2C8	0320	3071, 0336
A200	0220	304A, 3048, 304C, 3040, 304E, 304F, 3050, 3051, 3052, 3053, 3054, 3055, 3056, 3057, 3058, 3059, 305A, 3058, 305C, 3050, 305E, 305F, 0229
A240	0270	3060, 3061, 3062, 3063, 3064, 026B
A280	0209	306A, 0288, 02C8, 02E2
A281	02E3	3068, 02E0
A282	02FE	306C, 02F8
A283	02F9	3060, 0303
A284	0304	306E, 0311
A3C0	0300	3080, 3081, 0306, 03E9, 03F3
A3C4	03F4	3082, 3083, 0407, 0410
A300	0345	3073, 0342, 034E
A302	034F	3074, 0359
A304	035A	3075, 0364
A340	0368	3076, 3077, 0365, 0373, 0370
A38C	0380	307E, 307F, 03C8, 0305
A380	0381	3078, 3079, 037E, 0388, 0395
A384	0396	307A, 3078, 039F, 03A8
A388	03A9	307C, 3070, 0382, 038C
A4CC	05AC	30A8, 30A9, 05B8, 05C1
A4C0	0566	30A1, 0562, 0570
A4C2	0571	30A2, 30A3, 30A4, 30A5, 0571, 0574, 0582, 058B, 0595
A4C8	0596	30A6, 30A7, 05A2, 05A8
A400	0419	3084, 3085, 3086, 0411, 0426, 042E, 0439
A408	043A	3087, 3088, 3089, 0448, 0450, 0459
A44A	0500	3099, 309A, 3098, 050C, 0515, 051F
A440	04C4	3093, 3094, 3095, 048C, 04CF, 0407, 04E0
A444	04E1	3096, 3097, 3098, 04E0, 04F6, 04FF
A480	0549	309F, 0542, 0554
A482	0555	30A0, 0561
A5C0	074C	30CE, 30CF, 0747, 0756, 075F
A5C4	0760	3000, 3001, 076E, 0778
A5C8	0779	3002, 3003, 0788, 0792
A50A	0600	30AF, 3080, 3170, 0608, 0612
A50C	0610	3081, 3082, 061C, 0629, 0630
A50E	0631	3083, 3084, 062A, 0630, 0644
A500	05C8	30AA, 05C2, 0502
A502	0503	30AB, 0500
A504	050E	30AC, 30A0, 05E9, 05F4
A508	05F5	30AE, 05FF
A54A	06C1	30C0, 30C1, 0689, 06CA, 06D2
A54C	0603	30C2, 30C3, 06C8, 06DC, 06E4
A54E	06E5	30C4, 30C5, 06D0, 06EE, 06F6
A54F	06F7	30C6, 30C7, 06EF, 0701, 0709
A540	0663	3037, 3088, 3089, 0654, 0658, 066F, 0676, 0686
A544	0687	308A, 3088, 0670, 0692, 0699
A546	069C	306C, 3080, 0693, 069A, 06A7, 06AE
A548	06AF	308E, 308F, 06A8, 0688, 06C0

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A580	070F	30C8, 30C9, 0702, 070A, 0717, 0720
A584	0721	30CA, 30C8, 072A, 0734
A588	0735	30CC, 30CD, 0730, 0746
A6C0	0964	30F0, 30F1, 0954, 0958, 0974, 0978
A6C2	097C	30F2, 30F3, 0975, 098C, 0993
A6C4	0994	30F4, 30F5, 0980, 09A3, 09AA
A6C6	09A8	30F6, 30F7, 09A4, 0988, 09C2
A6C8	09C3	30F8, 30F9, 098C, 0903, 090A, 09E8
A6D0	09EC	3150, 09D4, 09D8, 09F7
A602	09F8	315E, 0A03
A603	0A04	315F, 0A0F
A605	0A10	3163, 0A18
A606	0A1C	3164, 0A27
A6F0	0A28	3165, 0A36
A6F1	0A39	3166, 0A37, 0A47
A60A	0707	30D9, 07E3
A60C	07E4	300A, 07F0
A60E	07F1	30D8, 07F0
A600	079F	30D4, 0796, 07A7
A602	07A8	3005, 0780
A604	0781	3006, 078C
A606	078D	30D7, 07C9
A608	07CA	3008, 0706
A64A	086C	30E3, 0879
A64C	087A	30E4, 0887
A640	0820	30DE, 3167, 081A, 082C, 0836
A642	0837	30DF, 0843
A644	0844	30E0, 0850
A646	0851	30E1, 0850
A648	085E	30E2, 0868
A660	088F	3157, 3158, 088A, 089C, 08A6
A662	08A7	3159, 315A, 0884, 088E
A664	088F	3158, 315C, 08CC, 08D6
A670	0809	3169, 08D7, 08E2, 08E9
A68C	0928	30E8, 30EC, 0932, 0938
A680	08EC	30E5, 30E6, 08E3, 08F7, 0900
A684	0901	30C7, 30E8, 0908, 0913
A688	0914	30E9, 30EA, 091E, 0927
A7C0	0C30	3125, 3126, 0C46, 0C4F
A7C0	0C01	311F, 3120, 08F3, 0C08, 0C15
A7C4	0C16	3121, 3122, 0C1F, 0C29
A7C8	0C2A	3123, 3124, 0C33, 0C3C
A70C	0A80	3100, 3101, 3102, 0A99, 0AA7, 0AAE
A700	0A48	30FA, 30FB, 0A54, 0A5E
A704	0A5F	30FC, 30FD, 0A68, 0A75
A708	0A76	30FE, 30FF, 0A82, 0A8C
A74C	0814	3108, 310C, 3100, 310E, 0820, 082A, 0836, 0830
A740	0A88	3103, 3104, 3105, 3106, 0AA8, 0AAF, 0AC8, 0AD3, 0ADF, 0AE6
A746	0AE7	3107, 3108, 3109, 310A, 0AF5, 0AFE, 080C, 0813
A78A	08C8	3118, 311C, 08D4, 080E
A78E	080F	3110, 311E, 08E8, 08F2
A780	088C	3115, 3116, 3117, 3118, 0870, 0898, 08A2, 08AE, 0885
A786	0886	3119, 311A, 08C0, 08CA
A80C	0C82	312F, 0CA8, 0C8C
A80E	0C80	3130, 0CC7
A800	0C58	3127, 3128, 3129, 312A, 0C50, 0C68, 0C73, 0C7F, 0C86
A806	0C87	3128, 312C, 3120, 312E, 0C80, 0C94, 0C9F, 0CAA, 0C81
A84A	00C6	3171, 00CF
A840	006A	3135, 0041, 0077
A842	0078	3136, 316F, 0D8C
A844	0080	3137, 0098
A846	009C	3138, 00A5
A848	00A6	3139, 00AF
A849	0080	313A, 3168, 0DC5
A85A	0000	3172, 0008
A88A	0E60	3173, 0E60
A88C	0E6E	3141, 3142, 3143, 0E79, 0E82, 0E8C
A880	0DE4	3138, 313C, 3160, 0009, 0DF3, 0DF0, 0E03, 0E0A



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A884	UF0B	313D, 313F, 3161, UF04, OF1B, OE29, OE32
A888	UE 43	313F, 3140, UE41, OE4F
A889	UF 50	3162, OE 5F
A900	UF82	3065, 3066, 3067, 3068, 3069
B40A	U497	3090, 3091, 3092, 04AA, 04B2, 04B8
B400	D45A	308A, 308B, 308C, 0465, 046E, 0477
B406	U478	308D, 308E, 308F, 04B4, 048D, 0496
F440	U520	309C, 309D, 309E, 0530, 0538, 0541
B500	U645	30B5, 30B6, U63E, U653, 065A
B600	U7FE	30DC, 080B
B602	C80C	30DD, 0819
B680	U93C	30FD, 30EE, 30FF, 0947, 0953, 095A
B742	OB3E	310F, 3110, 3111, 3112, 0B4A, 0B54, 0B60, 0B67
B747	OB68	3113, 3114, 0B72, 0B7C
B8A0	UEEC	314R, 314C, UEDR, OEFD, OF06
B8A1	UF07	314D, UEFF, UF10
B8A2	UF11	314E, OF 1A
F8A3	OF1B	314F, OF 24
B8A4	OE25	3150, OF 2E
B8A5	UF2F	3151, UF 39
B8C0	UF3A	3152, 3153, 3154, UF42, OF4A, OF53
B8C1	OF54	3155, UF 5C
B8C2	UF5D	3156, OF 66
B800	UCCB	3131, UCD2
B802	OC03	3132, OCDD
B804	OCDE	3133, OCE8
B806	OCF9	3134, OCF3
B807	UD18	316A, OCF4, OD22
B808	OD23	316B, OD2D
B809	OD2E	316C, OD39
B810	UD3A	316D, 316F, UD69
B887	UE8D	3144, 3145, 3146, OE99, UEA2, OEAE
B884	UEAF	3147, 3148, 3149, OE89, OEC2, OECE
B885	UECF	314A, UEDA
F00A	OF9F	OF8C
F00B	OF9D	UFA7, OFAA
F00C	OFD8	OFD4
F00E	OFB2	0371, 0389, 0380, 03C9, 03E7, 0405, 0424, 042C, 0446, 044E, 0463, 046C, 0482, 048B, 04A8, 04B0, 04CD, 04D5, 04EB, 04F4, 050A, 0513, 052E, 0536, 0580, 0589, 05A0, 05B6, 05E7, 0609, 0610, 066D, 0674, 0715, 0728, 073B, 0754, 076C, 0786, 082A, 089A, 08B2, 08CA, 08E7, 08F5, 0709, 091C, 0930, 0945, 0A52, 0A59, 0A80, 0A97, 0AC6, 0AD1, 0AF3, 0AFC, 0B1E, 0B2B, 0B48, 0B52, 0B70, 0B96, 0BA0, 0BBE, 0BD2, 0BE6, 0C09, 0C1D, 0C31, 0C44, 0C66, 0C71, 0C92, 0C9D, 0D51, 0D5A, 0DE1, 0DFB, 0E19, 0E27, 0E3F, 0E77, 0E80, 0E97, 0EAO, 0EB7, 0ECD, 0EF8, 0F40, 0F4B, 0F68, 0FCA, 0FCF, 0F08, 0FE5, 0FE6
F00F	CFA5	OF97
F00G	OFE7	OFE3
F00H	OFCE	OFB3
F00K	OF08	OFD7, OFDC
F00L	OF94	OFAE
F00X	OF9B	OFB4
E000	UE83	3174, 02D0, 02E8, 02F3, 02FE, 030C, 031D, 0327, 0331, 033C, 0349, 0354, 035F, 036E, 0378, 0386, 0390, 039A, 03A3, 03AD, 03B7, 03C6, 03D0, 03E4, 03EE, 0402, 040B, 0421, 0429, 0434, 0443, 044B, 0454, 0460, 0469, 0472, 047F, 048B, 0491, 04A5, 04AD, 04B6, 04CA, 04D2, 04DB, 04E8, 04F1, 04FA, 0507, 0510, 051A, 052B, 0533, 053C, 054F, 055C, 056B, 0577, 057D, 0586, 0590, 0597, 05A6, 05B3, 05BC, 05CD, 05D8, 05E4, 05EF, 05FA, 0606, 060D, 0617, 0624, 062B, 063B, 063F, 064F, 0655, 066A, 0671, 0681, 068D, 0694, 06A2, 06A9, 06B3, 06BB, 06C5, 06CD, 06D7, 06DF, 06E9, 06F1, 06FB, 0704, 0717, 0718, 0725, 072F, 0738, 0741, 0751, 075A, 0769, 0773, 0783, 07BD, 07A2, 07AB, 07B7, 07C4, 07D1, 07DE, 07EB, 07F8, 0806, 0814, 0827, 0831, 083E, 084B, 0858, 0866, 0874, 0882, 0897, 08A1, 08AF, 08B9, 08C7, 08D1, 08E4, 08E2, 08FB, 0906, 090E, 0919, 0922, 092D, 0936, 0942, 094E, 0955, 096F, 0976, 0987, 09BE, 099E, 09A5,

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F002	UF9C	0986, 09B0, 09CE, 09D5, 09F2, 09FE, 0A0A, 0A16, 0A22, 0A31, 0A42, 0A4F, 0A59, 0A66, 0A70, 0A7D, 0A87, 0A94, 0AA2, 0AA9, 0AC3, 0ACE, 0ADA, 0AE1, 0AF0, 0AF9, 0B07, 0B0E, 0B18, 0B25, 0B31, 0B38, 0B45, 0B4F, 0B5B, 0B62, 0B6D, 0B77, 0B93, 0B9D, 0BA9, 0BB0, 0BBB, 0BC5, 0BCF, 0BD9, 0BE3, 0BED, 0C06, 0C10, 0C1A, 0C24, 0C2E, 0C37, 0C41, 0C4A, 0C63, 0C6E, 0C7A, 0C81, 0C8F, 0C9A, 0CA5, 0CAC, 0CB7, 0CC2, 0CCD, 0CD8, 0CE3, 0CEE, 0D1D, 0D2B, 0D34, 0D4E, 0D57, 0D72, 0D7E, 0D85, 0D96, 0DA0, 0DAA, 0DB9, 0DC0, 0DCA, 0DD3, 0DEE, 0DF8, 0E05, 0E16, 0E24, 0E2D, 0E3C, 0E4A, 0E5A, 0E74, 0E7D, 0E87, 0E94, 0E9D, 0EA9, 0EB4, 0EBD, 0EC9, 0ED5, 0EFB, 0F01, 0F0B, 0F15, 0F1F, 0F29, 0F34, 0F3E, 0F46, 0F4F, 0F58, 0F62, 0F8D, 0F90, 0F93, 0F9D, 0F9F, 0FA2, 0FA3
F003	OFFC	0F8F
F004	OFED	0D3B, 0F88, 0FAD, 0FC0, 0FDF
F005	OFDE	029A, 02AA
		02E0, 02E8, 02F6, 0301, 030F, 0320, 032A, 0334, 033F, 034C, 0357, 0362, 037B, 0393, 039D, 03A5, 03BA, 03D3, 03F1, 040E, 0437, 0457, 0475, 0494, 04B9, 04DE, 04FD, 051D, 053F, 0552, 055F, 056E, 0593, 05A9, 05BF, 05D0, 05FB, 05F2, 05FD, 061A, 0627, 062E, 063B, 0642, 0651, 065B, 0664, 0690, 0697, 06A5, 06AC, 06B6, 06B8, 06CB, 06D0, 06DA, 06E2, 06EC, 06F4, 06FF, 0707, 071E, 0732, 0744, 075D, 0776, 0790, 07A5, 07AE, 07BA, 07C7, 07D4, 07E1, 07FE, 07FB, 0809, 0817, 0834, 0841, 084E, 085B, 0869, 0877, 0885, 08A4, 08BC, 08D4, 08E0, 08FE, 0911, 0925, 0939, 0951, 095B, 0972, 0979, 098A, 0991, 09A1, 09A8, 09B9, 09C0, 09D1, 09DB, 09F5, 0A01, 0A0D, 0A19, 0A25, 0A34, 0A45, 0A5C, 0A73, 0A8A, 0AA5, 0AAC, 0ADD, 0AE4, 0B0A, 0B11, 0B34, 0B3B, 0B5E, 0B65, 0B7A, 0BAC, 0BB3, 0BC8, 0BDC, 0BF0, 0C13, 0C27, 0C3A, 0C4D, 0C7D, 0CP4, 0CA8, 0CAF, 0CBA, 0CC5, 0CDD, 0CDB, 0CE6, 0CF1, 0D20, 0C2B, 0D37, 0D67, 0D75, 0DBA, 0D99, 0DA3, 0DA0, 0DC3, 0DCD, 0DD6, 0E01, 0E08, 0E30, 0E4D, 0E5D, 0E6B, 0E8A, 0EAC, 0ECC, 0ED8, 0F04, 0F0E, 0F18, 0F22, 0F2C, 0F37, 0F51, 0F5A, 0F64, 0FE4, 0FE7
F007	OFF4	029C, 02AC
F008	UFCF	0FC4, 0FDD
F009	OFD1	0FC7
F902	02CA	02AE, 02C1
F903	02CB	0297, 02A7, 02B7
F904	02CC	02CC
F911	02CD	0282, 0286
F912	02CE	0283, 0285, 02CD
F913	02CF	0284
F915	02D0	0280, 02B3
F916	02D1	028C
F917	02D2	02AF, 02C2, 02CA
F918	02D3	0289, 028D
F919	02D4	0296
F920	02D5	02A6, 0288
F922	02D6	029E, 028B
F923	02D8	029F, 02A0, 02A3, 02BC
G0C1	0144	0141
G0C2	0147	0145
G080	0130	012E
G081	0133	0130
G082	0138	0133
G083	013A	013B
G084	013B	013B
G14A	0181	017F
G14B	0185	0183
G14C	0189	0187
G14D	018D	018B
G14E	0191	018F
G14F	0195	0193
G140	015A	0158
G141	015D	015B
G142	0161	015F
G143	0165	0163

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G144	0169	0167
G145	0160	0168
G146	0171	016F
G147	0175	0173
G148	0179	0177
G149	0170	017B
G150	0199	0197
G18A	0100	01CE
G18B	0104	0102
G18C	0108	0106
G180	010C	010A
G18E	01E0	010E
G18F	01E4	01E2
G181	01AC	01AA
G182	0180	01AE
G183	0184	0182
G184	0188	0186
G185	018C	018A
G186	01C0	018E
G187	01C4	01C2
G188	01C8	01C6
G189	01CC	01CA
G2CC	033F	033A
G2C0	0320	0318
G2C4	032A	0325
G2C8	0334	032F
G20A	0250	0259
G20B	0267	0265
G20C	0268	026F
G20D	0262	0260
G200	0231	0220
G201	0236	0232
G202	023A	0236
G203	023E	023A
G204	0242	0240
G205	0247	0243
G206	024F	0240
G207	0254	0252
G208	0248	0247
G209	0258	0256
G280	02E0	0208
G281	02E8	02E6
G282	02F6	02F1
G283	0301	02FC
G284	030F	030A
G3C0	03E7	03E2
G3C2	03F1	03EC
G3C4	0405	0400
G3C6	040E	0409
G300	034C	0347
G302	0357	0352
G304	0362	0350
G340	0371	036C
G342	0375	0376
G38A	038A	0385
G38C	03C9	03C4
G38E	03D3	03CE
G380	0389	0384
G382	0393	038E
G384	0390	0398
G386	03A6	03A1
G388	0380	03AB
G4CA	05A9	05A4
G4CC	0586	0581
G4CD	058F	058A
G4C0	056E	0569
G4C2	0580	057C
G4C4	0589	0585

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G4C6	0593	058E
G4C8	05A0	0598
G40C	0446	0442
G40E	0457	0452
G400	042C	0427
G404	0424	041F
G406	0437	0432
G407	0443	0440
G408	044E	0449
G44A	0513	050E
G44C	0507	0504
G44D	050A	0506
G44E	0510	0518
G440	04D5	0400
G442	04C0	04C8
G443	040E	0409
G444	04F4	04EF
G446	04E8	04E5
G447	04E8	04E7
G448	04F0	04F8
G480	0552	0540
G482	055F	055A
G5CA	0790	078B
G5C0	0754	074F
G5C2	0750	0758
G5C4	076C	0767
G5C6	0776	0771
G5C8	0786	0781
G50A	0613	0602, 060C
G50C	0623	061F
G50E	063E	0634
G500	0500	05CC
G502	0508	0507
G504	05E4	05E2
G505	05E7	05E3
G506	05EF	05ED
G507	05F2	05EE
G508	05FA	05F8
G54A	06CC	06C2
G54C	060E	0604
G54E	06F0	06E6
G54F	0703	06F8
G540	0678	0666, 0677
G542	0684	067F
G544	069B	0689
G546	06A1	069D
G548	068A	0680
G58A	0744	073F
G580	0715	0710
G582	071E	0719
G584	0728	0723
G586	0732	072D
G588	073B	0736
G6C0	0979	0960
G6C2	0991	0985
G6C4	09A8	099C
G6C6	09C0	0984
G6C8	0908	09CC
G60A	07E1	07DC
G60C	07EE	07E9
G60E	07F8	07F6
G600	07A2	079F
G602	07A8	07A8
G604	07BA	0785
G606	07C7	07C2
G608	07D4	07CF
G64A	0877	0872
G64C	0885	0880



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G640	0820	0825
G641	0834	082F
G642	0841	083C
G644	084E	0849
G646	0858	0856
G648	0869	0864
G660	089A	0895
G661	08A4	089F
G662	08B2	08AD
G663	08BC	0887
G664	08CA	08C5
G665	08D4	08CF
G670	08E4	08D8
G671	08D8	080F
G672	08D0	08EA
G6AA	0925	0920
G68C	0930	0928
G63E	0939	0934
G680	08F2	08EF
G682	08FE	08F9
G684	0909	0904
G686	0911	090C
G688	091C	0917
G7CA	0C3A	0C35
G7CC	0C44	0C3F
G7CE	0C40	0C48
G7C0	0C09	0C04
G7C2	0C13	0C0E
G7C4	0C10	0C18
G7C6	0C27	0C22
G7C8	0C31	0C2C
G70A	0A8A	0A85
G70C	0A97	0A92
G70E	0AAC	0A9C
G700	0A52	0A40
G702	0A5C	0A57
G704	0A69	0A64
G706	0A73	0A6E
G708	0A80	0A7B
G74A	0B11	0B03, 0B00
G74C	0B1E	0B19
G74E	0B28	0B23
G740	0AC6	0AC1
G742	0AD1	0ACC
G744	0AE4	0AD6, 0AE0
G746	0AF3	0AE E
G748	0AFC	0AF7
G74A	0B02	0B00
G74C	0B0C	0B07
G74E	0B0E	0B01
G780	0B96	0B91
G782	0BA0	0B98
G784	0BB3	0BA5, 0BAF
G786	0BB E	0BB9
G788	0BC4	0BC3
G80A	0CAF	0CA1
G80C	0CBA	0C85
G80E	0CC5	0CC0
G800	0C66	0C61
G802	0C71	0C6C
G804	0C84	0C76
G806	0C92	0C8D
G808	0C90	0C98
G84A	0DCA	0DC8
G840	0D75	0D70
G842	0D88	0D83
G844	0D99	0D94
G846	0DA0	0D9E

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G848	0DAA	0DAA8
G849	0DC3	0D8E
G88A	0E40	0E48
G888	0E68	0E68
G88C	0E77	0E72
G88E	0E80	0E7B
G880	0DF1	0DEC
G881	0DFE	0DEB
G882	0DF8	0DF6
G883	0E05	0E00
G884	0E19	0E14
G885	0E2A	0E11
G886	0E27	0E22
G887	0E30	0E2C
G888	0E3F	0E3A
G889	0E50	0E58
G900	02AE	02A1, 02B5
G901	02A6	0295
G902	029E	02A5
G903	02B8	02C0
G904	02C1	0289, 028D, 02C6
H4C2	0570	057A
H4C3	057A	0575
H4C4	0586	0583
H40A	0480	04A8
H40D	04A8	04A3
H40E	04B9	04B4
H400	046C	0467
H402	0463	045E
H404	0475	0470
H405	0482	047E
H406	0488	0486
H407	047F	047C
H408	0494	048F
H440	0536	0531
H443	052E	0529
H444	053F	053A
H50A	0606	0604
H508	0617	0615
H50C	0624	0621
H50E	0642	0636
H508	05FD	05F9
H54A	06D0	06C4
H54C	06E2	06D6
H54E	06F4	06E8
H54F	0707	06FA
H540	066A	0668
H544	0680	0688
H546	06A9	06A0
H548	068E	0682
H6C0	0976	0968
H6C2	098E	0980
H6C4	09A5	0998
H6C6	0980	09AF
H6C8	09D5	09C7
H600	09F5	09F0
H602	0A01	09FC
H603	0A00	0A08
H605	0A19	0A14
H606	0A25	0A20
H6F0	0A34	0A2F
H6F1	0A42	0A3C, 0A3D, 0A3F, 0A40
H6F2	0A45	0A41
H600	07A5	07A1
H602	07AE	07AA
H640	0822	0822, 0824
H680	08F5	08F1
H74A	080E	0805

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H744 0AE1 0AD8
H780 09F0 0BE8
H784 0880 0BA7
H80A 0CAC 0CA3
H804 0C81 0C78
H84A 0DCD 0DC9
H842 0D81 0D7C
H846 0DA3 0D9F
H848 0DAD 0DA9
H849 0DB2 0DB2,0DB6
H85A 0DD6 0DD2
J50A 060D 0605
J50C 062B 0622
J50E 063F 0637
J540 0671 0669
J544 0694 068C
J546 06AC 069F
J600 0809 0804
J602 0817 0812
J680 0945 0940
J682 0958 094A
J70E 0AA9 0AA0
J74A 087A 0875
J740 0838 082D,0837
J742 0848 0843
J744 0852 084D
J746 0865 0857,0861
J748 0870 086B
J8AA 0F34 0F31,0F32
J8A0 0EF8 0EF6
J8A1 0F0E 0F0A
J8A2 0F0B 0F09
J8A3 0F18 0F14
J8A4 0F15 0F13
J8A5 0F22 0F1E
J8A6 0F1F 0F1D
J8A7 0F2C 0F28
J8A8 0F29 0F27
J8A9 0F37 0F33
J8C0 0F40 0F3C
J8C1 0F48 0F44
J8C2 0F51 0F4D
J8C3 0F58 0F56
J8C4 0F5A 0F57
J8C5 0F62 0F5F,0F60
J8C6 0F64 0F61
J800 0CD0 0CCB
J802 0CDB 0CD6
J804 0CE6 0CE1
J806 0CF1 0CEC
J808 0D20 0D1B
J809 0D28 0D26
J810 0D37 0D33
J811 0D44 0D53,0D5C,0D63
J812 0D51 0D4C
J813 0D5A 0D55
J814 0D38 0D66
J815 0D34 0D31
J816 0D5D 0D60
J88A 0ED5 0ED3
J88B 0ED8 0ED4
J880 0E8A 0E85
J882 0E97 0E92
J884 0EAO 0E9B
J886 0EAC 0EA7
J887 0EB7 0EB3
J888 0ECO 0EBB
J889 0ECC 0EC7

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K50B 061A 0616
K50C 062E 0623
K640 0824 088C
K682 0955 094C
K740 0838 0827
K746 0862 0859
K849 0D8C 0D87
N1C0 01F3 01EB
N1C1 01F4 01EE
N1D0 0211 01FB,01FC,01FF,0207
N1D1 0212 01F5,01F8,0208
N1D2 0213 0204,020D
N1E0 021E 0218,021A,021E
N1E1 021F 0214
N1F0 022A 022C
N1F1 022B 0224,0226,0228
N1F2 022C 0220
N100 0143 014C,014F
N140 019F 0154
N180 01EA 01A0,01A4
N2C0 0343 0319,031A,0323,032E
N2C2 0344 0324,032D,0337,0338,0339
N200 026C 0231,025D
N201 026D 0258
N202 026E 0262
N203 026F 0267
N240 0271 0271,0275
N241 0273 0270,0274
N242 027C 027F
N243 027D 0278,027E
N280 0313 02D9
N281 0314 02E3,0304
N282 0315 02E5,0309
N283 0316 02EE
N284 0317 02F0,02F9
N285 0318 02FB
N3C0 0412 03DF
N3C1 0413 03DD
N3C2 0414 03F6
N3C3 0415 03F4
N3C4 0416 03E1
N3C5 0417 03EB
N3C6 0418 03FF
N300 0366 0345,0346,034F
N302 0367 0350,0351,035A,035B,035C
N340 037F 0368
N341 0380 036A,0375
N380 03D7 0381
N381 03D8 0383,038D
N382 03D9 0396
N383 03DA 03A9,038D
N384 03DB 0384
N385 03DC 03CD
N4C0 05C3 0567,0568,0573,058C,0597,0599,05AD,05AF
N4C1 05C4 058D
N4C2 05C5 0598,05A3,05AE,05B9
N4C3 05C6 059A
N4C4 05C7 0580
N400 048D 0419,041C,0430
N401 048E 0499
N402 048F 047A
N403 04C0 045C,0485
N404 04C1 0466
N405 04C2 043A,045A,047B,0497
N406 04C3 043D
N440 0543 04C4,0520
N441 0544 04C6,04E3,0502,0522
N442 0545 04E1,04EE



PL

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PART NO. 2196471
PAGE 49

PROCESSOR-CONTROLLER FUNCTION TEST

N443	0546	0500
N444	0547	0500
N445	0548	0517
N460	0563	0549,0557
N481	0564	0548,0555,0559
N482	0565	054A,054C,0556,055B
N5C1	0798	074C,0760,0770,077F
N5C3	079A	0763,0766,0770,0779,078A,0793
N5C5	079C	0740,074E,0761,0764,0765,077A
N5C6	079D	0757,0762,076F,077B,077E,0780,0794
N5C7	079E	077C,0789,0795
N500	065C	05C9,0601,0613,0633
N501	065D	0504
N502	065E	05DF
N503	065F	05F6,0646
N504	0660	061E
N505	0661	0649,064A
N506	0662	064B
N507	05EA	05E0
N540	0708	0664,06AF
N541	070C	0679,067B
N542	0700	0670,0687,06C1,06D3
N543	070E	069C
N581	0748	070F
N582	0749	0735
N583	074A	0721
N584	074B	0722,072C
N6CA	09E6	096C
N6CB	09E7	0984,0A13,0A10
N6CD	09E8	0983,0A11,0A1F
N6CF	09EA	09C4,09CB
N6CU	09DC	0967,090C,09EF
N6C1	09DD	09D0,09EC,09F8,0A04,0A07
N6C2	09DE	09DE,09FB
N6C3	09DF	090F
N6C4	09E0	0965,0970,0995,0997,09AC,09C6,09E0,09E9
N6C5	09E1	09AE,09E1
N6C6	09E2	09E2
N6C7	09E3	09E3
N6C8	09E4	097F,09E4
N6C9	09E5	096A,0968,0982,0983,099A,099B,0981,0982,09C9,09CA
N6F0	0A20	0A2A
N6F1	0A38	0A29,0A2E,0A38
N6F2	0A3D	0A3A
N6F3	0A41	0A3E
N600	081B	0708,07EB,07F5,0811,081B
N601	081C	07B2,07B4,07BF,07C1,07CC,07CE,0709,07E6,07F3,0801,080F,081C
N602	081D	0803,0810
N603	081E	07B4,07CB,070B,07F5,07F2,0800,080C
N604	081F	080E
N640	088B	0821,0823,0820,083B,083A,083B,0845,0847,0848,0852,0854,0855,085F,0861,0862,0860,086F,0870,087B,0870,087E,08E9
N642	089C	082E
N643	0880	085E,086C,087A,088B
N644	088E	0820,0837,0844,0851,0863,0871,087F
N660	08D8	0893,0894,0890,089E,08AB,08AC,08B5,0886,08C3,08C4,08CD,08CE
N670	08EB	080A
N660	095C	08ED,08FB,0902,0915,0916
N681	095D	08EE
N682	095E	0903
N683	095F	0929,092A
N684	0960	0933,0930,093E
N686	0961	0949
N687	0962	091F
N688	0963	093F,094B

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PROCESSOR-CONTROLLER FUNCTION TEST

N7C0	0C51	0C01
N7C1	0C52	0C02
N7C2	0C53	0C03
N7C3	0C54	0C00
N7C4	0C55	0C16,0C17,0C2B,0C30
N7C5	0C56	0C21
N7C6	0C57	0C2A,0C3E
N700	0A80	0A49,0A60,0A8E
N701	0A81	0A4A,0A63,0A78,0A84,0A9F
N702	0A82	0A4B,0A55,0A67,0A6C,0A79,0A83,0A9C,0A9A,0A9E
N703	0A83	0A4C,0A61
N704	0A84	0A56,0A60
N705	0A85	0A77,0A6F,0A91
N706	0A86	0A98
N707	0A87	0A7A
N74A	0B88	0B22
N74B	0B89	0B4C
N74C	0B8A	0B3F
N740	0B7E	0A80,0AD4,0AEC,0AFF,0B17,0B2B,0B41,0B55
N742	0B80	0A69,0ABF,0ACA,0A6A,0E15,0B16,0B18,0B40,0B42
N744	0B82	0A6B
N746	0B84	0AE8,0B68
N747	0B85	0B69,0B6A
N748	0B86	0B01,0B2C,0B56,0B74
N78A	0BFE	0B08
N780	0C00	0B06
N780	0BF4	0B8F,0B6A3
N782	0BF6	0B8D,0BB7,0B0F
N784	0BF8	0B8E
N785	0BF9	0B02,0BEA
N786	0BFA	0B90,0B9A,0BB8,0BCC
N787	0BF8	0BF0
N788	0BFC	0B04
N8A0	0F68	0EF5,0F07,0F11,0F18,0F26
N8A1	0F69	0F0B,0F2F,0F30
N8A2	0F6A	0EF4,0EFE,0F1C
N8A3	0F68	0F12
N8A4	0F6C	0F25
N8C1	0F80	0F76
N8C2	0F81	029B,02AB
N8C3	0F82	0F77,0F80
N8C5	0F6E	0F3B
N8C6	0F70	0F3A,0F43,0F4C
N8C7	0F72	0F54,0F50,0F5E
N8C8	0F74	0F55
N60A	0CFE	0CC9
N80C	0D00	0C04
N80E	0D02	0C64,0CDF
N80F	0D03	0D25
N800	0CF5	0C50,0C74,0C8A,0CA0
N802	0CF6	0C59
N804	0CF8	0CB7
N806	0CFA	0C83
N807	0CF8	0CBF,0CE0,0CEB,0D5E
N808	0CFC	0C8E,0CCA
N810	0D04	0CEA
N811	0D05	0C6A
N812	0D06	0C58,0C5F,0C05
N813	0D07	0C88,0C8B
N816	0D08	0C96
N817	0D09	0D1A
N818	0D0A	0D19
N819	0D0C	0D24
N820	0D0E	0D30
N821	0D0F	0D43,0D44,0D45,0D49,0D4A,0D50,0D5F
N822	0D13	0D42,0D62
N823	0D14	0D2F
N824	0D16	0D47

PROCESSOR-CONTROLLER FUNCTION TEST

NR4A	000A	00C6
N840	0DDC	0060,006E,0091,0092,0C85,0C8C
N841	0D00	0D6F,0D93,0D80
N842	0DDE	0D79,0D81,0089
N843	0DE0	0088
N844	0DE1	00E2
N845	0DE2	0078,0D7B,00B3
N846	0DE3	0082
N85A	0008	0000
N86A	0EE6	0E8F,0EAF,0E00
N88B	0EE7	0E56
N88C	0EE8	0E6E
N88D	0EE9	0E80
N88E	0EEA	0E20,0E45
N88F	0FEB	0E66
N880	0EDC	0DE0,00F4,0E1C,0E1E,0E42,0E44,0E83,0E84,0EA3,0EA4, 0EC3,0EC5
N862	0EDE	0DE5,0E63
N884	0EE0	0E00,0E53,0CC6
N885	0EE1	0E33,0E46
N886	0EE2	0E12,0E35,0E38,0E91,0E8A
N887	0EE3	0E08
N888	0EE4	0E70
RST1	0FF7	0F87
RST2	0FF8	0F8A,0F8D
S501	0655	064B
S503	0658	064C
U00A	0F80	0F09
U00B	0F81	0F91,0FA0,0FC5,0FD6,0FDA
U00X	0FF5	0F84,0F86,0FC8,0FCD
U000	0FE9	0F85,0F9A
U001	0FEA	0F87,0F98
U003	0FAF	0FA1
U004	0FEE	
U006	0FEF	0F92,0FC9
U008	0FF2	
U009	0FF3	
V1AC	027A	027C
V154	0241	023E
V168	024E	0248
V170	0253	0250
V174	0257	0254
V180	0261	025E
V184	0266	0263
W8C0	0F76	0EF2,0F67
W8C4	0F70	0F7A
X000	0120	3000,0FFA
X001	0286	3001,0281
X003	02C7	3002,02C3
X007	0F7C	3003
Z000	0FF0	003D,0F89,0F94,0FAB,0FC1,0F01,0FE0,0FEC
Z020	0F7F	0F78



3001 ABS
ORG /3001
*
*
* ** PROGRAM WAITS **
*
3001 0 0130 DC WT1&1 WAIT 1
*
* NORMAL WAIT AFTER PROGRAM
* LOAD.BIT SWITCH 8 OFF TO
* RUN PROGRAM.BIT SWITCH 8
* ON TO SELECT SCOPING RTN.
* PUSH START TO EXECUTE
*
* NOTE
*
* INSERT 3 JUMPERS AS PER
* SECTION 3.2 A.SET DISPLAY
* ADDRESS REGISTER SWITCH TO
* DISPLAY CAR BEING TESTED.
*
3002 0 0146 DC WT2&1 WAIT 2
*
* OBSERVE CAR LAMPS. CAR
* SHOULD BE 0000. PUSH START
*
3003 0 014A DC WT3&1 WAIT 3
*
* OBSERVE CAR LAMPS. CAR
* SHOULD BE 7FFF. PUSH START
*
3004 0 0153 DC WT4&1 WAIT 4
*
* THIS WAIT WILL OCCUR 15
* TIMES TO ALLOW THE OPER.
* TO CHECK A 1 BIT RIPPLE
* THROUGH THE CAR. THE CAR
* SHOULD BE THE SAME AS THE
* A REG AT EACH WAIT. PUSH
* START .
*
3005 0 0169 DC WT5&1 WAIT 5
*
* THIS WAIT WILL OCCUR 16
* TIMES.AT EACH WAIT THE CAR
* CONTENTS SHOULD BE THE
* SAME AS THE A REG. EACH
* WAIT OCCURS AFTER THE CAR
* IS LOADED WITH 1 OF 16
* STARTING ADDRESSES AND
* STEPPED 50 TIMES. PUSH
* START AFTER EACH WAIT.
*
3006 0 0178 DC WT6&1 WAIT 6
*
* OBSERVE CAR LAMPS. CAR
* SHOULD BE 7FFF. THIS WAIT
* OCCURS AFTER CAR IS LOADED
* TO 0000 AND STEPPED 7FFF
* TIMES.PUSH START TO RETURN
* TO WAIT 1. THIS IS THE END
* OF PROGRAM WAIT.
*
3007 0 0180 DC WT7&1 WAIT 7
*
* SCOPE ROUTINE WAIT. ENTER
* DESIRED STARTING ADDRESS
* IN DATA ENTRY SWITCHES.
* PUSH START TO CONTINUE.
*
88500020
88500030
88500040
88500050
88500060
88500070
88500080
88500090
88500100
88500110
88500120
88500130
88500140
88500150
88500160
88500170
88500180
88500190
88500200
88500210
88500220
88500230
88500240
88500250
88500260
88500270
88500280
88500290
88500300
88500310
88500320
88500330
88500340
88500350
88500360
88500370
88500380
88500390
88500400
88500410
88500420
88500430
88500440
88500450
88500460
88500470
88500480
88500490
88500500
88500510
88500520
88500530
88500540
88500550
88500560
88500570
88500580
88500590
88500600
88500610
88500620
88500630
88500640
88500650
88500660
88500670
88500680
88500690

3008 0 0184 DC WT8&1 WAIT 8
*
*
* SCOPE ROUTINE WAIT. ENTER
* DESIRED NUMBER OF CAR
* STEPS IN DATA ENTRY SWITS.
* PUSH START TO CONTINUE.
*
3009 0 01C4 DC WT9&1 WAIT 9
*
* SCOPE ROUTINE WAIT.BIT SW
* 4 OPTION SELECTED. SINGLE
* STEP CAR WITH START BUTTON
*
300A 0 01ED DC WTA&1 WAIT A
*
* SCOPE ROUTINE WAIT.BIT SW
* 3 OPTION SELECTED. DESIRED
* NUMBER OF CAR STEPS HAVE
* BEEN ISSUED.CAR SHOULD BE
* THE SAME AS THE A REG.
* PUSH START TO CONTINUE
*
300B 0 022B DC WT8&1 WAIT 8
*
* AN INTERNAL INTERRUPT
* OCCURED.THE ILSW IS IN THE
* A REG.PUSH START TO CONTIN
* UE FROM POINT OF INTERRUPT
*
300C 0 0188 DC WTC&1 WAIT C
*
* SCOPE ROUTINE WAIT. ENTER
* CONTROL OPTIONS IN DATA
* ENTRY SWITCHES ACCORDING
* TO TABLE 1 SEC. 3.
* PUSH START TO CONTINUE
*
012C ORG 300
*
*
* *****
* *CHANNEL ADDRESS REG*
* CHECK PROGRAM *
* *
* ** CARCK **
* *****
*
* THIS PROGRAM IS TO BE USED
* IN CONJUNCTION WITH THE
* CYCLE STEAL REQUEST TEST
* AND THE CYCLE STEAL ACKNOW
* LEDGE TEST FEATURE OF THE
* 1800 SYSTEM DATA CHANNELS
*
012C 0 B500 DC /B500 PID
012D 0 631B CARCK LDX 3 27 LOAD INTFRRUPT
012E 0 C04A LD CONST *ADDRESS WITH TRAP
012F 0 D700 0007 STO L3 7 *ROUTINE ADDRESS
0131 0 73FF MDX 3 -1
0132 0 70FC MDX *-4
0133 0 6700 0228 LDX L3 ERROR SET INTERNAL INTRP
0135 0 6F00 0008 STX L3 8 *ADDRESS
*
0137 0 C868 LDD RESRT SET RESTART INSTRN
0138 0 DC00 0000 STD L 0
*
013A 0 0867 XIO UMSKO UNMASK INTERRUPT
013B 0 0868 XIO UMSK1 *LEVELS
88500700
88500710
88500720
88500730
88500740
88500750
88500760
88500770
88500780
88500790
88500800
88500810
88500820
88500830
88500840
88500850
88500860
88500870
88500880
88500890
88500900
88500910
88500920
88500930
88500940
88500950
88500960
88500970
88500980
88500990
88501000
88501010
88501020
88501030
88501040
88501050
88501060
88501070
88501080
88501090
88501100
88501110
88501120
88501130
88501140
88501150
88501160
88501170
88501180
88501190
88501200
88501210
88501220
88501230
88501240
88501250
88501260
88501270
88501280
88501290
88501300
88501310
88501320
88501330
88501340
88501350
88501360
88501370

```

013C 0 3001      *
                  WT1  WAIT  1      SET 8IT SW 8 TO
                  *      *      *SELECT MANUAL MDDE
                  *
013D 0 086C      *
013E 0 C03E      *      X10  8SW      GO TO MANUAL ROUTINE
013F 0 1008      *      LD  BSW1      *IF SELECTED
0140 0 4C28 01AE *      SLA  8
                  *      BSC  L  CARMN,&Z
0142 0 C048      *      LD  ADDR&16  LOAD CAR
0143 0 D062      *      STD  LOAD
0144 0 0861      *      X10  LOAD
0145 0 3002      *
                  WT2  WAIT  2      CAR SHDULD BE 0000
0146 0 C038      *
0147 0 D05E      *      LD  ADDR&1  LOAD CAR
0148 0 085D      *      STD  LOAD
                  *      X10  LOAD
0149 0 3003      *
                  WT3  WAIT  3      CAR SHOULD BE 7FFF
014A 0 6301      *
0148 0 6830      *      LDX  3 1      SET UP CHECK STORAGEF
014C 0 6310      *      STX  3 RIPL
                  *      LDX  3 16
014D 0 C700 018E *
014F 0 D056      *      CAR1 LD  L3 RIPL-1  GET BIT LOAD ADDRESS
0150 0 0855      *      STO  LOAD      SET ADDRESS IN IOCC
0151 0 C02A      *      X10  LOAD      LOAD CAR
                  *      LD  RIPL      LOAD A WITH EXP ADRS
0152 0 3004      *
                  WT4  WAIT  4      CAR SHOULD BE THE
0153 0 1001      *      *SAME AS A REG
0154 0 D027      *
0155 0 73FF      *      SLA  1      SFT CHECK STORAGE FOR
0156 0 70F6      *      STD  RIPL      *NEXT 8IT POSITION
                  *      MDX  3 -1      SKIP WHEN ALL BIT
                  *      MDX  CAR1      *POSITIONS CHECKED
0157 0 6110      *
0158 0 C500 017D *      ** CHECK CAR INCREMENT **
015A 0 D048      *
015B 0 801E      *      LDX  1 16      ADDRESS INDEX
015C 0 001E      *      LD  L1 ADDR-1  SET STARTING ADDRESS
015D 0 6232      *      STO  LDAD      *IN IOCC AND IN
015E 0 0847      *      A  ONE      *COUNTER
                  *      STD  COUNT
015F 0 0848      *      LDX  2 50      STFP INDEX
0160 0 7401 017B *      X10  LOAD      LOAD CAR
0162 0 1000      *
0163 0 72FF      *      CAR3 X10  STEP      STEP CAR
0164 0 70FA      *      MOX  L  COUNT,1  STEP COUNTER
0165 0 C015      *      NDP  2 -1      SKIP IF 50 STFPS
0166 0 1000      *      MDX  CAR3
0167 0 1000      *      LO  CDUNT
                  *      NDP  0      ELIMINATE 8IT PDS.
                  *      NDP  0      *0 FROM CK WORD
0168 0 3005      *
                  WT5  WAIT  5      CAR SHOULD BE THE
0169 0 71FF      *      *SAME AS A REG
016A 0 70ED      *
                  *      MDX  1 -1      SKIP IF ALL ADDRESS
                  *      MDX  CAR2      *USED
0169 0 71FF      *
016A 0 70ED      *      **CHECK INCREMENT FROM**
                  *      **ZERO TO 7FFF      **

```

88501380
88501390
88501400
88501410
88501420
88501430
88501440
88501450
88501460
88501470
88501480
88501490
88501500
88501510
88501520
88501530
88501540
88501550
88501560
88501570
88501580
88501590
88501600
88501610
88501620
88501630
88501640
88501650
88501660
88501670
88501680
88501690
88501700
88501710
88501720
88501730
88501740
88501750
88501760
88501770
88501780
88501790
88501800
88501810
88501820
88501830
88501840
88501850
88501860
88501870
88501880
88501890
88501900
88501910
88501920
88501930
88501940
88501950
88501960
88501970
88501980
88501990
88502000
88502010
88502020
88502030
88502040
88502050

016B 0 1010
016C 0 D039
016D 0 C011
016E 0 D00C
016F 0 0836

0170 0 0837
0171 0 74FF 017B
0173 0 1000
0174 0 C006
0175 0 4820
0176 0 70F9

0177 0 3006
0178 0 7084

0179 0 01F6

017A 0 0001

0178 0 0000

017C 0 0000

017D 0 0000

017E 0 FFFF

017F 0 7FFE

0180 0 7FEF

0181 0 7F0E

0182 0 7EFF

0183 0 70FE

0184 0 70EF

0185 0 700E

0186 0 6FFF

0187 0 0FFF

0188 0 0FEF

0189 0 0F0E

018A 0 0EFF

018B 0 00FF

018C 0 00EF

018D 0 000E

018E 0 FFFF

018F 0 7FFF

0190 0 3FFF

0191 0 1FFF

0192 0 0FFF

0193 0 07FF

0194 0 03FF

0195 0 01FF

0196 0 00FF

0197 0 007F

0198 0 003F

0199 0 001F

019A 0 000F

0198 0 0007

019C 0 0003

019D 0 0001

019E 0 0000

01A0 0000

01A0 0 4C00

01A1 0 012D

01A2 0 0000

01A3 0 0480

01A4 0 0000

01A5 0 0481

01A6 0 0000

01A7 0 05A0

01A8 0 0000

```

*      SLA  16
*      STC  LOAD
*      LD  ADDR&1  SET UP STEP COUNTER
*      STO  CDUNT
*      X10  LOAD      LOAD CAR

```

```

*      CAR4 X10  STEP      STEP CAR
*      MDX  L  CDUNT,-1  STEP COUNTER -1
*      NDP
*      LD  COUNT
*      BSC  7      SKIP IF ALL STEPS
*      MDX  CAR4

```

```

*      WT6  WAIT  6      CAR SHDULD BE 7FFF
*      *
*      MDX  CARCK      REPEAT CHECK

```

** PROGRAM CONSTANTS **

```

CONST DC  SVINT  INTERRUPT ADDRFS
DNE DC  1      CONSTANT ONE
CDUNT DC  0      STEP COUNTER
RIPL DC  0      RIPPLE CHECK WORD
BSW1 DC  0      BIT SWITCH READ IN
ADDRS DC  /FFFF  TABLF OF STARTING
DC  /7FFE      *ADDRESSES
DC  /7FEF
DC  /7F0E
DC  /7EFF
DC  /70FE
DC  /70EF
DC  /700E
DC  /6FFF
DC  /0FFE
DC  /0FEF
DC  /0F0E
DC  /0EFF
DC  /00FE
DC  /00EF
DC  /000E
DC  /000E
DC  /FFFF

```

```

RIPPL DC  /7FFF  RIPPLE TEST LOAD
DC  /3FFF      *ADDRESSES
DC  /1FFF
DC  /0FFF
DC  /07FF
DC  /03FF
DC  /01FF
DC  /00FF
DC  /007F
DC  /003F
DC  /001F
DC  /000F
DC  /0007
DC  /0003
DC  /0001
DC  /0000
DC  /0000
BSS E  0
RESRT DC  /4C00  RESTART INSTRUCTION
DC  CARCK
UMSK0 DC  /0000  UNMASK INTERRUPTS
DC  /0480      *IOCC
UMSK1 DC  /0000
DC  /0481
LOAD DC  0      LOAD CAR IOCC
DC  /05A0
STEP DC  0      INCREMENT CAR IOCC

```

88502060
88502070
88502080
88502090
88502100
88502110
88502120
88502130
88502140
88502150
88502160
88502170
88502180
88502190
88502200
88502210
88502220
88502230
88502240
88502250
88502260
88502270
88502280
88502290
88502300
88502310
88502320
88502330
88502340
88502350
88502360
88502370
88502380
88502390
88502400
88502410
88502420
88502430
88502440
88502450
88502460
88502470
88502480
88502490
88502500
88502510
88502520
88502530
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88502550
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88502590
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88502690
88502700
88502710

01A9	0	01A0		DC	/01A0	
01AA	0	017D	BSW	DC	BSW1	READ BIT SWITCH IOCC
01AB	0	0240		DC	/0240	
01AC	0	0000	SNSW	DC	/0000	READ SNS SWITCH IOCC
01AD	0	0760		DC	/0760	
			*			
			*			
			*			*****
			*			MANUAL AND SCOPE ROUTINE
			*			*****
01AE	0	1000	CARMN	NOP		
01AF	0	3007	WT7	WAIT	7	ENTER STARTING ADDRESS
			*			
01B0	0	08F9		XIO	BSW	READ IN ADDRESS AND
01B1	0	C0CB		LD	BSW1	*SAVE
01B2	0	D040		STD	MNAO	
			*			
01B3	0	3008	WT8	WAIT	8	ENTER NUMBER OF CNTS
01B4	0	08F5		XIO	BSW	
01B5	0	C0C7		LD	BSW1	READ IN NUMBER OF
01B6	0	D03D		STD	MNCT	*COUNTS AND SAVE
			*			
01B7	0	300C	WTC	WAIT	12	ENTER CNTRL OPTIONS
			*			
01B8	0	C03A	CARM2	LD	MNAO	SET STARTING ADDRESS
01B9	0	00EC		STD	LOAD	*INTO IOCC AND INTO
01BA	0	D0C0		STD	COUNT	*COUNTER
01BB	0	C038		LD	MNCT	SFT NUMBER OF STEPS
01BC	0	D038		STD	MNCTR	*IN STEP COUNTER
01BD	0	08E8		XIO	LOAD	LOAD CAR
			*			
01BE	0	08EB	CARM3	XIO	BSW	READ BIT SWITCHES
01BF	0	C08D		LD	BSW1	
01C0	0	1004		SLA	4	SKIP IF BIT SW 4 ON
01C1	0	4810		BSC	-	SKIP IF BIT SW 4 ON
01C2	0	700F		MDX	CARM4	
			*			
01C3	0	3009	WT9	WAIT	9	SINGLE STEP CAR WITH
			*			*START BUTTON
			*			
01C4	0	C02F		LD	MNCT	IF NUMBER OF STEPS
01C5	0	4818		BSC	&-	*ENTERED IS ZERO
01C6	0	70F1		MDX	CARM2	*SS WILL LOAD CAR
01C7	0	08E0		XIO	STFP	STEP CAR
01C8	0	7401	MDX	L	COUNT,1	STEP COUNTER
01CA	0	1000		NOP		
01CB	0	74FF	MDX	L	MNCTR,-1	DECREMENT STFP CNTR
01CD	0	1000		NOP		
01CE	0	C026		LD	MNCTR	
01CF	0	4820		BSC	7	SKIP IF COUNTER ZERO
01D0	0	70ED		MDX	CARM1	CONTINUE STEP
01D1	0	70E6		MDX	CARM2	RELOAD CAR
			*			
			*			** NDT BIT SWITCH 4 **
			*			
01D2	0	C021	CARM4	LD	MNCT	CHECK NUMBER OF STEP
01D3	0	4818		BSC	&-	*SKIP IF STEPS NOT 0
01D4	0	700A		MDX	CARM6	
01D5	0	08D2	CARM5	XIO	STEP	STEP CAR
01D6	0	7401	MDX	L	COUNT,1	STFP COUNTER
01D8	0	1000		NOP		
01D9	0	74FF	MDX	L	MNCTR,-1	DECREMENT STEP CNTR
01DB	0	1000		NOP		
01DC	0	C018		LD	MNCTR	
01DD	0	4820		BSC	Z	SKIP IF COUNTER ZERO
01DE	0	70F6		MDX	CARM5	
			*			
			*			**COUNT CMPLT CK BIT SW2**

88502720
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88502800
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88502990
88503000
88503010
88503020
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88503080
88503090
88503100
88503110
88503120
88503130
88503140
88503150
88503160
88503170
88503180
88503190
88503200
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88503250
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88503390

```

01DF 0 08CA
01E0 0 C09C
01E1 0 1002
01E2 0 4810
01E3 0 7001
01E4 0 70C9
01E5 0 1001
01E6 0 4810
01E7 0 70D0
01F8 0 C092
01E9 0 8090
01FA 0 1001
01F8 0 1801

01EC 0 300A


01E0 0 08BC
01EE 0 C08E
01EF 0 1002
01F0 0 4810
01F1 0 70C6
01F2 0 70BB

01F3 0 0000
01F4 0 0000
01F5 0 0000


01F6 0 0000
01F7 0 D02C
01F8 0 0820
01F9 0 7402 0223
01FB 0 1010
01FC 0 D023
01FD 0 C020
01FE 0 D023
01FF 0 C01D
0200 0 D020
0201 0 C01F
0202 0 100B
0203 0 E81D
0204 0 F81D
0205 0 D01F
0206 0 081D
0207 0 74FF 0221
0209 0 70F7
020A 0 7401 0220
020C 0 C013
0200 0 900E
020E 0 4808
020F 0 70EF
0210 0 74FF 0223
0212 0 7001
0213 0 7005
0214 0 C00A
0215 0 D00C
0216 0 1010
0217 0 D008
0218 0 70E6
0219 0 C00A
021A 0 4CC0 01F6

```

```

*
CARM6 XIO      BSW      READ BIT SWITCHFS
      LD        BSW1
      SLA       2
      RSC       -        SKIP IF BIT SW 2 ON
      MDX       *E1
      MDX       CARMN    CHANGE PARAMETERS
      SLA       1
      BSC       -        SKIP IF BIT SW 3 ON
      MDX       CARM2    LOOP ROUTINE
      LD        COUNT
      A         ONE
      SLA       1
      SRA       1        ELIMINATE BIT POS.
                          *0 FROM CK WORD
*
WTA    WAIT     10       CAR SHOULD BE SAME
*
*
*
      XIO      BSW      READ BIT SWITCHES
      LD        BSW1
      SLA       2
      BSC       -        SKIP IF BIT SW 2 ON
      MDX       CARM2    RERUN PRESENT SETUP
      MOX       CARMN    CHANGE PARAMETERS
*
MNAD   DC       0        ADDRESS ENTRY
MNCT   DC       0        NUMBER STEPS ENTRY
MNCTR  DC       0        STEP COUNTER
*
*
*
*****
***** INTERRUPT TRAP ROUTINE *****
*****
*
*
SVINT  DC       0
      STO      SVI0      SAVE ACCUMULATOR
      XIO      ILSW      RESET ILSW
      MDX      L SV7,2   SET PASS SWITCH
      SLA      16
      STO      SV4       CLEAR AREA COOF CNTR
      LD       SV2
      STD      SV6       SET IOCC IN USE SW
SVINO  LD       SV1
      STO      SV5       SET MODIFIER COUNTER
SVINI  LD       SV4
      SLA      11
      OR       SV5       *BUILT IOCC
      OR       SV6       *
      STO      SVIOE1    *
      XIO      SVIO      SENSE/RESET DISW
      MDX      L SV5,-1
      MDX      SVINI     BRNCH IF NOT ALL MOD
      MDX      L SV4,1   INCREMENT AREA CODE
      LD       SV4
      S        SV0       CHECK IF ALL AC USED
      RSC      E        SKIP IF ALL AC USED
      MDX      SVINO     GD SENSE WITH NXT AC
      MDX      L SV7,-1  SKIP IF SECOND PASS
      MDX      *E1
      MDX      SVEXT-1
      LD       SV3       SET IDCC FOR PI
      STO      SV6
      SLA      16
      STD      SV4       SET AC FOR NEXT PASS
      MOX      SVINO
      LD       SVIO
      SVEXT  BDSC I     RESTORE ACCUMULATOR
*
*
*
** CONSTANTS **

```

88503400
88503410
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88503990
88504000
88504010
88504020
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88504060
88504070

```

021C 0 001F      *      DC      /001F      NUMBER OF ARFA CODES      88504080
0210 0 00FF      SV1     DC      /00FF      NUMBER OF MODIFIERS      88504090
021E 0 0701      SV2     DC      /0701      SENSE/RESET DSW      88504100
021F 0 0700      SV3     DC      /0700      SENSE/RESET PISW      88504110
0220 0 0000      SV4     DC      0          AREA CODE INDICATOR      88504120
0221 0 0000      SV5     DC      0          MODIFIER INDICATOR      88504130
0222 0 0000      SV6     DC      0          IOCC IN USE      88504140
0223 0 0000      SV7     DC      0          PASS SWITCH      88504150
0224 0000        8SS     F 0          SFNSE DSW/PISW IOCC      88504160
0224 0 0000      SVIO    DC      0          SENSE ILSW IOCC      88504170
0225 0 0000      OC      0          88504180
0226 0 0000      ILSW    DC      0          SENSE ILSW IOCC      88504190
0227 0 0300      DC      /0300      88504200
                                     88504210
                                     88504220
*                                     88504230
*                                     ERROR TRAP ROUTINE
*                                     88504240
*                                     88504250
*                                     88504260
0228 0 0000      ERROR   DC      0          ENTRY PDINT      IE 88504270
0229 0 0BFC      XID     ILSW      SENSE ILSW      88504280
022A 0 3008      WTB     WAIT     11      ILSW IN A REG      88504290
022B 0 4CC0 0228 *      BDSC I  ERROR      EXIT      IX 88504300
                                     88504310
022E 012D        END     CARCK      88504320
NO STATEMENTS FLAGGED IN THE ABOVE ASSFM8LY
```

```

ADDRS 017F 0142 0146 0158 016D
BSW 01AA 013D 0180 0184 018E 01DF 01ED
BSW1 017D 013E 01AA 0181 0185 018F 01E0 01EE
CARCK 012D 0178 01A1 022E
CARMN 01AE 0140 01E4 01F2
CARM2 0188 01C6 01D1 01E7 01F1
CARM3 018E 01D0
CARM4 0102 01C2
CARM5 01D5 01DF
CARM6 010F 01D4
CAR1 014D 0156
CAR2 015B 016A
CAR3 015F 0164
CAR4 0170 0176
CONST 0179 012E
COUNT 0178 015C 0160 0165 016E 0171 0174 018A 01C8 01D6 01FB
ERROR 0228 0133 0228
ILSW 0226 01F8 0229
LOAD 01A6 0143 0144 0147 0148 014F 0150 015A 015F 016C 016F 0189 01BD
MNAD 01F3 0182 0188
MNCT 01F4 0186 0188 01C4 01D2
MNCTR 01F5 018C 01C8 01CE 01D9 01DC
ONF 017A 015B 01E9
RESRT 01A0 0137
RIPL 017C 0148 0151 0154
RIPPL 01BF 014D
SNSW 01AC
STFP 01A8 015F 0170 01C7 01D5
SVFXT 021A 0213
SVINT 01F6 0179 021A
SVINO 01FF 020F 0218
SVINI 0201 0209
SVID 0224 01F7 0205 0206 0219
SV0 021C 020D
SV1 021D 01FF
SV2 021E 01FD
SV3 021F 0214
SV4 0220 01FC 0201 020A 020C 0217
SV5 0221 0200 0203 0207
SV6 0222 01FE 0204 0215
SV7 0223 01F9 0210
UMSK0 01A2 013A
UMSK1 01A4 013B
WTA 01FC 300A
WT8 022A 3008
WTC 0187 300C
WT1 013C 3001
WT2 0145 3002
WT3 0149 3003
WT4 0152 3004
WT5 0168 3005
WT6 0177 3006
WT7 01AF 3007
WT8 0183 3008
WT9 01C3 3009
FND OF ASSEMBLY
```

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1. PURPOSE

THE CAR EXERCISER PROGRAM IS TO BE USED IN CONJUNCTION WITH THE CYCLE STEAL REQUEST TEST AND THE CYCLE STEAL ACKNOWLEDGE TEST FEATURES OF THE DATA CHANNEL. THE PROGRAM IS USED TO LOAD AND STEP THE CAR SELECTED FOR TEST. ALL BIT POSITIONS IN THE C.A.R. ARE TESTED.

2. PREREQUISITES

2.1 PROGRAM PREREQUISITES

THE BASIC DIAGNOSTIC LOADER IS REQUIRED TO LOAD THIS PROGRAM.

2.2 EQUIPMENT PREREQUISITES

THE FOLLOWING EQUIPMENT IS REQUIRED.

A. 1800 PROCESSOR/CONTROLLER
B. 1442 CARD READ/PUNCH OR 1054 PAPER TAPE READER.

3. USE PROCEDURE

3.1 PROGRAM LOADING

REFER TO BASIC DIAGNOSTIC LOADER DOCUMENTATION FOR PROGRAM LOADING PROCEDURES.

3.2 PROGRAM OPERATION

WITH PROGRAM STOPPED AT WAIT 1, B REG = 3001,

A. INSERT 3 JUMPERS AS FOLLOWS, TO ACTIVATE THE CYCLE STEAL REQUEST TEST AND CYCLE STEAL ACKNOWLEDGE TEST LEVELS.

JUMPER 1 - B-B1G2D02 (CR221) TO B-B1G5D09 (CQ111)

THIS JUMPER ACTIVATES THE SET CAR FUNCTION DURING AN INITIALIZE READ OR WRITE.

JUMPERS 2 AND 3 - REFER TO LOGIC PAGE CT971. INSTALL JUMPER 2 FROM CS REQUEST TEST SIGNAL TO CS REQUEST LEVEL TO BE TESTED. INSTALL JUMPER 3 FROM CS ACKNOWLEDGE TEST TO CS ACKNOWLEDGE LEVEL TO BE TESTED. INSTALL JUMPER 3 FROM CS ACKNOWLEDGE TEST TO CS ACKNOWLEDGE LEVEL TO BE TESTED.

NOTE - POINTS FOR CHANNEL 0 - 8 ARE ON THE 60 B - B1 BOARD AND POINTS FOR CHANNEL 9 - 14 ARE ON THE 60 D - A1 BOARD.

- B. SET 'DISPLAY ADDRESS REGISTER' SWITCH TO DISPLAY C.A.R. BEING TESTED.
- C. TO RUN PROGRAM CONTROLLED MODE, SET DATA ENTRY SWITCHES TO 0000. DEPRESS START. PROCEED WITH PROGRAM ACCORDING TO WAIT INSTRUCTIONS 2 THROUGH 6.
- D. TO RUN MANUAL CONTROLLED (SCOPING) MODE, SET DATA ENTRY SWITCH 8 ON AND DEPRESS START. PROGRAM WILL COME TO WAIT 7. PROCEED WITH PROGRAM ACCORDING TO WAIT INSTRUCTIONS 7 THROUGH A.

TABLE 1
PROGRAM OPTIONS - DATA ENTRY SWITCHES

NOTE -- FUNCTIONS OF SWITCHES 2,3,4 ARE FOR SCOPING ROUTINES ONLY.

*															
*	NOTE. TABLE 1 PROGRAM OPTIONS MAY BE ENTERED ONLY WHEN PROGRAM IS STOPPED AT WAIT 1.														
*															
*	*****														
*															
*	DATA ENTRY SWITCHES														
*	OPTION DESCRIPTION														
*	*****														
*	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14 15
*	*****														
*															
*	0.....RUN PROGRAM CONTROLLED MODE														
*	1.....RUN MANUAL CONTROLLED (SCOPING) MODE														
*	1.....SINGLE STEP C.A.R. WITH START														
*	PUSHBUTTON														
*	1.....STOP AFTER EACH PASS THROUGH SCOPE														
*	ROUTINE														
*	1.....RETURN TO WAIT 7 TO CHANGE ADDRESS														
*	AND NUMBER OF STEPS														
*	*****														

3.3 PROGRAM TERMINATION

IF RUNNING PROGRAM CONTROLLED MODE, PROGRAM WILL EXECUTE ONCE AND STOP AT WAIT 6. DEPRESSING THE START PUSHBUTTON WILL RETURN THE PROGRAM TO WAIT 1, WHICH IS THE START OF THE PROGRAM.

IF RUNNING MANUAL (SCOPE) CONTROLLED MODE, PROGRAM MAY BE TERMINATED BY DEPRESSING THE STOP PUSHBUTTON. DEPRESSING RESET AND START WILL RETURN THE PROGRAM TO WAIT 1.

IMPORTANT NOTE

BEFORE RETURNING SYSTEM TO THE CUSTOMER, INSURE THAT THE 3 JUMPERS INSERTED AT WAIT 1, ARE REMOVED FROM THE CHANNEL.

3.4 RESTART PROCEDURE

PRESS THE STOP, RESET AND START BUTTONS. THE PROGRAM SHOULD GO TO WAIT 1. IF THIS DOES NOT OCCUR, THE PROGRAM MUST BE RELOADED.

3.5 PROGRAM HALTS

PROGRAM WAITS ARE USED IN THIS PROGRAM, AND ARE IDENTIFIED BY
REFERENCING THE B REG AND 1 REG.

A PROGRAM WAIT IS OF THE FORM,

3DXX, (B REG).

A DESCRIPTION OF THE INDIVIDUAL PROGRAM WAITS CAN BE FOUND AT THE
BEGINNING OF THE PROGRAM LISTING. A TYPICAL WAIT DESCRIPTION FOLLOWS.
IT IS INCLUDED TO SHOW THE FORMAT OF THE LISTING, AND IT IS NOT
NECESSARILY A DESCRIPTION OF AN ACTUAL WAIT.

```
30D1 0 01ED      OC      WAIT1+1
                  *      WAIT 1
                  *
                  *      ONE OF THE METERED I/O UNITS
                  *      FAILED TO SEND A RESPONSE
                  *      INTERRUPT TO THE PROGRAM. INOEX
                  *      REGISTER 1 WILL HAVE THE ADDRESS
                  *      OF THE IOCC. THE AREA CODE WILL
                  *      INDICATE THE I/O UNIT NOT READY.
                  *      IF A 2401/02 DRIVE IS NOT READY,
                  *      PROGRAM WILL NOT STOP AT WAIT 1.
                  *
```

B REG, (FIRST 4 DIGIT GROUP) CORRESPONDS TO B REG READING.
1 REG, (SECOND 4 DIGIT GROUP) CORRESPONDS TO 1 REG READING.

4. PRINTOUTS

THERE ARE NO PRINTOUTS ASSOCIATED WITH THE CAR EXERCISER PROGRAM.

5. COMMENTS

(THE CAR EXERCISER CONSISTS OF A PROGRAM CONTROL ROUTINE, AND A
MANUAL (OPERATOR CONTROL) ROUTINE.

THE PROGRAM CONTROL ROUTINE CONTAINS THE STARTING ADDRESS AND
PREDEFINED NUMBER OF STEPS USED TO LOAD AND INCREMENT THE C.A.R.
BEING TESTED.

THE C.A.R. IS LOADED USING AN XIO INSTRUCTION WHOSE IOCC IS
00DD D5A0. THE CAR WILL AUTOMATICALLY BE INCREMENTED BY 1 EACH TIME
IT IS LOADED. THE INCREMENT BY 1 IS A HARDWARE FUNCTION. THE CAR IS
STEPPED USING AN XIO INSTRUCTION WHOSE IOCC IS 00DD D1A0. EACH XIO
CAUSES THE CAR TO BE INCREMENTED BY 1.

WAITS 2 AND 3 ARE USED TO DISPLAY THE RESULTS OF LOADING TESTS.
THE C.A.R. IS FIRST LOADED TO 7FFF. THE AUTO INCREMENT SHOULD
STEP IT TO DDD0. THIS RESULT IS DISPLAYED AT WAIT 2. THE C.A.R.
IS THEN LOADED TO 7FFE. THE AUTO INCREMENT SHOULD STEP IT TO 7FFF.
THIS IS DISPLAYED AT WAIT 3.

WAIT 4 IS USED TO DISPLAY THE RESULTS OF RIPPLING A 1 BIT THROUGH ALL
CAR BIT POSITIONS. EXCEPT BIT D. EACH TIME THE WAIT OCCURS, THE A
REG WILL CONTAIN THE VALUE THAT SHOULD APPEAR IN CAR.

THE LOAD ADDRESSES ARE AS FOLLOWS,

1. 0000	9. D0FF
2. D001	10. 01FF
3. DDD3	11. D3FF

4. 0007	12. 07FF
5. 000F	13. 0FFF
6. 001F	14. 1FFF
7. 003F	15. 3FFF
8. D07F	

WAIT 5 IS USED TO DISPLAY THE RESULTS OF THE C.A.R. INCREMENT TEST.
THE C.A.R. IS LOADED 16 TIMES WITH 16 DIFFERENT STARTING ADDRESSES,
AND AFTER EACH LOAD THE C.A.R. IS INCREMENTED 50 TIMES. EACH TIME
THE WAIT OCCURS, THE A REG. WILL CONTAIN THE VALUE WHICH SHOULD
APPEAR IN THE C.A.R.

THE STARTING ADDRESSES USED ARE AS FOLLOWS,

1. 7FFF	9. 6FFF
2. DDOE	10. 700E
3. 00EF	11. 70EF
4. 0DFF	12. 70FE
5. 0EFF	13. 7EFF
6. 0F0E	14. 7F0E
7. 0FEF	15. 7FEF
8. 0FFE	16. 7FFE

FOLLOWING THE INCREMENT TEST, THE CAR. IS LOADED TO 00D0 AND STEPPED
7FFE TIMES. AT WAIT 6, THE CAR SHOULD CONTAIN 7FFF.

ANY ERRORS OBSERVED BY THE OPERATOR CAN BE LOOPED BY SETTING THE
STARTING ADDRESS AND NUMBER OF STEPS USED BY THE AUTO ROUTINE INTO
THE MANUAL ROUTINE, AND RUNNING THE MANUAL ROUTINE WITH SENSE
SWITCH 0 ON.

THE MANUAL CONTROLLED ROUTINE WILL LOAD THE C.A.R. WITH THE ADDRESS
ENTERED BY THE OPERATOR AT WAIT 7. IT WILL THEN STEP THE C.A.R.
THE NUMBER OF TIMES SPECIFIED BY THE OPERATOR AT WAIT 8. CONTROL
OF THE ROUTINE IS TRANSMITTED VIA THE DATA ENTRY SWITCHES. (SEE
TABLE 1). IF THE NUMBER OF STEPS ENTERED BY THE OPERATOR AT WAIT 8
IS ZERO, THEN THE PROGRAM WILL ISSUE CONTINUOUS LOAD C.A.R.
INSTRUCTION ACCORDING TO THE DATA ENTRY SWITCH SETTING. IF NO
CONTROL OPTIONS ARE ENTERED, THE SCOPING ROUTINE WILL LOOP
CONTINUOUSLY USING THE DATA ENTERED AT WAITS 7 AND 8 AS INPUT
PARAMETERS.

6. APPENDIX (NONE)

----- LAST PAGE -----

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM
METER EXERCISER

PART NO. 2196479
PAGE 1

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM
METER EXERCISER

PART NO. 2196479
PAGE 1A

```
3001          ABS
              ORG   /3001
*-----*
*          PROGRAM WAIT SECTION
*
3001 0 01F6    DC      WAIT1&1    WAIT 1
*
*          ONE OF THE METERED I/O UNITS
*          FAILED TO SEND A RESPONSE
*          INTERRUPT TO THE PROGRAM. INDEX
*          REGISTER 1 WILL HAVE THE ADDRESS
*          OF THE IOCC. THE AREA CODE WILL
*          INDICATE THE I/O UNIT NOT READY.
*          IF A 2401/02 DRIVE IS NOT READY,
*          PROGRAM WILL NOT STOP AT WAIT 1.
*
3002 0 0210    DC      WAIT2&5    WAIT 2
*
*          SET THE PC DATA ENTRY SWITCHES TO
*          INDICATE THE NUMBER OF 72 SECOND
*          DELAY LOOPS DESIRED. PRESS THE
*          PC START BUTTON TO START THE
*          EXERCISE.
*
3003 0 0230    DC      WAIT3&1    WAIT 3
*
*          END OF DELAY. METERS SHOULD BE
*          READ AND THE ELAPSED TIME COMPUTED
*          BY HAND. TO RUN THE TEST AGAIN,
*          SET THE PC DATA ENTRY SWITCHES
*          TO INDICATE THE NUMBER OF 72
*          SECOND DELAY LOOPS DESIRED AND
*          PRESS THE PC START BUTTON.
*
3004 0 0200    DC      WAIT4&1    WAIT 4
*
*          DID NOT RECEIVE A PRINTER COMPLETE
*          INTERRUPT FROM THE 1443.
*          MAKE THE 1443 READY AND THEN PRESS
*          THE PC START BUTTON.
*-----*
0123          ORG   /0123
0123 0 B600    DC      /B600
*****
*          TO RESTART THE PROGRAM,
*          PRESS THE PC RESET BUTTON AND
*          START BUTTON. THE PROGRAM
*          WILL START ALL METERS AGAIN
*          AND STOP AT @WAIT 2@.
*****
0124 0 012C    DC      LOAD
012C          ORG   /012C
012C 0 C400 02D0  LOAD  LD  L  RSTR
012E 0 D400 0000          STO L  0
0130 0 C400 02D1          LD  L  RSTR&1
0132 0 D400 0001          STO L  1
0134 0 610D          LDX  1 13      XR1# NO OF EDITS
0135 0 6203          LDX  2 /0003
0136 0 6700 02A9          LDX  L3 CNFIG
*
*****
*          TRANSFER THE EDIT CARD INFO
*          TO I/O CONFIGURATION
```

```
0138 0 C200
0139 0 D300
013A 0 7201
013B 0 7301
013C 0 71FF
013D 0 70FA
```

```
013E 0 C400 02B8
0140 0 D400 02B7
0142 0 C400 02C8
0144 0 D400 02B6
0146 0 6500 02B0
0148 0 6600 02A9
014A 0 C200
014B 0 4B2B
014C 0 702B
014D 0 1B0B
014E 0 4B1B
014F 0 7022
0150 0 D400 02BA
0152 0 67B0 02BA
0154 0 C400 02C8
0156 0 B400 02BA
0158 0 7010
0159 0 1000
015A 0 73F2
015B 0 7005
015C 0 C400 02B8
015E 0 EC00 02C9
0160 0 700D
0161 0 C400 02B8
0163 0 1B01
0164 0 73FF
0165 0 70FD
0166 0 EC00 02C9
0168 0 7005
0169 0 C400 02B8
016B 0 1B01
016C 0 73FF
016D 0 70FD
016E 0 D4B0 02B7
0170 0 4C00 017F
0172 0 C400 02B8
0174 0 70F9
0175 0 D500 0000
0177 0 7102
0178 0 7402 02B7
017A 0 7202
017B 0 74FE 02B6
017D 0 70CC
017E 0 7023
```

```
017F 0 7401 02B7
0181 0 C200
0182 0 100B
0183 0 1B0C
0184 0 4B1B
0185 0 700F
```

```
*          TABLE. %CNFIG<
*****
XFER  LD  2 0
      STO 3 0
      MDX 2 1
      MDX 3 1
      MDX 1 -1
      MDX XFER
*
*****
*          BUILD INTR LVL WORD AND STORE
*          IN INTERRUPT TABLE. %ITBLE<
*****
RERUN LD  L  ITBLE      ADDR OF ITBLE
      STO L  ITBLI
      LD  L  EDCT      SET EDIT COUNT
      STO L  EDCT1     *TO 12.
      LDX L1 IOCC
      LDX L2 CNFIG
BUILD  LD  2 0
      BSC  &Z          IS THIS DEV ON SYS
      MDX  NODEV       DEV NOT ON SYSTEM
      SRA  8
      BSC  &-
      MDX  BIT0
      STO L  WORD
      LDX L3 WORD
      LD  L  EASY      A REG # 000E
      CMP L  WORD      IS INTR LVL LESS
      MDX  LES14       *THAN FOURTEEN
      NOP  0
      MDX  3 -14
      MDX  ILWD1
      LD  L  EIGHT     INTR LVL # 14
      OR  L  ONE
      MDX  ST01
ILWD1 LD  L  EIGHT
SHRT2 SRA  1
      MDX  3 -1
      MDX  SHRT2
      OR  L  ONE      BIT 15# 1 MEANS IL
      MDX  ST01       *IS GREATER THAN 13.
LFS14 LD  L  EIGHT
SHRT1 SRA  1
      MDX  3 -1
      MDX  SHRT1
ST01  STO I  ITBL1     STO IL WD IN TABLE
      BSC  L  ILSWD    GO TO BUILD ILSW WD
BIT0  LD  L  EIGHT     A0# 1
      MDX  ST01
NODEV STO L1 0
      MDX  1 2
      MDX  L  ITBL1,2
      MDX  2 2
      MDX  L  EDCT1,-2
      MDX  BUILD
      MDX  ADDR
*
*****
*          BUILD ILSW WORD AND STORE IN
*          INTERRUPT TABLE. %ITBLE<
*****
ILSWD MDX L  ITBL1,1
      LD  2 0
      SLA  8
      SRA  12
      BSC  &-
      MDX  BZERO
```

```
0186 0 D400 02BA      STC L WORD
0188 0 6780 02BA      LDX I3 WORO
018A 0 C400 02B8      LO L EIGHT
018C 0 1801          SHRT3 SRA 1
0180 0 73FF          MOX 3 -1
018E 0 70FD          MOX SHRT3
018F 0 D480 0287      STO2 STO I ITBL1
0191 0 7401 0287      MDX L ITBL1,1 INCR ITBLE
0193 0 4C00 0198      BSC L CTLWO
0195 0 C400 02B8      BZERO LD L EIGHT
0197 0 70F7          MDX STO2
*
*****
* BUILD IOCC CONTROL WORO
*****
0198 0 7201          CTLWO MDX 2 1
0199 0 7101          MDX 1 1
019A 0 C200          LO 2 0
019B 0 E900          OR 1 0
019C 0 0100          STO 1 0 PUT CTRL WO IN IOCC
*
* DECR REGISTERS
*
0190 0 7201          MDX 2 1
019E 0 7101          MDX 1 1
019F 0 74FE 0286      MDX L EDCT1,-2
01A1 0 70A8          MDX BUILD
*
*****
* LOAD THE ADDR OF %SVINT< INTO
* ALL INTR ADDR LOCATIONS.
*****
01A2 0 6218          ADDR LOX 2 24
01A3 0 6108          LOX 1 8
01A4 0 C400 02CC      LO L INTRN
01A6 0 D100          ADDR1 STO 1 0
01A7 0 7101          MDX 1 1
01A8 0 72FF          MOX 2 -1
01A9 0 70FC          MOX ADDR1
*
*****
* DETERMINE IF THIS DEVICE IS
* ON THE SYSTEM AND IF DESIREO
* INTR IS GREATER THAN
* LEVEL 13
*****
01AA 0 C400 02C8      LO L EDCT
01AC 0 D400 02B6      STO L EDCT1
01AE 0 6500 0280      LDX L1 IOCC XR1# IOCC
01B0 0 6600 02BC      LOX L2 ITBLE&1 LOC OF INTR TABLE
01B2 0 C100          LORT2 LO 1 0 PUT IOCC IN ACCUM
01B3 0 F400 02B9      EOR L FFFF
01B5 0 4818          BSC &- IS THIS DEV ON SYS
01B6 0 700C          MOX INCR NO
01B7 0 C200          LORT1 LO 2 0 PUT IL WD IN ACCUM
01B8 0 4804          BSC E GREATER THAN 13
01B9 0 7013          MOX GREAT
01BA 0 C400 028D      LD L MASK&1
01BC 0 E400 02CA      AND L FFFE SET BIT 15# ZERO
01BE 0 D400 028D      STO L MASK&1
01C0 0 C200          LO 2 0 PUT IL WD IN ACCUM
01C1 0 630B          LOX 3 /000B XR3# 11
01C2 0 7012          MOX VECT
01C3 0 C400 02B9      INCR LD L FFFF
01C5 0 D400 02D3      STO L PASS1
01C7 0 7102          MOX 1 2
01C8 0 7202          MDX 2 2
01C9 0 74FE 02B6      MOX L EDCT1,-2
01CB 0 70E6          MDX LDRT2 GO TO NEXT IOCC
```

```
01CC 0 703E          MOX WAIT2 RDY TO START OELAY
*
*****
* PLACE THE XFER VECTOR IN THE
* CORRECT ADDRESS
*****
01CD 0 C400 02BD      GRFAT LO L MASK&1
01CF 0 EC00 02C9      OR L ONE
01D1 0 D400 02BD      STO L MASK&1 SET BIT 15# ONE
01D3 0 6319          LDX 3 /0019
01D4 0 C200          LO 2 0 PUT IL WD IN ACCUM
*
01D5 0 4828          VECT BSC &Z IS BIT 0 ON
01D6 0 7003          MDX VECT1 * YES
01D7 0 1001          SLA 1 * NO
01D8 0 7301          MOX 3 1
01D9 0 70FB          MOX VECT
01DA 0 C400 02CD      VECT1 LD L XFER1 ADOR OF DESIREO LVL
01DC 0 D300          STO 3 0
*
* COMPLEMENT THE IL WO AND STORE
*
LO 2 0 PUT IL WD IN ACCUM
EOR L FFFF COMPLEMENT IL WORO
STO L MASK
XIO L MASK UNMASK THE DESIREO
*INTR LEVEL
*
*****
* PUT CORRECT AREA CODE IN SENSE
* DEVICE IOCC WORD AND START
* THE I/O METER%<.
*****
01E4 0 C101          LO 1 1 PUT CTRL WD IN ACCUM
01E5 0 EC00 02D2      OR L SENSE
01E7 0 D400 0293      STO L SNSD&1
01E9 0 7201          MDX 2 1
01EA 0 C400 02D3      LD L PASS1 XR2# ILSW WORO
01EC 0 4810          BSC - BYPASS THE FIRST 1442
01EE 0 7009          MOX BYPAS *IF THIS IS THE FIRST
01EF 0 0C00 0292      XIO L SNSO *PASS THRU THE PROGRAM.
01F0 0 7401 02BA      MDX L WORD,1 THESE 3 OPS NEEDED
01F2 0 0C00 0292      XIO L SNSD *IF THE DEVICE BEING
01F4 0 0900          XIO 1 0 *STARTED IS A TAPE DR
01F5 0 3001          WAIT1 WAIT 1 START A METER
01F6 0 1000          NOP 0
01F7 0 C400 02B9      BYPAS LD L FFFF
01F9 0 D400 02D3      STO L PASS1
01FB 0 C100          LO 1 0 ADOR WORD OF IOCC
01FC 0 F400 0284      EOR L IOCC&4 1443 IOCC ADOR WORD
01FE 0 4818          BSC &- IS THIS DEVICE 1443
01FF 0 3004          WAIT4 WAIT 4 YES, WAIT FOR
*PRINTER COMPL INTR
*
NOP 0
LO L INTRN RESTORE ALL XFER
STO 3 0 *VECTORS TO SVINT
MOX L EOCT1,-2
MOX INCR1
MDX WAIT2
INCR1 MDX 1 2 RDY TO START DELAY
MOX 2 1 XR1# NEXT IOCC
MDX LDRT2 XR2# I/O DEV INT TBLE
WAIT2 XIO L MASK1 MASK INTR 0-13
XIO L MASK2 MASK INTR 14-23
WAIT 2
NOP 0
*
*****
```

```
*
*      DETERMINE THE NO OF 72 SEC
*      LOOPS TO BE TAKEN %FROM DATA
*      ENTRY SWS< AND THEN START DLY.
*****
0211 0 0C00 027E  DLY0 XIO L DESWS  RD DATA ENTRY SWS
0213 0 6580 02CE  LDX I1 COUNT  XR1# NO DF 72 SEC LP
0215 0 C400 02B5  LD L CNFIG&12  CDRE STOR SPEED
0217 0 18C1      SRA 1
0218 0 4804      BSC E 2 DR 4 USEC STORAGE
0219 0 7019      MDX FAST 2 US STORAGE
021A 0 C400 02A6  LO L FDRUS 4 US STORAGE
021C 0 D4D0 02A8  DLY1 STO L CONST
021E 0 6365      DLY2 LDX 3 101 XR3# 101
021F 0 C400 02A8  DLY3 LD L CONST
0221 0 8400 02C9  DLY4 A L ONE ADD 1 TO ACCUM
0223 0 4820      BSC Z
0224 0 70FC      MDX DLY4
0225 0 73FF      MDX 3 -1
0226 0 70F8      MDX DLY3
0227 0 71FF      MDX 1 -1 MODIFY LOOP COUNT
0228 0 70F5      MDX DLY2
0229 0 C400 028B  LD L MASK-1 CHANGE IOCC FDR
022B 0 F400 02D4  EOR L TWTY *THE 2402
022D 0 D400 028B  STO L MASK-1
022F 0 30C3      WAIT3 WAIT 3 END DF TEST
*
*****
*      TO RUN TEST AGAIN, SET DATA
*      SWITCHES TO THE NUMBER OF
*      72 SECONO LOOPS DESIRED, AND
*      PRESS THE START BUTTON.
*****
0230 0 1000      NOP 0
0231 0 4C00 0211  BSC L DLY0
0233 0 C400 02A7  FAST LD L TWOUS
0235 0 70E6      MDX DLY1
*
*****
*      ROUTINE TO SERVICE PRDGRAM
*      GENERATED INTERRUPTS.
*****
0236 0 0000      IRTN OC 0
0237 0 0CC0 027A  XIO L SV8 SENSE ILSW
0239 0 E200      AND 2 0
023A 0 4C20 023F  BSC L SENS1,Z ODES ILSW MATCH
023C 0 C0F9      LD IRTN NO
023D 0 D005      STO SVINT
023E 0 7005      MDX SVINT&1 GD TO COMM INTR RTN
023F 0 0CC0 0292  SENS1 XIO L SNSD SENSE AND RESET DEV
0241 0 4CC0 0236  BOSC I IRTN EXIT
*
*****
*      ROUTINE TO SERVICE NON
*      PRDGRAM GENERATED INTERPT
*      %WILL HANDLE ONLY ONE
*      INTERRUPT AT A TIME<
*****
0243 0 0000      SVINT DC 0
0244 0 D037      STD SVID SAVE ACCUMULATOR
0245 0 0C00 027A  XIO L SV8 RESET ILSW
0247 0 7402 0279  MDX L SV7,2 SET PASS SWITCH
0249 0 1010      SLA 16
024A 0 002B      STO SV4 CLEAR AREA CODE CNTR
024B 0 C028      LD SV2
024C 0 D02B      STO SV6 SET IOCC IN USE SW
024D 0 C025      SVINO LD SV1
024E 0 D028      STO SV5 SET MODIFIER COUNTER
024F 0 C026      SVINI LO SV4 *
```

```
0250 0 100B      SLA 11
0251 0 E825      DR SV5
0252 0 E825      DR SV6
0253 0 D029      STO SVID&1
0254 0 0827      XIO SVID SENSE DSW AND RESET
0255 0 74FF 0277  MDX L SV5,-1
0257 0 70F7      MDX SVINI BRANCH IF NOT ALL MD
0258 0 7401 0276  MDX L SV4,1 INCREMENT AREA CODE
025A 0 C01B      LD SV4
025B 0 9016      S SV0 CHECK IF ALL AC USED
025C 0 4808      BSC & SKIP IF ALL AC USED
025D 0 70EF      MDX SVINO GD SENSE WITH NXT AC
025E 0 74FF 0279  MDX L SV7,-1 SKIP IF SECDNO PASS
0260 0 7001      MDX *61
0261 0 7005      MDX SVEXT
0262 0 C012      LD SV3
0263 0 D014      STO SV6 SET IOCC FOR PI
0264 0 1010      SLA 16
0265 0 D010      STO SV4 SET AC FDR NEXT
0266 0 70E6      MDX SVINO *PASS
0267 0 C400 02CF  SVEXT LD L NOOP IF INTERRUPTED OUT
0269 0 F480 0243  EOR I SVINT *OF ANY WAIT, RETURN
0268 0 4C20 026F  BSC L SV11,Z *TO THAT WAIT.
026D 0 74FF 0243  MDX L SVINT,-1
026F 0 C00C      SV11 LD SVIC RESTORE ACCUMULATOR
0270 0 4CC0 0243  BOSC I SVINT EXIT
*
*      ** CONSTANTS **
*
0272 0 001F      SV0 DC /001F NUMBER OF AREA CODES
0273 0 00FF      SV1 DC /00FF NUMBER OF MODIFIERS
0274 0 0701      SV2 DC /0701 SENSE/RESET OSW
0275 0 0700      SV3 DC /0700 SENSE/RESET PI SW
0276 0 0000      SV4 DC 0 AREA CODE INOICATOR
0277 0 0000      SV5 DC 0 MODIFIER INOICATOR
0278 0 0000      SV6 DC 0 IOCC IN USE
0279 0 0000      SV7 DC 0 PASS SWITCH
*
*      I/D CONTROL COMMANOS
*
027A 0 0000      BSS E 0
027A 0 0000      SV8 DC /0000 IOCC TO SENSE
027B 0 0300      SV9 DC /0300 THE ILSW
027C 0 0000      SVIO DC 0 SENSE DSW IOCC
027D 0 0000      DC 0
027E 0 02CE      DESWS DC COUNT IOCC TO READ THE
027F 0 0240      OC /0240 DATA ENTRY SWITCHES
0280 0 0000      IDCC DC /0000 IOCC TO START
0281 0 0402      DC /0402 1442 NO1 METER
0282 0 00C0      DC 0 IOCC TO START
0283 0 0402      DC /0402 1442 NO2 METER
0284 0 0294      DC PRINT IOCC TO START
0285 0 0500      DC /0500 1443 NO1 METER
0286 0 0290      DC READ IOCC TO START
0287 0 0600      DC /0600 2401 NO1 METER
0288 0 029D      DC READ IOCC TO START
0289 0 0620      DC /0620 2401 NO2 METER
028A 0 029D      DC READ IOCC TO START
028B 0 0600      OC /0600 2402 METER
028C 0 0000      MASK DC /0000 IOCC TO SET THE
028D 0 0480      DC /0480 MASK REGISTER
028E 0 FFFF      MASK1 OC /FFFF IOCC TO MASK
028F 0 0480      DC /0480 INTERRUPTS 0-13
0290 0 FFFF      MASK2 OC /FFFF IOCC TO MASK
0291 0 0481      OC /0481 INTERRUPTS 14-23
0292 0 0000      SNSD OC /0000 IOCC TO SENSE
0293 0 0701      OC /0701 A DEVICE
0294 0 0008      PRINT DC B 1443 PRINT TABLE
0295 0 2435      DC /2435 ME
0296 0 1335      DC /1335 TE
```

```
0297 0 29C0      OC      /2900      R
0298 0 3517      OC      /3517      EX
0299 0 3529      DC      /3529      ER
029A 0 3339      DC      /3339      CI
0298 0 1235      DC      /1235      SE
029C 0 2900      DC      /2900      R
029D 0 40C8      READ DC      /4008
029E 00C8      BSS      8
02A6 0 84AA      FORUS DC      /84AA      CONST FOR 4US SYS
02A7 0 0900      TWOUS DC      /0900      CONST FOR 2US SYS
02A8 0 0000      CONST OC      /0000      NO OF 72 SEC LOOPS
02A9 000D      CNFIG BSS      13      EDIT CD INFORMATION
02B6 0 000C      EOCT1 DC      /000C      KEEP TRACK OF ED CTS
02B7 0 028C      ITBL1 DC      /ITBLE&1      LOC OF INTR TABLE
02B8 0 8000      EIGHT DC      /8000      CONSTANT# /8000
02B9 0 FFFF      FFFF DC      /FFFF      CONSTANT# MINUS ONE
02BA 0 0000      WORD DC      /0000      A UTILITY LOCATION
02BB 0 028C      ITBLE DC      /ITBLE&1      ITBLE ADDRESS
02BC 000C      BSS      12
02C8 0 000C      EDCT DC      /000C      NO OF EDIT FIELDS
02C9 0 0001      ONE DC      /0001      CONSTANT# /0001
02CA 0 FFFE      FFFE DC      /FFFF      CONSTANT# /FFFF
02CB 0 000E      EASY DC      /000E      CONSTANT# /000E
02CC 0 0243      INTRN DC      /SVINT      SPURIOUS INTR RTN
02CD 0 0236      XFER1 DC      /IRTN      PROG GEN INTR RTN
02CE 0 0000      COUNT DC      /0000      DATA ENTRY SW SETNG
02CF 0 1000      NOOP DC      /1000
02D0 0 4C00      RSTR DC      /4C00
02D1 0.013E      DC      /RERUN
02D2 0 0701      SENSE DC      /0701
0050 0          BEGIN EQU      /50
02D3 0 00C0      PASS1 DC      0
02D4 0 0020      TWTY DC      /0020      CONSTANT FOR 2402
02D6 0050      END      BEGIN
NO STATEMENTS FLAGGED IN THE ABOVE ASSEMBLY
```

```
ADDR 01A2 017E
ADDR1 01A6 01A9
BEGIN 0050 02D6
BIT0 0172 014F
BUILD 014A 017D 01A1
BYPAS 01F7 01ED
BZERO 0195 0185
CNFIG 02A9 0136 0148 0215
CONST 02A8 021C 021F
COUNT 02CE 0213 027E
CTLWD 0198 0193
DESWS 027E 0211
DLY0 0211 0231
DLY1 021C 0235
DLY2 021E 0228
DLY3 021F 0226
DLY4 0221 0224
EASY 02C8 0154
EDCT 02C8 0142 01AA
EDCT1 02B6 0144 0178 019F 01AC 01C9 0204
EIGHT 02B8 015C 0161 0169 0172 018A 0195
FAST 0233 0219
FFFF 02CA 018C
FFFF 02B9 01B3 01C3 010E 01F7
FORUS 02A6 021A
GREAT 01CD 01B9
ILSWD 017F 0170
ILWD1 0161 0158
INCR 01C3 0186
INCR1 0208 0206
INTRN 02CC 01A4 0201
IOCC 0280 0146 01AE 01FC
IRTN 0236 023C 0241 02C0
IT8LE 02B8 013E 0180 0287 0288
ITBL1 02B7 0140 016E 0178 017F 018F 0191
LDRT1 01B7
LDRT2 01B2 01C8 020A
LES14 0169 0158
LOAD 012C 0124
MASK 028C 018A 01BE 01C0 0101 01E0 01E2 0229 0220
MASK1 028E 0208
MASK2 0290 020D
NOOEV 0175 014C
NOOP 02CF 0267
ONE 02C9 015E 0166 01CF 0221
PASS1 02D3 01C5 01EA 01F9
PRINT 0294 0284
READ 029D 0286 0288 028A
RERUN 013E 02D1
RSTR 02D0 012C 0130
SENSE 02D2 01E5
SENS1 023F 023A
SHRT1 0168 016D
SHRT2 0163 0165
SHRT3 018C 018E
SNSO 0292 01E7 01EE 01F2 023F
ST01 016E 0160 0168 0174
ST02 018F 0197
SVEXT 0267 0261
SVINT 0243 023D 023E 0269 026D 0270 02CC
SVIN0 024D 025D 0266
SVIN1 024F 0257
SVIO 027C 0244 0253 0254 026F
SV0 0272 0258
SV1 0273 024D
SV11 026F 0268
SV2 0274 0248
SV3 0275 0262
```



IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM
METER EXERCISER

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SV4 0276 024A 024F 0258 025A 0265
SV5 0277 024E 0251 0255
SV6 0278 024C 0252 0263
SV7 0279 0247 025E
SV8 027A 0237 0245
SV9 027B
TWOUS 02A7 0233
TWTY 02D4 022B
VECT 01D5 01C2 01D9
VECT1 01DA 01D6
WAIT1 01F5 3001
WAIT2 07CB 01CC 0207 3002
WAIT3 022F 3003
WAIT4 01FF 3004
WORD 02BA 0150 0152 0156 0186 018B 01F0
XFER 013B 013D
XFER1 02CD 01DA
END OF ASSEMBLY

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6.1 EDIT PROCEDURE	

1. PURPOSE

- A. CHECK THE ACCURACY OF ALL OF THE USE METERS.
- B. INSURE THAT NO METERS ADVANCE WHEN THE METER KEY IS SWITCHED TO CE MODE.
- C. CHECK THE METER CONTROL CIRCUITS.

2. PREREQUISITES

2.1 PROGRAM

THIS PROGRAM IS LOADED BY THE RELOCATABLE DIAGNOSTIC LOADER. AN EDIT CARD, CONTAINING THE NECESSARY IOCC INFORMATION FOR THIS 1800 SYSTEM, MUST FOLLOW THE LAST CARD OF THE PROGRAM. (SEE SEC 6.1)

2.2 EQUIPMENT

- A. CUSTOMER ENGINEER USE METER KEY.
- B. CARD OR PAPER TAPE READER.

3. USE PROCEDURE

THERE ARE 7 STEPS TO THE USE PROCEDURE. A DETAILED DESCRIPTION STARTS IN SECTION 3.1.

- 1. RECORD ALL METER READINGS.
- 2. MAKE ALL METERED I/O UNITS READY.
- 3. LOAD THE PROGRAM.
- 4. SET THE DESIRED NUMBER OF 72 SECOND LOOPS IN THE PC DATA ENTRY SWITCHES.
- 5. CHECK CUSTOMER METERS
- 6. COMPUTE THE ELAPSED TIME. (BY HAND)
- 7. CHECK THE METER CONTROL CIRCUITS.

3.1 LOADING

- 1. RECORD THE READINGS ON ALL OF THE CUSTOMER METERS. THE CUSTOMER

- MUST BE GIVEN CREDIT FOR THIS TEST TIME.
- 2. MAKE ALL METERED I/O UNITS READY.

- 1442 (FIRST) IF THE 1800 SYSTEM IS EQUIPPED WITH A 1442, IT MUST BE USED TO LOAD THE PROGRAM. AFTER THE PROGRAM HAS LOADED, DO NOT PRESS THE 1442 NPRO BUTTON, AS THIS WILL PREVENT THE METER FROM RUNNING
- 1442 (SECOND ONLY) PLACE A FEW CARDS IN THE FEED HOPPER AND PRESS THE 1442 START KEY. THE 1442 READY LAMP SHOULD GLOW.
- 1443 TURN ON THE POWER SWITCH AND PRESS THE 1443 START KEY. THE 1443 READY LAMP SHOULD GLOW.
- 2310 DISK STORAGE-TURN THE SWITCH ON THE FRONT OF THE UNIT TO 'ENABLE'.
- 2401 PLACE A REEL OF TAPE IN EACH TAPE DRIVE. PRESS THE 2401 LOAD REWIND BUTTON(S). PRESS THE 2401 START BUTTON(S).
- 2402 PLACE A REEL OF TAPE IN EACH TAPE DRIVE. PRESS BOTH LOAD REWIND BUTTONS AND BOTH START BUTTONS. IT IS NECESSARY TO RUN THE DELAY LOOP TWICE TO ENSURE THAT EACH DRIVE CAN CONTROL THE METER. DURING THE FIRST TIMING LOOP THE METER WILL BE UNDER CONTROL OF DRIVE ZERO. WHEN THE TIMING LOOP TERMINATES THE TAPE IN DRIVE ZERO SHOULD BE RETURNED TO LOAD POINT SO THAT ONLY DRIVE ONE CAN CONTROL THE METER DURING THE SECOND TIMING LOOP. DRIVE ZERO WILL BE STARTED BY THE PROGRAM ON ALL ODD NUMBERED RUNS AND DRIVE ONE ON ALL EVEN NUMBERED RUNS.

- 4. REFER TO THE RELOCATABLE DIAGNOSTIC LOADER DOCUMENTATION FOR THE LOADING PROCEDURE.

NOTE

IF THE PROGRAM LOADED CORRECTLY, AND ALL METERED I/O UNITS ARE IN A READY STATUS, THE PROGRAM WILL STOP AT WAIT 2. (B REG 3002)

3.2 OPERATION.

3.2.1 TO CHECK CUSTOMER METERS.

- 1. SET THE DATA ENTRY SWITCHES TO INDICATE THE NUMBER OF 72 SECOND LOOPS THAT YOU WISH TO MAKE.
- 2. IF THE 1442 WAS USED TO LOAD THE PROGRAM, RECORD THE METER READING. (AGAIN).
- 3. PRESS PC START BUTTON.
- 4. THE PROGRAM WILL STOP AT WAIT 3 (B REG 3003) WHEN THE DESIRED DELAY IS COMPLETED. ALL CUSTOMER METERS SHOULD HAVE ADVANCED .02 HOURS FOR EACH 72 SECOND LOOP RUN.
- 5. IF THE 2402 METER IS BEING CHECKED, IT IS NECESSARY TO RUN THE DELAY LOOP TWICE. DURING THE FIRST RUN (AND ALL SUBSEQUENT ODD NUMBERED RUNS), THE METER WILL BE UNDER CONTROL OF DRIVE ZERO. WHEN THE RUN IS COMPLETED (PROGRAM STOPS AT WAIT 3), PRESS THE 'RESET' AND THE 'LOAD REWIND' BUTTONS ON DRIVE ZERO, PLACE A FEW CARDS IN THE 1442 AND PRESS THE 1802 'RESET' AND 'START' BUTTONS. PROGRAM WILL STOP AT WAIT 2. PRESS THE 1802 'START' BUTTON TO START THE DELAY LOOP. DURING THE SECOND RUN (AND ALL SUBSEQUENT EVEN NUMBERED RUNS), THE METER WILL BE UNDER CONTROL OF DRIVE NUMBER 1. BEFORE EACH RUN, CHECK THAT BOTH DRIVES ARE AT LOAD POINT AND 'READY'.
- 6. TO REPEAT TEST, SLT LOOP COUNT IN THE DATA ENTRY SWITCHES AND PRESS THE PC START BUTTON.

3.2.2 CONTROL CIRCUITRY CHECK.

EACH METER WILL NOW BE CHECKED TO INSURE THAT IT RUNS AND STOPS

RUNNING AT THE CORRECT TIMES.

1. CHECK THAT NO METERS ARE RUNNING WHILE THE PROGRAM IS AT WAIT 3.
2. TURN THE METER KEY TO C.E. MODE. WHILE THE PROGRAM IS RUNNING CHECK THAT NO METERS ARE RUNNING. RETURN THE METER KEY TO THE NORMAL POSITION.
3. WHILE THE PROGRAM IS RUNNING IN A 72 SECOND DELAY LOOP, 1442-THE METER SHOULD STOP WHEN THE NPRO BUTTON IS PRESSED. (HOPPER MUST BE EMPTY)
1443-THE METER SHOULD STOP IF THE 1443 STOP BUTTON IS PRESSED.
2310-THE METER IS CONTROLLED BY THE 'ENABLE/DISABLE' SWITCH ON THE FRONT COVER. IF THE POSITION OF THE SWITCH IS CHANGED, THIS CHANGE SHOULD NOT AFFECT THE METER UNTIL AFTER THE PC HAS COME TO A WAIT. (AND THEN HAS STARTED AGAIN).
2401/D2 THE METER SHOULD RUN WHENEVER THE TAPE IS LOADED AND NOT AT LOAD POINT. IF A TAPE DRIVE IS LOADED AND NOT AT LOAD POINT, PRESSING THE 'LOAD REWIND' BUTTON SHOULD STOP THE METER WHEN THE TAPE REACHES LOAD POINT.

3.3 TERMINATION

THE PROGRAM WILL STOP AT WAIT 3 (B REG 3DD3) WHEN THE EXERCISE IS COMPLETED. TO REPEAT EXERCISE, REFER TO WAIT 3 DESCRIPTION.

3.4 RESTART

PLACE A FEW BLANK CARDS IN THE FEED HOPPER OF THE FIRST 1442 AND PRESS THE START KEY. THE 1442 READY LAMP SHOULD GLOW. PRESS THE PC RESET BUTTON THEN THE START BUTTON. THIS WILL REINITIALIZE THE PROGRAM, CONDITION ALL THE METERS TO RUN, THEN STOP AT WAIT 2 (8 REG 3D02).

3.5 PROGRAM HALTS

PROGRAM WAITS ARE USED IN THIS PROGRAM, AND ARE IDENTIFIED BY
REFERENCING THE B REG AND I REG.

A PROGRAM WAIT IS OF THE FORM,

3DXX, (B REG).

A DESCRIPTION OF THE INDIVIDUAL PROGRAM WAITS CAN BE FOUND AT THE BEGINNING OF THE PROGRAM LISTING. A TYPICAL WAIT DESCRIPTION FOLLOWS. IT IS INCLUDED TO SHOW THE FORMAT OF THE LISTING, AND IT IS NOT NECESSARILY A DESCRIPTION OF AN ACTUAL WAIT.

```

3DD1 0 01ED          DC      WAIT1+1           WAIT 1
*
*
*
*       ONE OF THE METERED I/O UNITS
*       FAILED TO SEND A RESPONSE
*       INTERRUPT TO THE PROGRAM. INDEX
*       REGISTER 1 WILL HAVE THE ADDRESS
*       OF THE IOCC. THE AREA CODE WILL
*       INDICATE THE I/O UNIT NOT READY.
*       IF A 24D1/D2 DRIVE IS NOT READY,
*       PROGRAM WILL NOT STOP AT WAIT 1.

```

B REG, (FIRST 4 DIGIT GROUP) CORRESPONDS TO B REG READING.

1 REG, (SECOND 4 DIGIT GROUP) CORRESPONDS TO 1 REG READING.

4. PRINTOUTS
THERE ARE NO PRINTOUTS.
5. COMMENTS (NONE)

-- LAST PAGE

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM
METER EXERCISER
6 APPENDIX

6.1 EDIT PROCEDURE

THE FOLLOWING EDIT PROCEDURE IS FOR CARD INPUT. THE EDIT PROCEDURE FOR PAPER TAPE INPUT IS LOCATED IN THE PAPER TAPE EDIT UTILITY PROGRAM DOCUMENTATION. THE PROPER EDIT CARDS MUST BE THE LAST CARDS IN THIS PROGRAM DECK. THE FOLLOWING FORMS ARE PROVIDED TO AID IN MANUALLY PREPARING THESE EDIT CARDS OR UPDATING EXISTING EDIT CARDS. IF IT IS NECESSARY TO PREPARE OR MODIFY EDIT CARDS, FILL IN THE NECESSARY DATA IN THE FORMS PRIOR TO PUNCHING THE CARDS. CARD COLUMNS THAT ARE SHADED SHOULD BE LEFT BLANK.

DDEF STANDS FOR DEVICE DEFINITION EDIT FIELD. IT INCLUDES:

1. THE INTERRUPT LEVEL ASSOCIATED WITH THIS DEVICE (USE HEX NOTATION, 00-17).
2. THE ILSW BIT POSITION ASSOCIATED WITH THIS DEVICE (USE HEX NOTATION, 0-F).
3. THE CHANNEL ASSIGNED TO THIS DEVICE (0-8). IF THIS IS A DPC DEVICE, PUNCH AN "F" IN THE CARD COLUMN.

[illegible]

CARD COLUMNS 77-80 WILL CONTAIN THE SPEED OF THE CORE STORAGE. FOR 2 MICRO SEC. STORAGE PUNCH 0002, AND FOR 4 MICRO SEC. STORAGE, PUNCH 0004.

NOTE: IF A DEVICE IS NOT ON THIS SYSTEM, PUNCH THE CORRESPONDING ENTRY FFFF FFFF.

NO "END EDIT" CARD IS REQUIRED FOR THIS PROGRAM.

DATE 28 FEB 66 DATE 1 MAY 66 DATE 01 JUL 66 DATE 01 SEP 67
EC 415120 EC 415120A EC 415178 EC 411857

0 0

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

DIMAL INITIAL LOADER (CARD)

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028C	*	SEE PID 0802 FOR DESCRIPTION	88700010
	*	ABS	88700020
	*	ORG /3500	88700030
	*	DIMAL INITIAL LOADER PROGRAM WAIT	88700040
	*	DESCRIPTION.	88700050
3500 0 0010	*	OC W3500+1 WAIT 500	88700060
	*		88700070
	*	A DSW ERROR WAS	88700080
	*	DETECTED DURING LOAD	88700090
	*	OPERATIONS.RELOAD THE	88700100
	*	INITIAL LOADER.	88700110
3501 0 0037	*	OC W3501+1 WAIT 501	88700120
	*		88700130
	*	AN INITIAL LOADER EDIT	88700140
	*	CARD ERROR HAS BEEN	88700150
	*	DETECTED.CHECK THE EDIT	88700160
	*	CARDS.INSURE THAT COLUMN	88700170
	*	1 OF BOTH CARDS CONTAINS	88700180
	*	AN 'E',THAT THE PIO CN	88700190
	*	BOTH CARDS IS 0200,THAT	88700200
	*	CARD 1 SEQUENCE NUMBER IS	88700210
	*	E000 AND CARD 2 IS FFFF.	88700220
	*	CORRECT ANY ERRORS,PLACE	88700230
	*	BOTH CARDS IN THE 1442.	88700240
	*	MAKE IT READY AND CONTINUE	88700250
3502 0 0056	*	OC W3502+1 WAIT 502	88700260
	*		88700270
	*	1442 IS NOT READY.READY	88700280
	*	THE 1442 WITH INITIAL	88700290
	*	LOADER EDIT CARDS AND	88700300
	*	CONTINUE.	88700310
3503 0 0058	*	OC W3503+1 WAIT 503	88700320
	*		88700330
	*	A 1442 DSW ERROR WAS	88700340
	*	DETECTED DURING EDIT CARD	88700350
	*	INPUT. REENTER BOTH EDIT	88700360
	*	CARDS IN THE 1442 HOPPER,	88700370
	*	MAKE IT READY AND CONTINUE	88700380
3504 0 00A7	*	DC W3504+1 WAIT 504	88700390
	*		88700400
	*	THE DISK PACK CE WORD	88700410
	*	HAS NOT FOUND ON THE	88700420
	*	HISTORY TRACK. INSURE THE	88700430
	*	CE DISK PACK HAS BEEN	88700440
	*	LOADED.OEPRESS START TO	88700450
	*	TRY AGAIN.IF ERROR PER-	88700460
	*	SISTS,REINITIALIZE THE	88700470
	*	CE DISK PACK.	88700480
3505 0 00AE	*	DC W3505+1 WAIT 505	88700490
	*		88700500
	*	HISTORY DATA FOUND ON THE	88700510
	*	HISTORY TRACK INDICATES	88700520
	*	THAT 4 OR MORE BAD CYLIN-	88700530
	*	DERS EXIST ON THE CE DISK	88700540
	*	PACK.THIS IS CONSIDERED A	88700550
	*	BAD PACK.A NEW CE PACK	88700560
	*	SHOULD BE USED.IF IT IS	88700570
	*	DESIRED TO USE THE BAD	88700580
	*	PACK,SET SNS/PGM SWITCH 0	88700590
	*	AND CONTINUE.	88700600
3506 0 0147	*	OC W3506+1 WAIT 506	88700610
	*		88700620
	*		88700630
	*		88700640
	*		88700650
	*		88700660
	*		88700670
	*		88700680

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	*	THE 1442 IS NOT READY	88700690
	*	READY THE 1442 WITH DIMAL	88700700
	*	OBJECT DECK,CR PRESS 1442	88700710
	*	START FOR LAST CARD AND	88700720
	*	CONTINUE.	88700730
3507 0 0149	*	DC W3507+1 WAIT 507	88700740
	*		88700750
	*	A 1442 DSW ERROR WAS	88700760
	*	DETECTED WHILE LOADING	88700770
	*	DIMAL. RELOAD THE DIMAL	88700780
	*	OBJECT DECK IN THE 1442	88700790
	*	HOPPER AND MAKE IT READY.	88700800
	*	DEPRESS THE 1800 RESET	88700810
	*	AND START BUTTONS.PROGRAM	88700820
	*	LOADING SHOULD OCCUR.	88700830
3508 0 016E	*	OC W3508+1 WAIT 508	88700840
	*		88700850
	*	A CHECKSUM ERROR WAS	88700860
	*	DETECTED DURING DIMAL	88700870
	*	LOADING.NPRO THE 1442.THE	88700880
	*	1ST CARD EJECTED IS THE	88700890
	*	CARD IN ERROR.INSURE CARD	88700900
	*	IS IN CORRECT SEQUENCE.IF	88700910
	*	NO PROBLEM IS APPARENT,	88700920
	*	REENTER BOTH EJECTED CARDS	88700930
	*	AND CONTINUE.RELOADING	88700940
	*	MAY ALSO BE DONE BY PLAC-	88700950
	*	ING THE ENTIRE DIMAL DECK	88700960
	*	IN THE 1442 HOPPER AND	88700970
	*	MAKE IT READY.DEPRESS 1800	88700980
	*	RESET AND START BUTTONS.	88700990
3509 0 01C8	*	OC W3509+1 WAIT 509	88701000
	*		88701010
	*	A LAST CARD SEQUENCE WAS	88701020
	*	INITIATED BEFORE THE	88701030
	*	COMPLETE DIMAL DECK WAS	88701040
	*	READ IN.INSURE THAT THE	88701050
	*	COMPLETE DIMAL DECK HAS	88701060
	*	BEEN LOADED.	88701070
350A 0 020E	*	DC W350A+1 WAIT 50A	88701080
	*		88701090
	*	DISK HOME BIT DID NOT	88701100
	*	COME ON IN THE OSW AFTER	88701110
	*	THE 3RD ATTEMPT TO SEEK	88701120
	*	HOME.CORRECT FAILURE AND	88701130
	*	CONTINUE.IF CORE IS DES-	88701140
	*	TROYED,RELOADED MUST BE	88701150
	*	ACCOMPLISHED.	88701160
350B 0 021C	*	DC W350B+1 WAIT 50B	88701170
	*		88701180
	*	2310 DISK DRIVE NOT READY.	88701190
	*	READY THE 2310 AND	88701200
	*	CONTINUE.	88701210
350C 0 022D	*	DC W350C+1 WAIT 50C	88701220
	*		88701230
	*	ATTEMPTED DISK READ.DRIVE	88701240
	*	WENT NOT READY.MAKE DRIVE	88701250
	*	READY AND CONTINUE. IF	88701260
	*	DISK ARM POSITION IS	88701270
	*	CHANGED,RELOADING DIMAL	88701280
	*	DECK IS REQUIRED.	88701290
	*		88701300
	*		88701310
	*		88701320
	*		88701330
	*		88701340
	*		88701350
	*		88701360

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DIMAL INITIAL LOADER (CARD)

```

3500 0 023A          DC      W350D+1      WAIT 50D      88701370
*                                     A DSW ERROR OCCURED ON A 88701380
*                                     DISK READ ON DN EACH OF 88701390
*                                     3 ATTEMPTS. THE A PEG. 88701400
*                                     CONTAINS THE ERROR BITS. 88701410
*                                     PRESSING START AFTER THE 88701420
*                                     ERROR WILL CAUSE PROGRAM 88701430
*                                     TO MAKE 3 MORE TRIES TO 88701440
*                                     READ. IF ERROR PERSISTS, 88701450
*                                     CORRECT AND RELOAD DIMAL. 88701460
*                                     88701470
*                                     88701480
*                                     88701490
350E 0 0245          DC      W350E+1      WAIT 50E      88701500
*                                     88701510
*                                     ATTEMPTED DISK WRITE. DRIVE 88701520
*                                     NOT READY. MAKE DRIVE READY 88701530
*                                     AND CONTINUE. IF DISK ARM 88701540
*                                     POSITION IS CHANGED, 88701550
*                                     RELOADING DIMAL DECK IS 88701560
*                                     REQUIRED. 88701570
*                                     88701580
350F 0 0253          DC      W350F+1      WAIT 50F      88701590
*                                     88701600
*                                     A DISK WRITE OR MODULO 4 88701610
*                                     CHECK ERROR EXISTED ON 88701620
*                                     EACH OF 3 ATTEMPTS TO WRITE 88701630
*                                     DEPRESS START BUTTON WILL 88701640
*                                     CAUSE PROGRAM TO MAKE 3 88701650
*                                     ADDITIONAL ATTEMPTS TO 88701660
*                                     WRITE. IF ERROR PERSISTS, 88701670
*                                     CORRECT AND RELOAD DIMAL. 88701680
*                                     88701690
3510 0 0299          DC      W3510+1      WAIT 510      88701700
*                                     88701710
*                                     THIS WAIT INDICATES THAT 88701720
*                                     THE LOADING OF THE CARO 88701730
*                                     DECK REPRESENTING THE 88701740
*                                     PAPER TAPE VERSION OF 88701750
*                                     DIMAL HAS BEEN COMPLETED 88701760
*                                     88701770
3511          ORG      0          CARD 1      88701780
*                                     88701790
03E8          IN      EQU      1000      88701800
044C          IOA     EQU      1100      88701810
0440          SIO     EQU      IOA+1     88701820
044E          OUT     EQU      SIO+1     88701830
*                                     88701840
*                                     DIMAL SYSTEM INITIAL LOADER 88701850
*                                     88701860
*                                     THE INITIAL LOADER PERFORMS THE 88701870
*                                     FOLLOWING FUNCTIONS. 88701880
*                                     88701890
*                                     1. INPUT AND CONVERT THE LOADER EDIT 88701900
*                                     CAROS. 88701910
*                                     2. INSURES THAT THE CE DISK PACK IS 88701920
*                                     LOADED, AND USABLE. 88701930
*                                     3. ASSIGNS THE CYLINDERS TO BE USE BY 88701940
*                                     THE DISK DIAGNOSTIC MONITOR. 88701950
*                                     4. INPUTS THE DDM HEADER, COLO START 88701960
*                                     LOADER, DDM LOADER/ORGANIZER AND THE 88701970
*                                     DDM SELECT/EXECUTE PROGRAMS AND 88701980
*                                     WRITES THEM ON THE DISK. 88701990
*                                     5. INPUTS THE DDM LOADER/ORGANIZER FROM 88702000
*                                     THE DISK UPON COMPLETION OF INITIAL- 88702010
*                                     IZING THE DISK WITH THE DIMAL SYSTEM 88702020
*                                     88702030
*                                     THIS IS THE 1ST CARO OF THE LOADER. IT 88702040

```

DIMAL INITIAL LOADER (CARO)

		IS READ IN BY THE IPI OPERATION AND IS USED TO LOAD THE REST OF THE LOADER.					
0000	0	0819	IPL1	X10	RDPAC	READ PACKED MODE	88702050
0001	0	081A	IPL2	X10	DSW	SENSE 1442 STATUS	88702060
0002	00	4C040001	BSC	L	IPL2,F	BRANCH IF NOT READY	88702070
0004	0	8017	CMP	DSW		LOOK FOR OP COMPLETE	88702080
0005	0	7009	MDX	W3500		BRANCH - BIT 2 ON	88702090
0006	0	7006	MDX	IPL3		BRANCH - BITS 5 OR 6	88702100
0007	00	7424001A	MDX	L	RDPAC,36	SET IOCC FOR NXT CD	88702110
0009	00	74FF001E	MDX	L	CDCT,-1	SKIP WHEN LOADER IN	88702120
0008	0	70F4	MDX	IPL1		GO READ NEXT CARD	88702130
000C	0	7004	MDX	PREP		PREPARE INPUT AREA	88702140
000D	0	8011	IPL3	CMP	K0100	CHECK IF BITS 5 OR 6	88702150
000E	0	1000		NOP			88702160
000F	0	3500	W3500	DC	/3500	DSW INDICATES ERROR	88702170
0010	0	70F0		MDX	IPL2		88702180
0011	00	67000141	PREP	LOX	L3 321	SET CONSTANT 321 IN	88702190
0013	00	6F00044C		STX	L3 IOA	*INPUT AREA	88702200
0015	0	C802		LDO	BRN	PICKUP RESTART INSTR	88702210
0016	0	08E9		STO	0	SET IN LOCS 0 AND 1	88702220
0017	0	700C		MDX	EDIT	BRANCH TO INPUT EDIT	88702230
0018		0000		BSS	E 0		88702240
0018	00	4C00027E	BRN	BSC	L RSTR	RESTART INSTRUCTION	88702250
001A	C	0024	RDPAC	OC	36	READ 1442 PACKED	88702260
0018	0	1601		OC	/1601	*IOCC WORD	88702270
001C	0	0800	OSW	DC	/0800	SENSE 1442 OSW IOCC	88702280
001D	0	1700		OC	/1700		88702290
001E	0	0013	CDCT	OC	19	LOADER CARD COUNT	88702300
001F	0	0100	K0100	OC	/0100	ERR CK USW CONSTANT	88702310
0020	0	FFFF	KFFFF	OC	/FFFF	CONSTANT	88702320
0021			*****				88702330
			ORG	36	CARD 2		88702340
			*****				88702350
			*****				88702360
			*****				88702370
			*****				88702380
			*****				88702390
			*****				88702400
			*****				88702410
			*****				88702420
			*****				88702430
			*****				88702440
			*****				88702450
			*****				88702460
			*****				88702470
			*****				88702480
			*****				88702490
			*****				88702500
			*****				88702510
			*****				88702520
			*****				88702530
			*****				88702540
			*****				88702550
			*****				88702560
			*****				88702570
			*****				88702580
			*****				88702590
			*****				88702600
			*****				88702610
			*****				88702620
			*****				88702630
			*****				88702640
			*****				88702650

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DIMAL INITIAL LOADER (CARO)

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```
0048 00 C7000450
004A 00 D70001FF
004C 0 73FF
004E 0 700A
004F 00 C400044F
0051 0 F0CE
0052 0 4B20
0053 0 70E2
0054 0 7034
0055 0 3502
0056 0 70C0
0057 0 3503
0058 0 70C8
0059 0 E000

005A 0000
005A 0 03E8
0058 0 1600
005C 0 D200
0050 0 1703
005E 0 0000
```

ORG 72 CARO 3

```
*****
LD L3 OUT+2 PLACE CONVERTED EDIT
STD L3 EOWD-1 *WOS IN SAVE LOCATNS
MOX 3 -1 SKIP WHEN OONE
MOX *-6 CONTINUE SAVE OP
MDX E0IT1-1 GO READ NEXT CARD
E0IT3 L OUT+1 PICK UP 2ND EDIT ETY
EOR KFFFF CHECK FOR TERMINATOR
DSC 2 SKIP IF TERM CARO
MOX W3501 BRANCH NOT TERM CARD
MOX INT
W3502 OC /3502 1442 NOT READY
MDX E0IT TRY AGAIN
W3503 OC /3503 OSM INDICATES ERROR
MOX E0IT TRY FOR REREAD
KE000 OC /E000 CONSTANT
```

BSS E 0

```
ROEO OC IN 1442 READ IOCC
OC /1600
DSW1 OC /0200 1442 SENSE/RESET
OC /1703 *OSW IOCC
E0SW OC 0 E0IT SWITCH
```

THIS ROUTINE CONVERTS 1 HEXIDECIMAL CARO TO BINARY.

HBCV LDX 1 -31 SET XR TO CONV.30 WD

```
SLA 16 CLEAR CONVERTED WORD
STO LOC *STORE POINTER
HBCVI LOX 2 4 SET COLUMN XR = 4
MOX 1 1 SKIP WHEN OONE
MDX *-1 CONVERT A WORD
MOX E0IT2+1 CONTINUE MAINLINE
SLA 16 CLEAR CONVERSION
STO SAVE *WORK LOCATIONS.
STO SAVE1
HBCV2 SLA 4 POSITION FOR NXT CHR
STO SAVE1 SAVE CONVERTED CHARS
LCX 3 0 SET CHARAC XR = 0
```

ORG 108 CARO 4

```
*****
LO L1 IN+31 PICK UP HEX COLUMN
BSC +2 SKIP IF NOT ALPHA
MOX 3 9 ADD 9 FOR ALPHA CHAR
SLA 3 REMOVE ZONE BITS
BSC L HBCV4,+ XFER IF CHAR = 0
HBCV3 MOX 3 1 ADD 1 TO CHARACT XR
BSC L HBCV4,+2 XFER IF DIGIT FGUNO
SLA 1 POSITION NEXT BIT
MOX HBCV3 CHECK NEXT BIT
HBCV4 STX 3 SAVE STORE BIN CHARACTER
LD SAVE FETCH BIN CHARACTER
HBCV5 OR SAVE1 ADD TO PREVIOUS CHAR
MOX 1 1 ADD 1 TO HEX WORD XR
MOX 2 -1 SUB 1 FROM COLUMN XR
MOX HBCV2 GO FOR NEXT COLUMN
LOX 13 LOC PICK UP STORE POINTN
```

STD L3 OUT SET IN OUTPUT AREA

```
MOX L LOC,1 ADD 1 TO POINTER
MOX HBCVI GO FOR NEXT WORD
LDC DC D STORAGE POINTER
SAVE OC D *CONVERSION WORK
SAVE1 DC D *LOCATIONS
```

88702730
88702740
88702750
88702760
88702770
88702780
88702790
88702800
88702810
88702820
88702830
88702840
88702850
88702860
88702870
88702880
88702890
88702900
88702910
88702920
88702930
88702940
88702950
88702960
88702970
88702980
88702990
88703000
88703010
88703020
88703030
88703040
88703050
88703060
88703070
88703080
88703090
88703100
88703110
88703120
88703130
88703140
88703150
88703160
88703170
88703180
88703190
88703200
88703210
88703220
88703230
88703240
88703250
88703260
88703270
88703280
88703290
88703300
88703310
88703320
88703330
88703340
88703350
88703360
88703370
88703380
88703390
88703400

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```
0089 DD C40002D2
008B D 0031
008C 00 C4000200
008E 0 0020
008F 0 1BD3
```

OC90

```
0090 00 D400026A
0092 J 6308
0093 00 C4000201
0095 00 EF000264
0097 00 D7000264
0099 D 73FE
009A D 70F8
009B 00 44000203
009C 00 44000216
009F 00 44000225
COA1 00 C400044E
00A3 0 F00E
00A4 00 4C1800A8
00A6 0 3504
00A7 0 70F3
00AB 00 C400044F
00AA 0 F008
00AB 00 4C1000BE
00A0 0 3505
00AE D 0BD5
DOAF 00 4C2B00BE
00B1 D 70F6
00B2 0 CE0C
00B3 D 0018
```

00B4

```
00B4 0000
00B4 0 0030
00B5 D D760
```

```
00B6 D 0000
00B7 0 D000
00B8 D 000G
00B9 0 0000
00BA 0 000D
00BB 0 0000
00BC D 00DD
00BD 0 0000
```

```
OCBE D 62FA
00BF D C0F4
00C0 DD 6780044F
00C2 D 730D
00C3 0 7GD7
00C4 DD 060000BC
00C6 D 7201
00C7 D 70D1
00C8 0 70D8
00C9 0 8009
DDCA 0 7DF5
00CB DD 87D0D44F
DOCD D 7002
00CE 0 7DD1
```

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THIS ROUTINE BUILDS THE DISK COMMANDS, DETERMINES IF THE CE PACK IS BEING USED AND IF THE CE PACK IS GOOD.

```
INT LD L EDW0+2 GET OUTPUT DEVICE IN 0
STD L3 USTB+7 SET IN USE TABLE
LO L EDW0 GET HISTORY CYLINDER
STO USTB+6 SAVE IN USE TABLE
SRA 3 REMOVE SECTOR BITS
```

DRG 144 CARD 5

```
*****
STO L OSK SET IN SEEK COMMAND
LOX 3 11 SET BUILD INDEX
INT1 LO L EDW0+1 PICKUP DISK AREA CD
OR L3 HOME ADD AREA CODE TO
STO L3 HOME *DISK IDCC
MOX 3 -2 SKIP WHEN OONE
MOX INT1 CONTINUE
INT2 BSI L HM GO SEEK HM
INT3 BSI L SEEK SEEK TO HISTORY TRK
BSI L READ READ HISTORY TRACK
LD L IOA+2 PICK UP CE WORD LOC
EOR CK CHECK FOR CE PACK
BSC L INT4,+ BRANCH IF CE WORD
DC /3504 CE WORD NOT READ
MOX INT2 TRY AGAIN
INT4 LD L IOA+3 GET BAO CYL COUNT
EDR CK1 CHECK FOR MORE THAN
BSC L INT5,- *3 BAO CYLINDERS
W3505 DC /3505 4 OR MORE BAD CYLS
XID SNSW SENSE SWITCH INPUT
BSC L INT5,+2 IF SSO USE BAO PACK
MOX INT4
CK OC /CEDC
CK1 DC 24
```

ORG 180 CARD 6

```
*****
BSS E 0
SNSW DC /0030 SENSE S/P SWITCH
OC /0760 *ICCC
```

THIS SECTION ASSIGNS THE OOM CYLINDERS

```
USTB OC 0 HEADER/LOR CYLINDER
DC 0 OOM LOR/ORG CYLINDER
DC 0 OOM SEL/EXC CYLINDER
DC 0 WORK CYLINDER
OC 0 WORK CYLINDER
OC 0 LOC.0IR - EDIT TABLE
DC 0 HISTORY TRACK
OC 0 OUTPUT DEVICE
```

```
INT5 LDX 2 -6 SET TABLE XT
LD SNSW PICKUP CYL 6 ADDRESS
INT6 LDX 13 SIO+2 SET ERROR TABLE XR
MOX 3 0 SKIP IF ERROR XR = 0
MDX INT9 GO CHECK FOR GOOD CY
STD L2 USTB+6 SET CYL NMBR IN TBL
MOX 2 1 ADD 1 TO TABLE XR
MDX *-1 CONTINUE
MDX GEN GO TO NEXT SECTION
INT8 A KB ADD 8 TO CYLINDER XR
MOX INT6 CHECK NEXT CYLINDER
INT9 CMP L3 SIO+2 COMPARE WITH ERR CYL
MDX INT10 DK
MDX INT10 OK
```

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88703990
88704000
88704010
88704020
88704030
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88704060
88704070
88704080

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OIMAL INITIAL LOADER (CARD)

```
00CF 0 70F9      MDX  INT8  CONTINUE CHECK
0000 0 73FF      INT10 MOX  3 -1  DECREMENT ERR XR
0001 0 70F9      MOX  INT9  CHECK NEXT ERR ENTRY
0002 0 70F1      MOX  INT7  CHECK NEXT CYLINDER
0003 0 0008      OC   8      CONSTANT 8

*
* THE FOLLOWING SECTIONS WILL INPUT THE
* OOM AND WRITE IT ON THE DISK.
*
0004 00 44000203 GEN  BSI  L  HM  GO SEEK HOME
0006 0 000F      LO  UST8  PICKUP 1ST CYLINDER
0007 0 1803      SRA  3      REMOVE SECTOR BITS

*****
***** ORG 216 CARO 7 *****
*****
0008 00 0400026A STO  L  DSK  SET IN SEEK COMMAND
000A 00 44000216 BSI  L  SEEK GO SEEK TO DES. CYL.
000C 0 6300      GEN1 LOX  3 0  INITIALIZE CARO
000D 0 6B4A      STX  3 CDC *COUNT INOICATOR
000E 0 6100      LDX  1 0  SET OUTPUT AREA XK
000F 0 7056      GEN3 MOX  RDCO GO READ A CARO
00E0 0 C049      LO  LCO  PICK UP LAST CARO SW
00E1 00 4C2001BF BSC  L  LAST,Z BRANCH IF CN
00E3 0 7068      MOX  PACK GO PACK BINARY DATA
00E4 0 C043      LO  CDC  PICK UP CARO COUNT
00E5 00 4C2000EA BSC  L  GEN4,Z BRANCH IF NOT 1ST CO
00E7 00 74010128 MOX  L  CDC,1 ADD 1 TO CO COUNT
00E9 0 70F5      MOX  GEN3 IGNORE HEADER CO
00EA 00 7400C129 GEN4 MDX  L  ADRS,0 SKIP IF NO DATA IN
00EC 0 7035      MOX  ENOCK BRANCH TO END CO CK
00ED 00 4C000270 BSC  L  CKAO BRANCH TO CHECK ADRS
00EF 0 0039      GEN9 STO  ADRS SAVE CARO ADRS
00F0 0 C03A      LO  SCIO PICK UP SECTION 10
00F1 00 4C1800FC BSC  L  GEN6,+- BRANCH IF SECTION 1
00F3 0 6308      LOX  3 11 TRANSFER USE TABLE
00F4 0 62F8      LOX  2 -8 *TO PROGRAM SECTION
00F5 00 C600008E GEN5 LO  L2 UST8+8 *
00F7 00 070003E8 STO  L3 IN *
00F9 0 7301      MOX  3 1 *
00FA 0 7201      MOX  2 1 *SKIP WHEN 8 WOS XF
00FB 0 70F9      MOX  GEN5 *

*****
***** ORG 252 CARO 8 *****
*****
00FC 0 1010      GEN6 SLA  16  CLEAR ZEROS SWITCH
00FD 0 002E      STO  ZERO *
00FE 00 C40033E8 LO  L  IN  PICK UP CARO ADDRESS
0100 0 F028      EOR  L  ADRS CHECK IF EXPECTED
0101 00 4C20011F BSC  L  FILL,Z BRANCH IF NOT EXECT
0103 00 C40003EA LO  L  IN+2 PICK UP CARO WO CNT
0105 0 E02A      AND  RSN  SAVE WORD COUNT BITS
0106 00 040003EA STO  L  IN+2 RESTORE TO DPG LCC
0108 00 678003EA LOX  13 IN+2 CARO WORD COUNT XR
010A 0 6209      LOX  2 9  SET INPUT DATA XR
010B 00 C60003E8 LD  L2 IN  MOVE DATA FROM INPUT
010D 00 0500044E STO  L1 OUT TO OUTPUT AREA
010F 00 74010129 GEN8 MDX  L  ADRS,1 ADD 1 TO ADRS INOC
0111 0 7101      MOX  1 1 ADD 1 TO OUTPUT XR
0112 0 7201      MOX  2 1 ADD 1 TO INPUT OT XR
0113 00 7401012F MOX  L  HOCT,1 ADD 1 TO WORD CCUNTR
0115 0 C019      LO  WOCT PICK UP WORD COUNT
0116 0 F016      EOR  K320 CHECK FOR 320 WORDS
0117 00 441801A5 BSI  L  WRITE,+- WRITE DISK IF WC 320
0119 00 7400012C MDX  L  ZERO,0 SKIP IF ZERO SW OFF
011A 0 70E0      MOX  GEN6 GO CHECK NEXT ADRS
011C 0 73FF      MOX  3 -1  SKIP IF ALL WORDS
011D 0 70E0      MOX  GEN7 GO MOVE NEXT WORD
011E 0 70C8      MOX  GEN4-3 GO READ NEXT CARD
```

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OIMAL INITIAL LOADER (CARD)

```
011F 0 680C      FILL STX  ZERO  SET ZEROS SWITCH
0120 0 0000      ORG  288  CARO 9

*****
***** SLA  16  CLEAR A REG
0120 0 1010      MDX  GEN7+2 FILL OA WITH ZERO
0121 0 70E8      ENOCK LO  L  IN+2 PICK UP WORD CNT LOC
0122 00 C40003EA EOR  KOF00 CHECK FOR ENO CARO
0124 0 F009      BSC  L  ENO,+- BRANCH IF ENO CARO
0125 00 4C180179 MOX  GEN6 CONTINUE
0127 0 7004      OC   0  CARD COUNTER
0128 0 0000      ADRS OC  0  ADDRESS INOICATOR
0129 0 0000      IC0  OC  0  LAST CARO INOICATOR
012A 0 0000      SCIO OC  0  SECTION 10
012B 0 0000      ZERO OC  0  ZERO FILL INOICATOR
012C 0 0000      K320 OC  320 CONSTANT
012D 0 0140      KOF00 OC  70F00 CONSTANT HEX OF00
012E 0 0F00      WOCT OC  0  OUTPUT AREA WO CNTR
012F 0 0000      RSN  OC  0  SENSE 1442 IOCC
0130 0 003F      DC  1700 REAO 1442 IOCC
0131 0 1700      RO  OC  1N  RESET/SENSE IOCC
0132 0 03E8      RESN OC  1
0133 0 1600      OC  1703
0134 0 0001
0135 0 1703

*
* THIS ROUTINE REAOs THE OOM OBJECT CAROS
*
0136 0 08F9      R0CD XIO  RSN  SENSE STATUS
0137 00 4C040146 BSC  L  W3506,E BRANCH IF NOT REAOY
0139 0 08F8      XIO  RO  REAO A CARD
013A 0 08F5      R0CD1 XIO  RSN  SENSE STATUS
013B 0 1801      SRA  1  POSITION
013C 00 4C04013A BSC  L  R0C01,E SPIN WHILE BUSY
013E 0 1808      SRA  11  POSITION
013F 00 4C04014A BSC  L  LST,E BRANCH IF LAST CARO
0141 0 1801      R0CD2 SRA  1  POSITION
0142 00 4C040148 BSC  L  W3507,E BRANCH IF ERROR

*****
***** ORG 324 CARO 10 *****
*****
0144 0 08EF      XIO  RESN  RESET OSW
0145 0 709A      MOX  GEN3+1 EXIT
0146 0 3506      W3506 OC  73506 1442 NOT REAOY
0147 0 70EE      MOX  RDCO TRY AGAIN
0148 0 3507      W3507 OC  73507 OSW INOICATES ERROR
0149 0 70EC      MOX  RCO  TRY AGAIN
014A 0 680F      LST  STX  LCO  SET LAST CARO SWITCH
014B 0 70F5      MOX  R0C02 CONTINUE

*
* THIS ROUTINE PACKS BINARY 12-4 DATA
*
014C 0 6924      PACK STX  1 PACK4+1 SAVE INOEX 1
014D 0 6188      LOX  1 -72 SET UP WORD INOEX
014E 0 6300      LOX  3 0  SET UP STORE INOEX
014F 0 62F0      PACK1 LOX  2 -3 SET UP SHIFT INOEX
0150 00 C6000177 PACK2 LO  L2 SHIFT+3 PICK UP SHIFT INSTRN
0152 0 D0D6      STO  PACK3 SET IN ROUTINE
0153 00 C5000431 LD  L1 IN+73 PICK UP 2ND HALF WO
0155 0 1800      RTE  16  SET IN G REG.
0156 00 C5000430 LD  L1 IN+72 PICK UP 1ST HALF WD
0158 0 1804      SRA  4  POSITION
0159 0 1000      PACK3 SLA  0  PACK A AND Q
015A 00 D70003E8 STO  L3 IN  STORE CONVERTED WO
015C 0 7301      MOX  3 1  MODIFY STORE INOEX
015D 0 7101      MOX  1 1  MODIFY WORD INOEX
015E 0 7201      MOX  2 1  MODIFY SHIFT INOEX
015F 0 70F0      MOX  PACK2 GO CONVERT NXT WCRO
```

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DIMAL INITIAL LOADER (CARO)

PART NO. 2242251
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IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

DIMAL INITIAL LOADER (CARO)

PART NO. 2242251
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```
0160 0 7101 MDX 1 1 MODIFY FOR NXT GROUP
0161 0 70E0 MOX PACK1 GO CONVERT NXT GROUP

*
* THIS ROUTINE PERFORMS THE CHECKSUM
*
LOX 2 -54 SET DATA INDEX
LD CDC GET CARD COUNT
SUM A L2 IN+54 SUM DATA WORD
BSC C SKIP ON CARRY
A RESN ADD 1
*****
ORG 360 CARO 11
*****
MDX 2 1 SKIP WHEN DONE
MOX SUM CONTINUE
A RESN ADD 1
BSC L PACK4,+- BRANCH IF CHECKSUM DK
W3508 /3508 CHECKSUM ERROR
BSC L GEN3 GO REREAD CARD
PACK4 LDX L1 0 RESTORE INDEX 1
BSC L GEN3+5 RETURN TO MAIN LINE
SHIFT SLT 4 SHIFT 4
SLT 8 SHIFT 8
SLT 12 SHIFT 12

*
* THIS SECTION SERVICES THE END CARD
*
KFFFO DC /FFFO CONSTANT
K3 DC 3 CONSTANT 3
END MDX 1 0 SKIP IF NO DATA TO WRITE
BSI WRITE GO WRITE DISK
SLA 16 CLEAR ACC
STD ADRS CLEAR LOC ADRS
MDX L SCID,0 SKIP IF SECTION 1
MOY EN01
MDX L TEST,-1 SKIP IF ALL H-TESTS
MDX EN02
LD SCID PICKUP SECTION ID
EOR K3 CHECK FOR SECT 3
BSC L LAST,+- BRANCH IF 4TH SECT
MDX L SCID,1 ADD 1 TO SECT IND
LD SCID PICKUP SECT IND
SRA 1 CHECK FOR SECTION 1
BSC 2 SKIP IF SECTION 1
*****
ORG 396 CARD 12
*****
MDX EN03 BRANCH IF NOT SEC 1
END2 BSC L GEN1 GO INPUT NXT SECTION
END3 MDX L REF,1 INCR TABLE REF
LDX 13 REF SET XR = REF.
LD L3 USTB PICK UP NEXT CYL
S L3 USTB-1 SUB PREVIOUS CYL
SRA 3 REMCVE SECTOR BITS
STD L DSK SET SEEK COUNT
BSI SEFK SEEK TO NEXT CYLINDER
LD L DWRT+1 ZERO WRITE IOCC
ANO KFFFO *SECTOR COUNT
STD L DWRT+1
MDX EN02 EXIT

*
REF OC 0 USE TABLE REFERENCE
K7 OC 7 CONSTANT
K321 DC 321 WRT/RO CONSTANT
TEST DC 7 NUMBER OF HEADER TESTS

*
* THIS ROUTINE SETUPS TO WRITE A DISK
RECORD.
```

88705450
88705460
88705470
88705480
88705490
88705500
88705510
88705520
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88705580
88705590
88705600
88705610
88705620
88705630
88705640
88705650
88705660
88705670
88705680
88705690
88705700
88705710
88705720
88705730
88705740
88705750
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88705770
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88705800
88705810
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88706000
88706010
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88706060
88706070
88706080
88706090
88706100
88706110
88706120

```
01A5 0 0000 * WRITE DL 0 ENTRY PCINT
01A6 0 6101 LOX 1 1 SET UP FOR 1 WORD
01A7 00 6D30044C STX L1 1DA * DISK READ
01A9 00 C4000269 LO L DWRT+1 SETUP THE DISK READ
01AB 0 1883 SRT 3 *IOCC TO PEAO THE
01AC 00 C40D0267 LD L DRD+1 *SECTDR ID OF THE
01AE 0 1803 SRA 3 *SECTDR TO BE WRITN
01AF 0 1083 SLT 3
*****
01B0 ORG 432 CARD 13
*****
01B0 00 04000267 STO L DRD+1
01B2 0 4072 BSI READ GO READ DISK SID
01B3 0 COEF LD K321 SET UP WORD COUNT
01B4 00 0400044C STO L 1DA *FOR WRITE TABLE
01B6 00 4400023F BSI L WRIT GO WRITE THE DISK
01B8 00 74010269 MDX L DWRT+1,1 ADD 1 TO SECTOR CNT
01BA 0 6100 LDX 1 0 SET OUTPUT AREA XR
01BB 00 6D00012F STX L1 DWCT CLEAR OUTPUT WC
01BD 00 4C8001A5 BSC 1 WRITE EXIT SUBROUTINE

*
* THE FOLLOWING ROUTINES ARE PERFORMED ON
* COMPLETION OF WRITING DIMAL ON DISK.
*
LAST SLA 16 CLEAR ACC
STD L LCD CLEAR LAST CARO SW
LD L SCID PICKUP SECT IND
EOR K3 CHECK FOR 4TH SECT
BSC L CKPT,+- BRANCH IF 4TH SECTDR
W3509 OC /3509 DIMAL NCT ALL LDAOED
BSC L GEN1 CONTINUE INPUT
LAST1 BSI HM SEEK TO HOME

*
* THIS ROUTINE WRITES THE MAINTENANCE
* PACK ID (ABCO) ON THE HISTORY TRACK,
*
LD L USTB+6 PICKUP SECTDR ADRES
SRA 3 REMOVE SECTDR BITS
STO L DSK SET IN SEEK IOCC
BSI SEEK GO SEEK TO HIST TRAK
LD L DRD+1 SET READ IOCC FOR
AND KFFFO *SECTDR 0
*****
01D4 ORG 468 CARD 14
*****
01D4 00 04000267 STO L DRD+1
01D6 0 6303 LDX 3 3 SET WORD COUNT TO 3
01D7 00 6F00044C STX L3 1DA
01D9 00 C4000269 LD L DWRT+1 SET DISK WRITE IOCC
01DB 0 E098 ANO KFFFO *FOR SECTDR 0
01DC 00 04000269 STO L DWRT+1
01DE 0 4046 BSI READ READ HIST TRK SEC 0
01DF 00 6700ABCD LOX L3 /ABCO SET MAINT. PACK ID
01E1 00 6F00044E STX L3 OUT *IN OUTPUT AREA
01E3 00 C400008B LD L USTB+5 GET LAST USED CYL
01E5 00 0400044F STO L OUT+1 SET IN OUTPUT AREA
01E7 00 4400023F BSI L WRIT WRITE IO ON HIST TRK

*
* THIS ROUTINE WILL INPUT THE LOADER,
* WHICH IN TURN INPUTS THE DOM LOADER
* ORGANIZER PROGRAM
*
LD BSI HM SEEK TO HOME
LD L USTB PICKUP LOADER CYL AO
SRA 3 REMCVE SECTOR BITS
STO OSK SET IN SEEK COMMAND
BSI SEEK SEEK TO LOADER CYL
```

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88706140
88706150
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```
01EF 00 67000DAA      LDX L3 /0DAA      SET XR = INPUT ADDR
01F1 0 6874          STX 3 OR0      SET ADRS IN READ CMO
01F2 0 C080          LD K321      PICKUP RD WORD COUNT
01F3 0 D300          STO 3 0      SET IN LOC HEX 0DAA
01F4 0 C072          LD DR0+1      PICKUP READ COMMAND
01F5 0 EBAC          OR K7        SET SECTOR BITS = 7
01F6 0 D070          STJ DRD+1     RESTORE READ COMMAND
01F7 0 4020          BSI READ      GO INPUT LOADER
*****
01F8                ORG 504        CARD 15
*****
01F8 0 1010          SLA 16        SETUP INIT LDR CALL
01F9 00 D400000C      STO L /C      SET IN LOC HEX C
01FB 0 C005          LO EDWD+1      PICKUP DRIVE A.C.
01FC 00 D400000D      STO L /D      SET IN LOC HEX D
01FE 00 4C000DAD      BSC L /0DAD   BRANCH TO LOADER
0200 0 0000          EDWD OC 0      CE HISTORY TRACK
0201 0 0000          DC 0          DRIVE AREA CODE
0202 0 0000          DC 0          OUTPUT DEVICE
*
* THIS ROUTINE SEEKS THE 2310 TO ITS
* HOME POSITION
*
0203 0 0000          HM DC 0        ENTRY POINT
0204 0 6304          LDX 3 4        SET TRY INDEX
0205 0 0868          XIO DSNSR      SENSE/RESET STATUS
0206 0 D00E          STO SKST       SAVE STATUS
0207 0 1004          SLA 4          POSITION HOME BIT
0208 00 4CA80203      BSC I HM,+Z   EXIT IF DISK HOME
020A 0 75FF          MOX 3 -1      SKIP IF 3RD TRY
020B 0 7003          MDX HM2        GO ISSUE SEEK CMND
020C 0 C008          LO SKST       RETRIEVE LAST DSW
020D 0 350A          W350A OC /350A  SEEK HOME ERROR
020E 0 70F5          MDX HM+1      TRY AGAIN
020F 0 0854          HM2 XIO HOME   SEEK TO HOME
0210 0 085B          XIO OSNS       SENSE DISK STATUS
0211 0 1001          SLA 1          POSITION OP CP BIT
0212 00 4C100210      BSC L HM2+1,- BRANCH IF NOT DONE
0214 0 70F0          MDX HM1        GO CHECK HOME BIT
*
0215 0 0000          SKST DC 0      SEEK DSW SAVE LOC
*
* THIS ROUTINE SEEKS 2310 TO DESIRED CYL
*
0216 0 0000          SEEK OC 0      ENTRY POINT
0217 0 0854          XIO DSNS       SENSE DISK STATUS
0218 0 1002          SLA 2          POSITION DSW
0219 00 4C10021D      BSC L SEEK1,- BRANCH ON READY
021B 0 350B          W350B DC /350B  DISK NOT READY
*****
021C                ORG 540        CARD 16
*****
021C 0 70FA          MOX SEEK+1     TRY AGAIN
021D 0 084C          SEEK1 XIO DSK   SEEK DISK
021E 0 084D          XIO DSNS       SENSE STATUS
021F 0 1001          SLA 1          POSITION DSW
0220 00 4C10021E      BSC L SEEK1+1,- BRANCH TILL DONE
0222 0 084B          XIO DSNSR      RESET DSW
0223 00 4C800216      BSC I SEEK    EXIT
*
* THIS ROUTINE READS THE DISK
*
0225 0 0000          READ DC 0      ENTRY POINT
0226 0 6815          STX 3 READ2+1  SAVE IMDEX REG 3
0227 0 6303          LOX 3 3        SET UP TRY COUNTER
0228 0 0843          XIO DSNS       SENSE DISK STATUS
0229 0 1007          SLA 2          POSITION DSW
022A 00 4C10022E      BSC L READ1,- BRANCH ON READY
```

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DIMAL INITIAL LOADER (CARD)

```
022C 0 350C          W350C DC /350C  DISK NOT READY-READ
022D 0 70F9          MDX READ+2     TRY AGAIN
022E 0 0837          READ1 XIO DRD   READ DISK
022F 0 083C          XIO DSNS       SENSE STATUS
0230 0 1001          SLA 1          POSITION DSW
0231 00 4C10022F      BSC L READ1+1,- BRANCH TILL NOT BUSY
0233 0 083A          XIO DSNSR      RESET DSW
0234 0 E039          AND DSNSR      CHECK FOR ERROR
0235 00 4C18023B      BSC L READ2,+ BRANCH IF NO ERROR
0237 0 73FF          MDX 3 -1      SKIP IF 3 TRIES
0238 0 70EF          MDX READ+3     TRY AGAIN
0239 0 350D          W350D DC /350D  DISK READ ERROR
023A 0 70EC          MDX READ+2     REPEAT
023B 00 67000000      READ2 LDX L3 0  RESTORE XR3
023D 00 4C800225      BSC I READ    RETURN TO USER
*
* THIS ROUTINE WRITES THE DISK AND PER-
* FORMS A MODULO 4 CHECK
*
023F 0 0000          WRIT DC 0      ENTRY POINT
*****
0240                ORG 576        CARD 17
*****
0240 0 082B          XIO DSNS       SENSE DISK STATUS
0241 0 1002          SLA 2          POSITION DSW
0242 0 481C          BSC -         SKIP IF NOT READY
0243 0 7002          MDX WRIT1      OK CONTINUE
0244 0 350E          W350E DC /350E  DISK NOT READY-WRITE
0245 0 70FA          MDX WRIT+1     TRY AGAIN
0246 0 6103          WRIT1 LDX 1 3  SET TRY INDEX
0247 0 0820          XIO DWRT       WRITE DISK
0248 0 0823          XIO OSNS       SENSE STATUS
0249 0 1001          SLA 1          POSITION DSW
024A 00 4C100248      BSC L WRIT1+2,- BRANCH TILL DONE
024C 0 0821          XIO DSNSR      RESET DSW
024D 0 E020          AND DSNSR      CHECK FOR ERROR
024E 00 4C180254      BSC L WRIT2,+ BRANCH IF NO ERROR
0250 0 71FF          MDX 1 -1      SKIP IF 3 TRIES
0251 0 70F5          MDX WRIT1+1    TRY AGAIN
0252 0 350F          W350F DC /350F  DISK WRT/MOD 4 ERROR
0253 0 70ED          MDX WRIT+2     REPEAT
0254 0 C014          WRIT2 LD DWRT+1 SETUP MODULO 4 ICC
0255 0 F016          EOR DSNS       SET IN WRITE IOCC
0256 0 D012          STO DWRT+1     DO MODULO 4 CHECK
0257 0 0810          XIO DWRT       SENSE STATUS
0258 0 0813          XIO DSNS       POSITION DSW
0259 0 1001          SLA 1          SPIN TILL NOT BUSY
025A 00 4C100258      BSC L WRIT2+4,- RESTORE WRITE IOCC
025C 0 C00C          LD DWRT+1
025D 0 F00E          EOR DSNS
025E 0 D00A          STO DWRT+1     SET IN IOCC
025F 0 080E          XIO DSNSR      RESET DSW
0260 0 E00D          AND DSNSR      CHECK FOR ERROR
0261 00 4C98023F      BSC I WRIT,+ RETURN TO USER IF OK
0263 0 70EC          MDX W350F-2    ERROR - BRANCH
*
*****
0264                ORG 612        CARD 18
*****
0264 0 0000          BSS E 0
*
0264 0 00CA          HOME DC 202     SEEK HOME IOCC
0265 0 0404          DC /0404
0266 0 044C          DRD DC 10A      READ DISK IOCC
0267 0 0603          DC /0603
0268 0 044C          DWRT DC 10A     WRITE DISK IOCC
0269 0 0500          DC /0500
026A 0 0000          DSK DC 0        SEEK DISK IOCC
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DIMAL INITIAL LOADER (CARO)

```

0268 0 0400      DC      /0400
026C 0 0380      OSNS    OC      /0380
0260 0 0700      DC      /0700
026E 0 8740      DSNSR   DC      /8740
026F 0 0701      DC      /0701

0270 00 C40003E8  CKAD   LO   I   IN      PICK UP STARTING ADR
0272 0 8009      CMP     K3000  CK ADRS FOR LEGAL
0273 0 7001      MDX     CKAD1   GREATER
0274 0 7005      MDX     CKAD2   AORS OK-LESS
0275 0 8007      CKAD1  CMP     K70FF  CK AORS FOR LEGAL
0276 0 7003      MDX     CKAD2   AORS OK
0277 0 1000      NOP
0278 00 4C0000E7  BSC     L   GEN4-3  IGNORE CARD
027A 00 4C0000EF  K3000  OC      /3000  AORS IS OK-MOVE DATA
027C 0 3000      K70FF  OC      /70FF  AORS CK CONSTANTS
027D 0 70FF

```

PROGRAM RESTART OPERATION.

```

027E 0 1010      RSTRT  SLA     16      CLEAR PROGRAM CONTRCL
027F 00 040001A1  STO     L   REF      *SWITCHES
0281 00 04000128  STO     L   SCID
0283 00 04000129  STO     L   ADRS
0285 00 0400012F  STO     L   WDCT
0287 0 00E1      LD       ORRT+1  PICKUP WRITE COMMAND
*****
0288              ORG       648      CARO 19
*****
0288 00 E4000177  AND      L   KFFF0  SET SECTOR BITS TO 0
028A 0 89DE      STO     ORRT+1  REPLACE COMMAND
028B 00 C40001A2  LD       L   K7      GET CONSTANT 7
028D 00 D40001A4  STO     L   TEST     SET IN HEADER TEST SW
028F 00 4C000004  BSC     L   GEN4     GO INPUT DIMAL
0291 0 0808      CKPT   ^IO    OESW   CK FOR PT LOAD FROM CARO
0292 00 F4000020  EOR      L   KFFFF  PT IF SWS = FFFF
0294 00 4C2001CA  BSC     L   LAST1,2  BRANCH IF NORM CARO LOAD
0296 00 44000203  BSI     L   HM       RETLRN ARM TO HOME
0298 0 3510      W3510  DC      /3510  PT DIMAL LOADED
0299 0 70FE      MDX     W3510  END OF PT LOAD TRAP
029A 0 0000      BSS     E   0
029A 0 0000      OESW   OC      0
029B 0 0740      DC      /0740  SENSE DE SWITCHES ICC
029C 0 0000      ENO     0

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88708170
88708180
88708190
88708200
88708210
88708220
88708230
88708240
88708250
88708260
88708270
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88708290
88708300
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88708370
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88708600

8870859 88708600

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DIMAL INITIAL LOADER (CARO)

CROSS REFERENCE LISTING

SYMBOL	VALUE	REFERENCES
AORS	0129	00EA,00EF,0100,010F,017C,0283
BRN	0018	0015
COC	0128	00GD,00E4,00F7,0163
COCT	001E	0009
CK	0082	00A3
CKAC	0270	00ED
CKAD1	0275	0273
CKA02	027A	0274,0276
CKPT	0291	01C5
CK1	0083	00AA
DESW	029A	0291
ORD	0266	01AC,0180,01D1,0104,01F1,01F4,01F6,022E
OSK	026A	0090,00D8,0198,01CE,01ED,0210
OSNS	026C	0210,0217,021E,0228,022F,0240,0248,0255,0258,0250
DSNSR	026E	0205,0222,0233,0234,024C,024D,025F,0260
OSW	001C	0001,0004,0024,002A
OSW1	005C	0031,0038
OWRT	0268	0198,019E,01A9,0188,0109,01DC,0247,0254,0256,0257,025C,025E,0287,028A
ED1T	0024	0017,0037,0056,0058
ED1T1	002A	002C,004E
ED1T2	0038	0034,0065
ED1T3	004F	0040
EDSW	005E	0028,003E,0046
EDWD	0200	004A,0089,008C,0093,01F8
ENO	0179	0125
ENOCK	0122	00EC
END1	0183	017F
END2	0180	0182,01A0
EN03	018F	018C
FILL	011F	0101
GEN	0004	00C8,028F
GEN1	00DC	0180,01C8
GEN3	00DF	00E9,0145,016E,0172
GEN4	00EA	00E5,011E,0278
GEN5	00F5	00F8
GEN6	00FC	00F1,0118,0127
GEN7	0108	011D,0121
GEN8	010F	
GEN9	00EF	027A
H8CV	005F	0038
H8CV1	0062	0085
H8CV2	0069	0070
H8CV3	0073	0077
H8CV4	0078	0071,0074
H8CV5	007A	
HM	0203	0098,0004,01CA,01E9,0208,020E,0296
HM1	0205	0214
HM2	020F	0208,0212
HOME	0264	0025,0097,020F
IN	03E8	0032,005A,006C,00F7,00FE,0103,0106,0108,0108,0122,0132,0153,0156,015A,0164,0270
INT	0089	0054
INT1	0093	009A
INT10	00D0	00C0,00CE
INT2	0098	00A7
INT3	009D	
INT4	00A8	00A4,00B1
INT5	008E	00A8,00AF
INT6	00C0	00CA
INT7	00C4	0002
INT8	00C9	00CF
INT9	00C8	00C3,00D1
IOA	044C	0000,0013,00A1,00A8,01A7,0184,0107,0266,0268
IPL1	0000	0008

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DIMAL INITIAL LOADER (CARD)

IPL2	0001	0002,0010
IPL3	0000	0006
KE000	0059	0043
KFFFF	0020	0051,0292
KFFF0	0177	0190,0103,0108,0288
K0F00	012E	0124
K0100	001F	0000
K3	0178	0184,01C4
K3000	027C	0272
K320	0120	0116
K321	01A3	0183,01F2
K7	01A2	01F5,0288
K70FF	0270	0275
K8	0003	30C9
LAST	018F	00E1,0185
LAST1	01CA	0294
LCD	012A	00E0,014A,01C0
LO	01E9	
LOC	0086	0061,007E,0083
LST	014A	013F
OUT	044E	0039,0041,0048,004F,0C81,010D,01E1,01E5
PACK	014C	00E3
PACK1	014F	0161
PACK2	0150	015F
PACK3	0159	0152
PACK4	0170	014C,0168
PREP	0011	000C
RD	0132	0139
RDC0	0136	00DF,0147,0149
RDC01	013A	013C
RDC02	0141	0148
ROED	0C5A	0C29
RUPAC	001A	00C0,0007
READ	0225	009F,0182,01DE,01F7,022D,0238,023A,023D
REA01	022E	022A,0231
READ2	0238	0226,0235
REF	01A1	018F,0191,027F
RESN	0134	0144,0167,016A
RSN	0130	0105,0136,013A
RSTRY	027E	0018
SAVE	0087	0067,0078,0079
SAVE1	0080	0068,006A,007A
SC10	0128	00F0,0170,0183,01E7,0189,01C2,0281
SEEK	0216	009D,00DA,019A,01D0,01EE,021C,0223
SEEK1	021D	0219,0220
SHIFT	0174	0150
SID	0440	0000,00C0,00C8
SKST	0215	0206,020C
SNSW	0084	00AE,008F
SUM	0164	0169
TEST	01A4	0180,028D
USTB	0086	008B,008E,00C4,00D6,0CF5,0193,0195,01C8,01E3,01EA
WDCY	012F	0113,0115,0188,0285
WRIT	022F	0186,01E7,0245,0253,0261
WRITE	01A5	0117,017A,018D
WRIT1	0246	0243,024A,0251
WRIT2	0254	024E,025A
W350A	0200	350A
W350B	0218	350B
W350C	022C	350C
W350D	0239	350D
W350E	0244	350E
W350F	0252	350F,0263
W3500	000F	3500,0005
W3501	0036	3501,003D,0045,0053
W3502	0055	3502,0025
W3503	0057	3503,002F
W3504	00A6	3504

DIMAL INITIAL LOADER (CARD)

W3505	00AD	3505
W3506	0146	3506,0137
W3507	0148	3507,0142
W3508	0160	3508
W3509	01C7	3509
W3510	0298	3510,0299
ZERG	012C	00FD,0119,011F

BASIC DIAGNOSTIC LOADER

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ABS
* CARD 01 -----
0000 ORG 0 88800020
* ----- LOADER SHOULD BE IN LOCS. 00 - 28 ----- 88800030
* ----- SEE THAT B IS 30FF. PRESS START ----- 88800040
0000 0 30FF WAIT -1 --BEGINNING OF LOADER. 88800050
0001 0 0C00 000C XIO L B1 READ ONE CARD INTO LOC. 28 88800060
0003 0 0C00 0010 XIO L K0800 RESET DSW 88800070
0005 0 0C00 000E A1 XIO L K0003 SENSE DSW FOR 1442 88800080
0007 0 F400 000E EOR L K0003 CHECK BITS 14&15 ONLY 88800090
0009 0 4820 BSC Z SKIP BITS 14&15 ONLY 88800100
000A 0 6016 LDX E1 CONTINUE DSW ANALYSIS 88800110
000B 0 6005 LOX A1 CARD IS BEING READ 88800120
000C 0 0028 B1 DC /0028 READ CONTROL 88800130
000D 0 1601 DC /1601 1442, 8/8 FORMAT 88800140
000E 0 0003 K0003 DC /0003 SENSE DSW CONTROL 88800150
000F 0 1700 DC /1700 WITHOUT TURN OFF 88800160
0010 0 0800 K0800 DC /0800 SENSE DSW CONTROL 88800170
0011 0 1703 DC /1703 TURN OFF REQUEST 88800180
0012 0 F400 0010 D1 EOR L K0800 RETURN DSW WORD TO ACC. 88800190
0014 0 3001 WAIT /1 **ERR. SEE ACC. DSW NOT RIGHT 88800200
0015 0 6001 LDX /1 TRY AGAIN 88800210
0016 0 0C00 0010 E1 XIO L K0800 SENSE AND TURN OFF DSW 88800220
0018 0 F400 0010 EOR L K0800 CHECK FOR BIT 4 ONLY 88800230
001A 0 4820 BSC Z SKIP OPERATION COMPLETE 88800240
001B 0 6012 LOX D1 DSW ERROR CONDITION 88800250
001C 0 6023 LDX /23 88800260
001D 0 0000 OC 0 88800270
001E 0 0000 OC /0000 SPACE FILLER 88800280
001F 0 0000 OC /0000 SPACE FILLER 88800290
0020 0 0000 DC /0000 SPACE FILLER 88800300
0021 0 0000 OC /0000 SPACE FILLER 88800310
0022 0 0000 DC /0000 SPACE FILLER 88800320
0023 0 6028 LOX /28 SPACE FILLER 88800330
GO TO PROG. LOADFO 88800340

```

ADDRESS	DATA	OPERATION	STATUS	DESCRIPTION	ADDRESS	DATA	OPERATION	STATUS	DESCRIPTION
0024		ORG		/0028					88800370
0028	0 0C00	XIO	L	K0800			RESET DSW		88800380
002A	0 0C00	XIO	L	B2			READ A CARD INTO /0000		88800390
002C	0 0C00	XIO	L	K0003			SENSE DSW FOR 1442		88800400
002E	0 F400	EOR	L	K0003			CHECK BITS 14&15 ONLY		88800410
0030	0 4820	BSC		Z			SKIP BITS 14 & 15 ONLY		88800420
0031	0 603E	LDX		E2			CONTINUE DSW ANALYSIS		88800430
0032	0 602C	LQX		A2			CARD IS BEING READ		88800440
0033	0 0000	DC		/0000			SPACE FILLER		88800450
0034	0 0000	B2	OC	/0000			READ CONTROL & CONSTANT		88800460
0035	0 1601		OC	/1601			1442, 818 FORMAT		88800470
0036	0 0003	K0003	OC	/0003			SENSE OSW CONTROL		88800480
0037	0 1700		OC	/1700			WITHOUT TURN OFF		88800490
0038	0 0800	K0800	OC	/0800			SENSE DSW CONTROL		88800500
0039	0 1703		DC	/1703			TURN OFF REQUEST		88800510
003A	0 0C00	02	XIO	L	K0800		RESET OSW TO ACC		88800520
003C	0 3002		WAIT	/2			**ERR. SEE ACC. OSW NOT RIGHT		88800530
003D	0 6028	R2	LOX	/28			TRY AGAIN		88800540
003E	0 0C00	E2	XIO	L	K0003		SENSE OSW		88800550
0040	0 F400		EOR	L	K0800		CHECK FOR BIT 4		88800560
0042	0 1801		SRA	1			REMOVE NOT READY BIT		88800570
0043	0 4820		BSC	Z			SKIP OPERATION COMPLETE		88800580
0044	0 603A		LOX	D2			DSW ERROR CONDITION		88800590
0045	0 0C00		XIO	L	K0800		SENSE - RESET OSW		88800600
		*					CARD 11 WILL START HERE		88800610
0047	0 C400	BLO	LO	L	R2		SET PROG. LOADED TO RETURN		88800620
0049	0 D400		STO	L	/23		TO THIS PROG.		88800630
004B	0 6000		LOX		/0000		GO TO PROG. LOADED		88800640

```
* CARD 03 ----- 8B800660
* TEST FOR XIO OF READ AND SENSE OSW 8B800670
*---*** RUN ONLY ON IPL ***----- 8B800680
*---*** RUN ONLY ON IPL ***----- 8B800690
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0000	0	6023		ORG	0		88800700
0001	0	0C00	0016	LDX	/23		88800710
0003	0	0C00	001A	XIO	L B3	READ A CARD	88800720
0005	0	0C00	0018	XIO	L K0800	RESET DSW	88800730
0007	0	3003		XIO	L K0003	SENSE DSW FOR 1442	88800740
			A3	WAIT	/0003	--ACC. HAS DSW. SHOULD BE0003	88800750
			*			IF OK PRESS START	88800760
0008	0	0C00	001A	XIO	L K0800	SENSE AND RESET DSW	88800770
000A	0	3003		WAIT	/0003	--ACC. HAS DSW SHOULD BE0800	88800780
			*				88800790
000B	0	0C00	0016	LP	XIO L B3	LOOP FOR TESTING REAO CARD	88800800
000D	0	0C00	001A		XIO L K0800	RESET DSW	88800810
000F	0	0C00	0018	A3A	XIO L K0003	SHOULD READ AS LONG AS	88800820
0011	0	F400	0018		EOR L K0003	CARDS IN READER	88800830
0013	0	4820			BSC Z	SKIP BITS 14&15 #BUSY,READY	88800840
0014	0	601C			LOX E3		88800850
0015	0	600F			LOX A3A	BUSY AND NOT READY GET DSW	88800860
0016	0	0028		B3	DC /0028	RD CARD CONTROL WORDS	88800870
0017	0	1601			OC /1601		88800880
0018	0	0003		K0003	OC /0003	SENSE DSW CONTROL	88800890
0019	0	1700			DC /1700	WITHOUT TURN OFF	88800900
001A	0	0800		K0800	DC /0800	SENSE OSW CONTROL	88800910
001B	0	1703			DC /1703	TURN OFF REQUEST	88800920
001C	0	0C00	001A	E3	XIO L K0800	SENSE AND TURN OFF DSW	88800930
001E	0	F400	001A		EOR L K0800	CHECK BIT 4 ONLY	88800940
0020	0	4820			BSC Z	SKIP OPERATION COMPLETE	88800950
0021	0	3003			WAIT /3	**ERR. OSW WRONG. ACC. HAS	88800960
			*			DSW AFTER AN EOR WITH BIT 4	88800970
0022	0	600B		LOX	LP	GO READ NEXT CARD	88800980
0023	0	6001		LOX	/1	CHANGED TO LDX 28 BY LOADER	88800990
			*				

0024		ORG	FOR LOCATIONS 0 THRU 3F	88801020
0000	0 6002	LOX	/0000	88801030
0001	0 3004	LOX	/0002	88801040
0002	0 601D	WAIT	/4	88801050
0003	0 3004	LOX	/0010	88801060
0004	0 3004	WAIT	/4	88801070
0005	0 3004	WAIT	/4	88801080
0006	0 3004	WAIT	/4	88801090
0007	0 3004	WAIT	/4	88801100
0008	0 3004	WAIT	/4	88801110
0009	0 3004	WAIT	/4	88801120
000A	0 3004	WAIT	/4	88801130
000B	0 3004	WAIT	/4	88801140
000C	0 3004	WAIT	/4	88801150
000D	0 3004	WAIT	/4	88801160
000E	0 3004	WAIT	/4	88801170
000F	0 3004	WAIT	/4	88801180
0010	0 3004	WAIT	/4	88801190
0011	0 3004	WAIT	/4	88801200
0012	0 3004	WAIT	/4	88801210
0013	0 3004	WAIT	/4	88801220
0014	0 3004	WAIT	/4	88801230
0015	0 3004	WAIT	/4	88801240
0016	0 3004	WAIT	/4	88801250
0017	0 3004	WAIT	/4	88801260
0018	0 3004	WAIT	/4	88801270
0019	0 3004	WAIT	/4	88801280
001A	0 3004	WAIT	/4	88801290
001B	0 3004	WAIT	/4	88801300
001C	0 3004	WAIT	/4	88801310
001D	0 6023	LOX	/0023	88801320
001E	0 3004	WAIT	/4	88801330
001F	0 3004	WAIT	/4	88801340
0020	0 3004	WAIT	/4	88801350
0021	0 3004	WAIT	/4	88801360
				88801370

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```
0022 0 3004      WAIT  /4      **ERR. LOX, IPL CARD-4      8B8013B0
0023 0 6000      LDX    /0000  --CHANGED TO LOX 28 BY LOADER 8B801390
* CAROS 5 & 7 & 8 TEST THE FOLLOWING-----
*1.CRP REAOS IN 0 & 1 EACH BIT POSITION      8B801400
*2.LO 1 PUTS 0 & 1 IN EACH BIT OF ACC.      8B801410
*3.BSC Z SKIPS ON ACC#0000 & NOT SKIP ANY 1 BIT. 8B801420
* 4 TEST EOR FOR 1,1 & 0,0      8B801430
*      8B801440
*      8B801450
* CARO 05 -----
0024      ORG    /0000      8B801460
0000 0 C400 0009  LD    L K0000      SET ACC. TO 0000      8B801470
0002 0 4820      BSC    Z      TEST SKIP ON ZERO      8B801480
0003 0 3005      WAIT   /5      **ERR. BSC Z FAILED OR ACC. 8B801490
                                NOT QUAL 0000.      8B801500
0004 0 C400 000A BIT00 LO L K8000      SET BIT 0 TO 1, OTHERS 0. 8B801510
0006 0 4820      BSC    Z      SHOULD NOT SKIP      8B801520
0007 0 6008      LOX    BIT01      8B801530
0008 0 3005      WAIT   /5      **ERR. BSC Z FAIL OR ACC#0000 8B801540
0009 0 0000      K0000 OC /0000      8B801550
000A 0 8000      K8000 OC /8000      8B801560
000B 0 C400 0010 BIT01 LO L K4000      SET BIT 1 TO 1, OTHERS 0. 8B801570
0000 0 4820      BSC    Z      SHOULD NOT SKIP      8B801580
000E 0 6011      LOX    BIT02      8B801590
000F 0 3005      WAIT   /5      **ERR. BSC Z FAIL OR ACC#0000 8B801600
0010 0 4000      K4000 OC /4000      8B801610
0011 0 C400 0016 BIT02 LO L K2000      SET BIT 2 TO 2, OTHERS 0. 8B801620
0013 0 4820      BSC    Z      SHOULD NOT SKIP      8B801630
0014 0 6017      LOX    BIT03      8B801640
0015 0 3005      WAIT   /5      **ERR. BSC Z FAIL OR ACC#0000 8B801650
0016 0 2000      K2000 OC /2000      8B801660
0017 0 C400 001C BIT03 LO L K1000      SET BIT 3 TO 1, OTHERS 0. 8B801670
0019 0 4820      BSC    Z      SHOULD NOT SKIP      8B801680
001A 0 6010      LOX    BIT04      8B801690
001B 0 3005      WAIT   /5      **ERR. BSC Z FAIL OR ACC#0000 8B801700
001C 0 1000      K1000 OC /1000      8B801710
0010 0 C400 0022 BIT04 LO L K0800      SET BIT 4 TO 1, OTHERS 0. 8B801720
001F 0 4820      BSC    Z      SHOULD NOT SKIP      8B801730
0020 0 6023      LOX    /0023      8B801740
0021 0 3005      WAIT   /5      **ERR. BSC Z FAIL OR ACC#0000 8B801750
0022 0 0800      K0800 OC /0800      8B801760
0023 0 6000      LOX    /0000      CHANGED TO LOX 28 BY LOADER 8B801770
                                8B801780
                                8B801790
                                8B801800
* CARO 06 -----
0024      ORG    /0000      CARO 6--BSC Z & LO L TEST-- 8B801810
0000 0 C400 0005 BIT05 LO L K0400      SET BIT 5 TO 1, OTHERS 0. 8B801820
0002 0 4820      BSC    Z      SHOULD NOT SKIP      8B801830
0003 0 6006      LOX    BIT06      8B801840
0004 0 3006      WAIT   /6      **ERR. BSC Z FAIL OR ACC#0000 8B801850
0005 0 0400      K0400 OC /0400      8B801860
0006 0 C400 0008 BIT06 LO L K0200      SET BIT 6 TO 2, OTHERS 0. 8B801870
0008 0 4820      BSC    Z      SHOULD NOT SKIP      8B801880
0009 0 600C      LOX    BIT07      8B801890
000A 0 3006      WAIT   /6      **ERR. BSC Z FAIL OR ACC#0000 8B801900
000B 0 0200      K0200 OC /0200      8B801910
000C 0 C400 0011 BIT07 LO L K0100      SET BIT 7 TO 1, OTHERS 0. 8B801920
000E 0 4820      BSC    Z      SHOULD NOT SKIP      8B801930
000F 0 6012      LOX    BIT08      8B801940
0010 0 3006      WAIT   /6      **ERR. BSC Z FAIL OR ACC#0000 8B801950
0011 0 0100      K0100 OC /0100      8B801960
0012 0 C400 0017 BIT08 LO L K0080      SET BIT 8 TO 1, OTHERS 0. 8B801970
0014 0 4820      BSC    Z      SHOULD NOT SKIP      8B801980
0015 0 6018      LOX    BIT09      8B801990
0016 0 3006      WAIT   /6      **ERR. BSC Z FAIL OR ACC#0000 8B802000
0017 0 0080      K0080 OC /0080      8B802010
0018 0 C400 0010 BIT09 LO L K0040      SET BIT 9 TO 1, OTHERS 0. 8B802020
001A 0 4820      BSC    Z      SHOULD NOT SKIP      8B802030
001B 0 6023      LOX    /0023      8B802040
001C 0 3006      WAIT   /6      **ERR. BSC Z FAIL OR ACC#0000 8B802050
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0010 0 0040      K0040 OC /0040      8B802060
001E 0 0000      OC    /0000      SPACE FILLER      8B802070
001F 0 0000      OC    /0000      SPACE FILLER      8B802080
0020 0 0000      OC    /0000      SPACE FILLER      8B802090
0021 0 0000      OC    /0000      SPACE FILLER      8B802100
0022 0 0000      OC    /0000      SPACE FILLER      8B802110
0023 0 6000      LOX    /0000      CHANGED TO LOX 28 BY LOADER 8B802120
                                8B802130
* CARO 07 -----
0024      ORG    /0000      CARO 7--BSC Z & LO 1 TEST-- 8B802140
0000 0 C400 0005 BIT10 LO L K0020      SET BIT 10 TO 1, OTHERS 0. 8B802150
0002 0 4820      BSC    Z      SHOULD NOT SKIP      8B802160
0003 0 6006      LOX    BIT11      8B802170
0004 0 3007      WAIT   /7      **ERR. BSC Z FAIL OR ACC#0000 8B802180
0005 0 0020      K0020 OC /0020      8B802190
0006 0 C400 0008 BIT11 LO L K0010      SET BIT 11 TO 1, OTHERS 0. 8B802200
0008 0 4820      BSC    Z      SHOULD NOT SKIP      8B802210
0009 0 600C      LOX    BIT12      8B802220
000A 0 3007      WAIT   /7      **ERR. BSC Z FAIL OR ACC#0000 8B802230
000B 0 0010      K0010 OC /0010      8B802240
000C 0 C400 0011 BIT12 LO L K0008      SET BIT 12 TO 1, OTHERS 0. 8B802250
000E 0 4820      BSC    Z      SHOULD NOT SKIP      8B802260
000F 0 6012      LOX    BIT13      8B802270
0010 0 3007      WAIT   /7      **ERR. BSC Z FAIL OR ACC#0000 8B802280
0011 0 0008      K0008 OC /0008      8B802290
0012 0 C400 0017 BIT13 LO L K0004      SET BIT 13 TO 1, OTHERS 0. 8B802300
0014 0 4820      BSC    Z      SHOULD NOT SKIP      8B802310
0015 0 6018      LOX    BIT14      8B802320
0016 0 3007      WAIT   /7      **ERR. BSC Z FAIL OR ACC#0000 8B802330
0017 0 0004      K0004 OC /0004      8B802340
0018 0 C400 0010 BIT14 LD L K0002      SET BIT 14 TO 1, OTHERS 0. 8B802350
001A 0 4820      BSC    Z      SHOULD NOT SKIP      8B802360
001B 0 6023      LOX    /0023      8B802370
001C 0 3007      WAIT   /7      **ERR. BSC Z FAIL OR ACC#0000 8B802380
0010 0 0002      K0002 OC /0002      8B802390
001E 0 0000      OC    /0000      SPACE FILLER      8B802400
001F 0 0000      OC    /0000      SPACE FILLER      8B802410
0020 0 0000      OC    /0000      SPACE FILLER      8B802420
0021 0 0000      OC    /0000      SPACE FILLER      8B802430
0022 0 0000      OC    /0000      SPACE FILLER      8B802440
0023 0 6000      LOX    /0000      CHANGED TO LDX 28 BY LOADER 8B802450
                                8B802460
                                8B802470
* CARD 08 -----
0024      ORG    /0000      CARD 8 BSC Z & LD BIT 15 8B802480
0000 0 C400 0005 BIT15 LD L K0001      AND EOR 1,1 & 0,0 8B802490
0002 0 4820      BSC    Z      SET BIT 15 TO 1, OTHERS 0. 8B802500
0003 0 6006      LOX    TEOR      SHOULD NOT SKIP      8B802510
0004 0 3008      WAIT   /8      **ERR. BSC Z FAIL OR ACC#0000 8B802520
0005 0 0001      K0001 DC /0001      8B802530
0006 0 C400 0012 TEOR LO L KFFFF      8B802540
0008 0 F400 0012 EOR L KFFFF      TEST 0#FFFF & A#FFFF 8B802550
000A 0 4820      BSC    Z      SHOULD SKIP      8B802560
000B 0 3008      WAIT   /8      **ERR. SEE ACC. SHOULD # 0000 8B802570
000C 0 F400 0011 EOR L K0000      TEST 0#0000 & A#0000 8B802580
000E 0 4820      BSC    Z      SHOULD SKIP      8B802590
000F 0 3008      WAIT   /8      **ERR. SEE ACC. SHOULD # 0000 8B802600
0010 0 6023      LOX    /0023      8B802610
0011 0 0000      K0000 OC /0000      8B802620
0012 0 FFFF      KFFFF OC /FFFF      8B802630
0013 0 0000      OC    /0000      SPACE FILLER      8B802640
0014 0 0000      OC    /0000      SPACE FILLER      8B802650
0015 0 0000      OC    /0000      SPACE FILLER      8B802660
0016 0 0000      OC    /0000      SPACE FILLER      8B802670
0017 0 0000      OC    /0000      SPACE FILLER      8B802680
0018 0 0000      OC    /0000      SPACE FILLER      8B802690
0019 0 0000      OC    /0000      SPACE FILLER      8B802700
001A 0 0000      OC    /0000      SPACE FILLER      8B802710
                                8B802720
                                8B802730
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001B 0 0000 DC /0000 SPACE FILLER 8B802740
001C 0 0000 OC /0000 SPACE FILLER 8B802750
001D 0 0000 DC /0000 SPACE FILLER 8B802760
001E 0 0000 OC /0000 SPACE FILLER 8B802770
001F 0 0000 OC /0000 SPACE FILLER 8B802780
0020 0 0000 DC /0000 SPACE FILLER 8B802790
0021 0 0000 OC /0000 SPACE FILLER 8B802800
0022 0 0000 DC /0000 SPACE FILLER 8B802810
0023 0 6000 LOX /0000 CHANGED TO LDX 28 BY LOADER 8B802820

* CARD 09 -----
* TEST LOAD AND STORE LONG FORM 8B802830
* TEST ALL BITS TRANSFER B-D-A-U-A, A-M, A-B 8B802840
* EOR L & BSC Z COULD CAUSE FAILURE 8B802850
0024 ORG /0000 8B802860
0000 0 C400 000D TST1 LO L KON1&1 LOAO A TO 3333 8B802870
0002 0 0400 CCCC KON2 STO L /CCCC STORE 3333 IN CCCC 8B802880
0004 0 C400 CCCC LD L /CCCC GET 3333 FROM CCCC 8B802890
0006 0 F400 000D EOR L KON1&1 EOR A#3333 TO 0#3333 8B802900
0008 0 4820 BSC Z SHOULD SKIP 8B802910
0009 0 3009 WAIT /9 **ERR. A NOT 0000. GO TO TST1 8B802920
000A 0 C400 0003 TST2 LO L KON2&1 LO A TO CCCC 8B802930
000C 0 0400 3333 KON1 STO L /3333 STORE CCCC AT 3333 8B802940
000E 0 C400 3333 LD L /3333 GET CCCC FROM 3333 8B802950
0010 0 F400 0003 EOR L KON2&1 EOR A#CCCC TO D#CCCC 8B802960
0012 0 4820 BSC Z SHOULD SKIP 8B802970
0013 0 3009 WAIT /9 **ERR. A NOT 0000. GO TO TST2 8B802980
0014 0 6023 LOX /0023 8B802990
0015 0 0000 OC /0000 SPACE FILLER 8B803000
0016 0 0000 DC /0000 SPACE FILLER 8B803010
0017 0 0000 DC /0000 SPACE FILLER 8B803020
0018 0 0000 OC /0000 SPACE FILLER 8B803030
0019 0 0000 DC /0000 SPACE FILLER 8B803040
001A 0 0000 OC /0000 SPACE FILLER 8B803050
001B 0 0000 DC /0000 SPACE FILLER 8B803060
001C 0 0000 OC /0000 SPACE FILLER 8B803070
001D 0 0000 OC /0000 SPACE FILLER 8B803080
001E 0 0000 OC /0000 SPACE FILLER 8B803090
001F 0 0000 DC /0000 SPACE FILLER 8B803100
0020 0 0000 DC /0000 SPACE FILLER 8B803110
0021 0 0000 OC /0000 SPACE FILLER 8B803120
0022 0 0000 OC /0000 SPACE FILLER 8B803130
0023 0 6000 LDX /0000 CHANGED TO LDX 28 BY LOADER 8B803140

* CARD A TEST EOR FOR 1,0 0,1-----
* SRA 1 PARTLY TESTED 8B803150
* 8B803160
* 8B803170
* 8B803180
* 8B803190
* CARD 0A -----
* ORG /0000 8B803200
0024 ORG /0000 8B803210
0000 0 C400 001B LO L K8000 PUT BIT 0#1 IN ACC. 8B803220
0002 0 6008 LDX RETRY-2 GO STORE IT IN TEST 8B803230
0003 0 1801 SHIFT SRA 1 SHIFT TO TEST NEXT BIT 8B803240
0004 0 4820 BSC Z SHOULD NOT SKIP 8B803250
0005 0 6008 LOX RETRY-2 8B803260
0006 0 300A WAIT /A **ERR. SRA 1 DROPPED BIT 8B803270
* LOC. TEST HAS WORD BEFORE 8B803280
* SRA 1 THAT FAILED. 8B803290
0007 0 600A LDX RETRY 8B803300
0008 0 D400 0018 STO L TEST STORE BIT FOR EOR TEST 0&1 8B803310
000A 0 C400 0019 LD L K0000 CLEAR ACC. 8B803320
000C 0 F400 0018 EOR L TEST EOR ACC#0 0 HAS A 1. 8B803330
000E 0 4820 BSC Z SHOULD NOT SKIP 8B803340
000F 0 6012 LDX CONTA 8B803350
0010 0 300A WAIT /A **ERR. A IS 0000 GO TO RETRY 8B803360
0011 0 600A LDX RETRY 8B803370
0012 0 F400 0019 CONTA EOR L K0000 EOR ACC. HAS A 1 & 0#0 8B803380
0014 0 4820 BSC Z SHOULD NOT SKIP 8B803390
0015 0 601C LDX CONTB 8B803400
0016 0 300A WAIT /A **ERR. A IS 0000 GO TO RETRY 8B803410

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0017 0 600A LDX RETRY 8B803420
0018 0 0000 TEST DC /0000 BIT WITH 1 IS BEING TESTED 8B803430
0019 0 0000 K0000 DC /0000 8B803440
001A 0 0001 K0001 OC /0001 8B803450
001B 0 8000 K8000 OC /8000 8B803460
001C 0 F400 001A CONTB EOR L K0001 TEST FOR BIT 15#1 8B803470
001E 0 4820 BSC Z SKIP BIT 15#1, ALL POS. DONE 8B803480
001F 0 6003 LOX SHIFT GO TO 00 NEXT BIT POSITION. 8B803490
0020 0 6023 LOX /0023 8B803500
0021 0 0000 OC /0000 SPACE FILLER 8B803510
0022 0 0000 DC /0000 SPACE FILLER 8B803520
0023 0 6000 LDX /0000 CHANGED TO LDX 28 BY LOADER 8B803530

* CARD 0B -----
* ORG 0 8B803540
* TEST ADD BY POSITIVE AND NEGATIVE ONES 8B803550
0024 ORG 0 8B803560
0000 0 C400 001C LO L H0000 CLEAR TO ZERO 8B803570
0002 0 D400 001B STO L SUMMI SUM OF MINUS ONES 8B803580
0004 0 D400 001A STO L SUMPL SUM OF PLUS ONES 8B803590
0006 0 C400 001B ADD LD L SUMMI GET SUM OF MINUS ONES 8B803600
0008 0 8400 001E A L HFFFF ADD MINUS ONE 8B803610
000A 0 D400 001B STO L SUMMI STORE SUM OF MINUS ONES 8B803620
000C 0 C400 001A LO L SUMPL GET SUM OF PLUS ONES 8B803630
000E 0 8400 001D A L H0001 ADD PLUS ONE 8B803640
0010 0 D400 001A STO L SUMPL STORE SUM OF PLUS ONES 8B803650
0012 0 4820 BSC Z SKIP WHEN SUM IS 0000 8B803660
0013 0 6015 LOX TOTAL 8B803670
0014 0 6023 LDX /0023 BRANCH WHEN ONE PASS DONE. 8B803680
0015 0 8400 001B TOTAL A L SUMMI ADD SUMMI TO SUMPL 8B803690
0017 0 4820 BSC Z SHOULD SKIP 8B803700
0018 0 300B WAIT /B **ERR. TOTAL SHOULD BE ZERO 8B803710
0019 0 6006 LDX ADD 8B803720
001A 0 0000 SUMPL OC /0000 LOC. FOR SUM OF PLUS ONES 8B803730
001B 0 0000 SUMMI DC /0000 LOC. FOR SUM OF MINUS ONES 8B803740
001C 0 0000 H0000 OC 0 8B803750
001D 0 0001 H0001 OC 1 8B803760
001E 0 FFFF HFFFF OC -1 8B803770
001F 0 0000 OC 0 8B803780
0020 0 0000 DC 0 SPACE FILLER 8B803790
0021 0 0000 DC 0 SPACE FILLER 8B803800
0022 0 0000 OC 0 SPACE FILLER 8B803810
0023 0 6000 LDX /0000 CHANGED TO LDX 28 BY LOADER 8B803820

* CARD 10 -----
* ORG 0 8B803830
0024 ORG 0 8B803840
0000 0 0C00 000C X10 L B10 READ ONE CARD INTO 8LO 8B803850
0002 0 0C00 0038 X10 L K0800 RESET DSW 8B803860
0004 0 0C00 0036 X10 L K0003 SENSE DSW FOR 1442 8B803870
0006 0 F400 0036 EOR L K0003 CHECK 8ITS 14&15 ONLY 8B803880
0008 0 4820 BSC Z SKIP 8ITS 14 & 15 ONLY 8B803890
0009 0 6012 LOX E10 CONTINUE DSW ANALYSIS 8B803900
000A 0 6004 LDX A10 CARD IS BEING READ 8B803910
000B 0 6028 C10 LOX /002B 8B803920
000C 0 0047 B10 DC BLD READ CONTROL 8B803930
000D 0 1601 DC /1601 1442 8/8 FORMAT 8B803940
000E 0 F400 0038 D10 EOR L K0800 RETURN DSW WORD TO ACC. 8B803950
0010 0 3010 WAIT /10 **ERR. SEE ACC. DSW NOT RIGHT 8B803960
0011 0 6000 LOX /0 TRY AGAIN 8B803970
0012 0 0C00 0038 E10 X10 L K0800 SENSE AND TURN OFF DSW 8B803980
0014 0 F400 0038 EOR L K0800 CHECK FOR BIT 4 ONLY 8B803990
0016 0 4820 BSC Z SKIP OPERATION COMPLETE 8B804000
0017 0 600E LDX 010 DSW ERROR CONDITION 8B804010
0018 0 6028 LOX /0028 READ A CARD & FORM ITS SUM 8B804020
* CARD 11 -----
* ORG BLD 8B804030
* THIS CARD IS READ OVER THE LOADER IN 46 THRU 68 8B804040
* USES ITS INSTRUCTIONS FROM 0028 THRU 0045 8B804050
8B804060
8B804070
8B804080
8B804090

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0047 0 C400 0024      LD  L /0D24      GET WORD COUNT      8B80410D
0049 0 4820            BSC  Z          SKIP IF WORD COUNT ZERO 8B804110
004A 0 604F            LDX  SUM1        8B80412D
0048 0 3011            WAIT /11        **ERR. WORD COUNT IS ZERO 8B80413D
004C 0 6028            LDX  /DD28      START LOADS NEXT CARD 8B804140
004D 0 0000            KDD0D DC /0000 8B804150
004E 0 0001            K0001 DC /DD01 8B804160
004F 0 C400 004D      SUM1 LD  L K0000  RESTORE MODIFIED ADDRESS 8B804170
0051 0 D400 0058      STO  L CKLDD&1 8B804180
0053 0 D40D 0027      STD  L /0D27    CLEAR SUM LOC.      8B804190
0055 0 C40D 0027      LD  L /0027    8B80420D
0057 0 84D0 FFFF      CKLOD A  L /FFFF  FORM SUM OF LOCS. 0 THRU 26 8B804210
0059 0 D40D 0027      STO  L /DD27    8B804220
005B 0 C40D 0058      LD  L CKLOD&1  MODIFY ADDRESS      8B804230
005D 0 8400 004E      A  L K0001      8B80424D
005F 0 D40D 0058      STD  L CKLDD&1 8B804250
0061 0 F400 DD56      EOR  L CKLOD-1  CHECK THAT ALL WORDS DONE 8B804260
0063 0 4820            BSC  Z          SKIP ALL LOCS. ADDED 8B804270
0064 0 6055            LDX  CKLOD-2    8B804280
0065 0 C400 DD27      LO  L /0027    LOAD SUM 0 THRU 26      8B804290
0067 0 4820            BSC  Z          SKIP READ IN OK.      8B80430D
0068 0 3D11            WAIT /11        **ERR. IN CHECK SU. START 8B804310
                                LOADS NEXT CARD.      8B804320
                                *
                                MOVE LDX /0D00  RUN CARD LOADED. CARD 13 8B804330
                                *                                BEGINS LOADING HERE. 8B804340
                                *                                8B804350
                                * CARD 12 ----- 8B80436D
                                ORG  D      8B80437D
                                * CHECK SUM CHECK CARD. THIS IS USED TO DETECT 8B804380
                                * ERRORS THAT OCCUR AS THE RESULT OF WRONG CHECK 8B804390
                                * SUM FOR CARD IMAGE IN LOCS. 0000 THRU 0026 8B804400
                                * CHECK SUM ROUTINES ADD LOC. 0 THRU 26 IN SEQUENCE 8B804410
                                * . CORRECT ACC. AFTER ADD IS SHOWN BELOW IN 8B804420
                                * SEQUENCE THE PROG. FOLLOWS. 8B804430
                                * CORE CONTENTS. CORRECT SUM. CORE LOC. 8B804440
                                *
                                OC /6028 6028 0000 8B804450
                                DC /9FD7 FFFF 0001 8B804460
                                DC /FFFF FFFF 0002 8B804470
                                DC /0001 FFFF 0003 8B804480
                                DC /0001 0000 0004 8B804490
                                DC /0001 0001 0005 8B804500
                                DC /0001 0002 0006 8B804510
                                DC /0002 0004 0007 8B804520
                                DC /0004 0008 0008 8B804530
                                DC /0008 0010 0009 8B804540
                                DC /0010 0020 000A 8B804550
                                DC /0020 0040 000B 8B804560
                                DC /0040 0080 000C 8B804570
                                DC /0080 0100 000D 8B804580
                                DC /0100 0200 000E 8B804590
                                DC /0200 0400 000F 8B804600
                                DC /0400 0800 0010 8B804610
                                DC /0800 1000 0011 8B804620
                                DC /1000 2000 0012 8B804630
                                DC /2000 4000 0013 8B804640
                                DC /4000 8000 0014 8B804650
                                DC /8000 0000 0015 8B804660
                                DC /5555 5555 0016 8B804670
                                DC /5555 AAAA 0017 8B804680
                                DC /AAAA 5554 0018 8B804690
                                DC /0001 5555 0019 8B804700
                                DC /AAAA FFFF 001A 8B804710
                                DC /AAAA AAA9 001B 8B804720
                                DC /5557 FFFF 001C 8B804730
                                DC /5555 5554 001D 8B804740
                                DC /AAAB FFFF 001E 8B804750
                                DC /1000 0FFF 001F 8B804760
                                DC /F100 00FF 0020 8B804770
```

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```
0021 0 FF1D            DC  /FF10      000F      0021      8B804780
0022 0 FFF1            DC  /FFF1      0000      0022      8B80479D
0023 0 321D            DC  /3210      3210      0023      8B804800
                                *          /0D24      3234      0024      8B804810
                                *          /010D      3334      0025      8B804820
                                *          /CCCC      0000      0026      8B804830
                                *                                8B804840
                                * CARD 13 ----- 8B804850
                                ORG  0      8B804860
                                XID  L B13  REAO TWD CARDS      8B80487D
                                XID  L K080D RESET DSW      8B804880
                                XID  L KDD03 SENSE DSW FOR 1442 8B804890
                                EDR  L KDD03 8B80490D
                                BSC  Z          SKIP BITS 14 & 15 ONLY 8B804910
                                LDX  E13  CDNTINUE DSW ANALYSIS 8B804920
                                LOX  A13  CARD IS BEING READ 8B804930
                                CON2 DC  CD15 8B804940
                                B13 DC  MDVE  READ CONTROL PROG. MODIFYS. 8B804950
                                DC  /1601 1442 8/8 FORMAT 8B80496D
                                C13 LDX /0D28 8B804970
                                D13 EOR L K080D RETURN DSW WORD TO ACC. 8B804980
                                WAIT /13 **ERR. SEE ACC. DSW NOT RIGHT 8B804990
                                LDX /D TRY AGAIN 8B805000
                                XID L K080D SENSE AND RESET DSW 8B805010
                                EOR L K080D CHECK FDR BIT 4 ONLY 8B805020
                                BSC Z          SKIP OPERATION COMPLETE 8B80503D
                                LDX D13 DSW ERROR CONDITION 8B805040
                                MOD1 LO L C13 SET THIS PROG. TO READ 2ND 8B805050
                                STO L MOD1 CARD & BR. TO READ IN 8B80506D
                                LD L CDN2 FIRST PROG. CARD 8B805070
                                STD L B13 8B80508D
                                LDX /0 8B805090
                                *                                8B805100
                                * CARD 14 ----- 8B805110
                                DRG  MOVE 8B805120
                                MOVE LD L /0025 GET ADDRESS FOR FIRST WORD 8B805130
                                BSC Z          SKIP ADDRESS EQU. 0000 8B805140
                                LDX STORE 8B805150
                                LDX /0000 8B805160
                                HOP LDX L CKMOV 8B805170
                                STORE STO L PUT&1 SET FIRST WORD ADDRESS 8B805180
                                LD L K0000 8B805190
                                STO L GET&1 SET TU GET FIRST WORD AT 0 8B805200
                                GET LD L /FFFF GET PROG. WORD 8B805210
                                PUT STO L /FFFF PUT PROG. WORD 8B805220
                                LD L PUT&1 MODIFY PUT 8B805230
                                A L K0001 8B805240
                                STD L PUT&1 8B805250
                                LD L GET&1 MOOIFY GET 8B805260
                                A L K0001 8B805270
                                STO L GET&1 8B805280
                                EOR L /0024 CHECK FOR ALL WORDS MOVED 8B805290
                                BSC Z          SKIP ALL WORDS MOVED 8B805300
                                LDX GET 8B805310
                                CD15 WAIT /14 **ERR. CARD 15 SHOULD READ 8B805320
                                * OVER THIS WAIT. 8B805330
                                *                                8B805340
                                * CARD 15 ----- 8B805350
                                DRG  CD15 8B805360
                                SUM2 LD L /0025 GET ADDRESS OF FIRST WORD 8B805370
                                STO L CKMOV&1 PUT IT INTO ROUTINE 8B805380
                                LD L K0000 GET ADDRESS OF FIRST WORD 8B805390
                                STO L COMP&1 IN IMAGE & STORE IT. 8B805400
                                CKMOV LD L /FFFF GET WORD MDVED 8B805410
                                CDMP EOR L /FFFF COMPARE WITH CARD IMAGE 8B805420
                                BSC Z          SKIP WORD STORED OK 8B805430
                                WAIT /15 **ERR. WORD NOT STORFD OK. 8B805440
                                LD L CKMOV&1 MODIFY FOR NEXT WORD 8B805450
```

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```
009A 0 B400 004E      A  L  K0001
009C 0 D400 0093      STD L  CKMDV&1
009E 0 C400 0095      LD  L  CDMPE1    MODIFY FOR NEXT CDMPE
00A0 0 B400 004E      A  L  K0001
00A2 0 D400 0095      STD L  CDMPE1
00A4 0 F400 0024      EDR L  /0024    CHECK IF ALL DONE
00A6 0 4820           BSC  Z          SKIP ALL WDRDS CHECKED
00A7 0 606E           LDX  HDP        GO TO CKMOV
00A8 0 6028           LDX  /0028     GET NEXT CARD
```

*
* CARD 16 -----
* THIS CARD IS USED TO CHECK THAT THE MOVE
* PORTION OF THE LOADER WORKS. THE CARD SHOULD
* BE PLACED IN LOCS. 0100 THRU 0123 .
* EACH LOCATION SHOULD CONTAIN ITS OWN ADDRESS.
* THE LISTING SHOWS THE SUM DURING CHECK SUM ADD.
*
* -SUM OF LOCS.-

```
00A9      ORG  /0100
0100 0 0100      DC  /0100      0100
0101 0 0101      DC  /0101      0201
0102 0 0102      DC  /0102      0303
0103 0 0103      DC  /0103      0406
0104 0 0104      DC  /0104      050A
0105 0 0105      DC  /0105      060F
0106 0 0106      DC  /0106      0715
0107 0 0107      DC  /0107      081C
0108 0 0108      DC  /0108      0824
0109 0 0109      DC  /0109      092D
010A 0 010A      DC  /010A      0937
010B 0 010B      DC  /010B      0942
010C 0 010C      DC  /010C      0A4E
010D 0 010D      DC  /010D      0B5B
010E 0 010E      DC  /010E      0C69
010F 0 010F      DC  /010F      0D7B
0110 0 0110      DC  /0110      0E88
0111 0 0111      DC  /0111      0F99
0112 0 0112      DC  /0112      10AB
0113 0 0113      DC  /0113      118E
0114 0 0114      DC  /0114      12D2
0115 0 0115      DC  /0115      13E7
0116 0 0116      DC  /0116      14FD
0117 0 0117      DC  /0117      1614
0118 0 0118      DC  /0118      1726
0119 0 0119      DC  /0119      183F
011A 0 011A      DC  /011A      1959
011B 0 011B      DC  /011B      1A76
011C 0 011C      DC  /011C      1B92
011D 0 011D      DC  /011D      1CAF
011E 0 011E      DC  /011E      1DCD
011F 0 011F      DC  /011F      1EEC
0120 0 0120      DC  /0120      200C
0121 0 0121      DC  /0121      212D
0122 0 0122      DC  /0122      224F
0123 0 0123      DC  /0123      2472
0124 0000      END  0
```

NO STATEMENTS FLAGGED IN THE ABOVE ASSEMBLY

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```
C R D S S  R E F E R E N C E
NAME  VALUE  REFERENCES
ADD    0006  0019
A1     0005  000B
A10    0004  000A
A13    0004  000A
A2     002C  0032
A3     0005
A3A    000F  0015
BIT00  0004
BIT01  000B  0007
BIT02  0011  000E
BIT03  0017  0014
BIT04  001D  001A
BIT05  0000
BIT06  0006  0003
BIT07  000C  0009
BIT08  0012  000F
BIT09  001B  0015
BIT10  0000
BIT11  0006  0003
BIT12  000C  0009
BIT13  0012  000F
BIT14  0018  0015
BIT15  0000
BLD    0047  000C
B1     000C  0001
B10    000C  0000
B13    000C  0000,001F
B2     0034  002A
B3     0016  0001,000B
CD15   008A  000B
CKLDD  0057  0051,005B,005F,0061,0064
CKMOV  0092  006E,00BC,0098,009C
CDMP    0094  0090,009E,00A2
CONTA  0012  000F
CONTB  001C  0015
CON2   000B  001D
C10    000B
C13    000E  0019
D1     0012  001B
D10    000E  0017
D13    000F  0018
D2     003A  0044
E1     0016  000A
E10    0012  0009
E13    0013  0009
E2     003E  0031
E3     001C  0014
GET     0076  0074,00B0,0084,0089
HFFFF  001E  0008
HDP     006E  00A7
H0000  001C  0000
H0001  001D  000E
KFFFF  0012  0006,0008
KDN1   000C  0000,0006
KDN2   0002  000A,0010
KD000  0009  0000
KD001  0005  0000
KD003  000E  0005,0007
KD800  0010  0003,0012,0016,0018
KD000  0011  000C
K0000  0019  000A,0012
K0001  001A  001C
K0003  0018  0005,000F,0011
K0000  004D  004F,0072,00BE
K0001  004E  005D,007C,0082,009A,00A0
K0002  001D  0018
```


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1. PURPOSE

THE 1800 BASIC DIAGNOSTIC LOADER IS A SELF-CHECKING PROGRAM USED TO LOAD THE PROCESSOR DIAGNOSTIC PROGRAMS AND TO VERIFY THEIR CORRECT LOADING. THE LOADER CONTAINS ONE-CARD PROGRAMS USED AS AIDS IN DIAGNOSIS OF BASIC FAILURES IN THE PROCESSOR.

2. PREREQUISITES

2.1 PROGRAM PREREQUISITES

AN 1800 PROCESSOR DIAGNOSTIC PROGRAM PUNCHED IN 8-8 FORMAT IS REQUIRED.

2.2 EQUIPMENT PREREQUISITES

- A. 1800 DATA ACQUISITION AND CONTROL SYSTEM PROCESSOR.
- B. 1442 SERIAL CARD READ/PUNCH.

3. OPERATING PROCEDURE

3.1 PROGRAM LOADING

A. AT 1442 SERIAL CARD READ/PUNCH,

- 1. DEPRESS NPRO PUSHBUTTON TO RUN OUT ANY CARDS REMAINING IN FEED.
- 2. PLACE BASIC LOADER DECK FOLLOWED BY MAIN PROGRAM AND ONE BLANK IN READER HOPPER.
- 3. DEPRESS START PUSHBUTTON. READY INDICATOR SHOULD LIGHT.

B. USING CONTROLS OF 1800 PROCESSOR CLEAR STORAGE TO
TOFF AS FOLLOWS,

- 1. SET MODE SWITCH TO RUN.
- 2. SET CHECK STOP SWITCH TO OFF.
- 3. SET WRITE STOR PROT BITS SWITCH TO YES.
- 4. SET DATA ENTRY SWITCHES TO 7DFF.
- 5. HOLD DOWN THE CLEAR STOR PUSHBUTTON AND DEPRESS START PUSHBUTTON TO CLEAR STORAGE.
- 6. DEPRESS STOP BUTTON TO TERMINATE CLEAR OPERATION.

C. AT 1800 PROCESSOR SET SWITCHES AS FOLLOWS,

- 1. SET CHECK STOP SWITCH TO ON.
- 2. SET WRITE STOR PROT BITS TO NO.

D. DEPRESS RESET PUSHBUTTON.

E. DEPRESS PROG LOAD PUSHBUTTON. CHECK THAT ONLY ONE CARD FEEDS.
POSSIBLE FAILURES FOLLOW.

FAILURE		FAILURE / ACTION	
* AFTER DEPRESSION OF		* REPEAT LOAD PROCEDURE WITH MODE SWITCH IN SI	
* PROG LOAD BUTTON NO		* POSITION. IF FAILURE REOCCURS A PROG LOAD FAILURE	
* CARD FEEDS, OR MORE		* IS POSSIBLE. FEEDING MORE THAN ONE CARD COULD ALSO	
* THAN ONE CARD FEEDS.		* BE CAUSED BY FAILURE TO PERFORM WAIT INSTRUCTION	
		* (B REG=30FF) THAT SHOULD HAVE READ INTO LOCATION	
		* 00DD.	

F. CHECK THAT PROGRAM HAS STOPPED WITH I REG=0001, AND B REG=3DFF.
POSSIBLE FAILURES FOLLOW.

FAILURE		FAILURE / ACTION	
* PROGRAM DOES NOT STOP		* IF LOCATION 0000 CONTAINS A WAIT INSTRUCTION (30FF),	
* WITH I REG=0001 AND		* EITHER THE WAIT OPERATION IS FAILING, OR PROG LOAD	
* B REG=3DFF.		* IS NOT SETTING THE I REGISTER TO 0000.	
* PROGRAM STOPS WITH		* THE FIRST WORD OF CARD 01 OF LOADER IS 30FF. FIND	
* I REG=0001, BUT B REG		* OUT WHY THE WORD WAS NOT READ INTO LOCATION 0000.	
* READING IS NOT 30FF.			

G. DEPRESS START PUSHBUTTON. CHECK THAT ALL LOADER AND PROGRAM CARDS FEED, AND THAT PROGRAM BEGINS EXECUTION. POSSIBLE FAILURES FOLLOW

FAILURE	FAILURE / ACTION
PROGRAM STOPS AT WAIT WITH I REG=0001, AND B REG=30FF.	CARD 02 PROGRAM READS A CARD WHICH REMOVES WAIT FROM LOCATION 0000. AN XIO FAILURE IS INDICATED. RUN CARD 03 TO HELP ISOLATE FAILURE. (PAR. 3.2.1).
PROGRAM STOPS AT ERROR WAIT. (B REG READING BETWEEN 3001 AND 3015).	REFER TO TABLE 1- ERROR WAIT DIAGNOSTIC GUIDE.
PROGRAM STOPS AT OTHER THAN A WAIT INSTRUCTION.	DETERMINE WHICH PROGRAM CAUSED FAILURE. IF MORE THAN TWO CARDS HAVE FED, FAILURE IS MOST LIKELY DUE TO LAST CARD READ.
PROGRAM RUNNING BUT CARDS DO NOT FEED.	STOP PROGRAM. DETERMINE WHICH PROGRAM IS ACTIVE. THE ACTIVE PROGRAM SHOULD BE STORED BETWEEN LOCATIONS 0000 AND 0025, OR BETWEEN LOCATIONS 0028 AND 004F. DISPLAY ACTIVE PROGRAM UNTIL A WAIT IS FOUND. DETERMINE THE PROGRAM NUMBER BY REFERENCING THE LAST 10 BITS OF THE WAIT INSTRUCTION. REFER TO LISTING FOR THE PROGRAM AND RUN IN SI MODE TO DETERMINE FAILURE. EXECUTION OF ONE-CARD PROGRAMS MAY ALSO BE HELPFUL. (PAR. 3.2.1 AND 3.2.2).

TABLE 1
ERROR WAIT DIAGNOSTIC GUIDE

WAIT B REGISTER I REGISTER	FAILURE / RECOMMENDED ACTION
3001	CHECK A REGISTER. IT CONTAINS THE 1442 DSW. IF THE DSW IS OTHER THAN 0003, OR 0000, THE DSW IS IN ERROR. DEPRESS PRDG LOAD BUTTON TO LOAD CARD 03 ONE-CARD PROGRAM. (CARD 03 IS AN XIO TEST PROGRAM.) SEE CARD 03 TEST PROCEDURE (PARAGRAPH 3.2.1).
3002	SAME FAILURE AS 3001. PRESS PRDG LOAD BUTTON TO LOAD CARD 03 TO HELP ISOLATE FAILURE. (REFER PARAGRAPH 3.2.1).

WAIT B REGISTER I REGISTER	FAILURE / RECOMMENDED ACTION
3003	THIS WAIT WILL NOT NORMALLY OCCUR WHILE LOADING A MAIN PROGRAM AS CARD 03 IS BYPASSED. REFER TO CARD 03 PROGRAM TEST PROCEDURE (PARAGRAPH 3.2.1).
3004	FAILURE OF LDX INSTRUCTION. REFER TO PROGRAM LISTING. RUN CARD 04 SEPARATELY. IF WAIT REOCCURS A SCOPING LOOP MAY BE SET UP BY REPLACING ERRDR WAIT BY AN LDX /0000 INSTRUCTION (6000).
3005 3006 3007 3008	0005 READ IN FAILURE FROM CARD READER, OR BIT TRANSFER INTO A REG FAILURE, OR BSC Z INSTRUCTION FAILURE. REFER TO LISTING. SET I REG TO ADDRESS OF LD INSTRUCTION JUST BEFORE WAIT INSTRUCTION AND STEP THROUGH PROGRAM IN SI MODE TO LOCATE FAILING INSTRUCTION.
3008	000C EOR OF ALL ONES AGAINST ALL ONES DID NOT RESULT IN A REG EQUAL 0000. REDEVELDP ERROR BY STARTING PROGRAM AT LOCATION 0000. STEP THROUGH IN SI MODE.
3008	0010 EDR OF ALL ZEROS AGAINST ALL ZEROS DID NOT RESULT IN A REG EQUAL 0000. REDEVELDP ERROR BY STARTING AT LOCATION 0000 IN SI MODE.
3009	LOAD LONG FAILURE, STORE LONG FAILURE, OR POSSIBLE EOR FAILURE. REFER TO LISTING. RUN IN SI MODE CHECKING THAT AFTER A LOAD INSTRUCTION A REG IS CORRECT, AND THAT AFTER A STORE INSTRUCTION THE A, B, AND M REGISTERS ARE CORRECT. DATA PATH TEST MAY ALSO HELP (SEE PARAGRAPH 3.5).
300A	0007 SRA 1 DROPPED THE 1 DURING THE SHIFT. FOLLOWING THIS WAIT, PROGRAM RUNS AGAIN THROUGH SAME CONDITIONS THAT CAUSED THE ERROR. STEP THROUGH IN SI MODE TO LOCATE FAILURE.
300A	0011 EOR OF A 1 IN STORAGE AGAINST A 0 IN A REG RESULTED IN A 0 IN A REG. RUN IN SI MODE TO LOCATE ERROR.

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WAIT		FAILURE / RECOMMENDED ACTION
B REGISTER	I REGISTER	
30DA	0D17	* EOR OF A 0 IN STORAGE AGAINST A 1 IN A REG RESULTED * IN A 0 IN A REG. RUN IN S1 MODE TO LOCATE ERROR.
300B		* ADD FAILURE. SUM OF SUMPL AND SUMMI NOT EQUAL 0000. * ERROR SUM IS IN A REG. DISPLAY SUMPL AND SUMMI, AND * DETERMINE IF THEIR SUM SHOULD BE 0000. IF THEIR SUM * SHOULD BE 0000, DIAGNOSE THE PROBLEM. IF THEIR SUM * SHOULD NOT BE 0000, EITHER SUMPL OR SUMMI IS IN * ERROR. RUN MANUAL ENTRY ADD TEST (PARAGRAPH 3.4).
3010		* ERROR OSM DETECTED. CHECK A REG. FAILURE COULD BE * IN THE 1442 READER, OR IN ITS ATTACHMENT CIRCUITRY, * OR COULD BE CAUSED BY INTERMITTENT PROCESSOR * FAILURES. RUN CARD 03 ONE-CARD PROGRAM.
3011	004B	* WORD COUNT OF CARD JUST READ IN WAS FOUND TO BE * 0000. WORD COUNT IS READ INTO LOCATION 0024. CHECK * CARD JUST READ. IT SHOULD HAVE INFORMATION PUNCHED * IN COLUMN 73. IF CORRECTLY PUNCHED, THE CARD CAN BE * RE-LOADED BY DEPRESSING THE START BUTTON ON THE * PROCESSOR CONSOLE. A READ-IN FAILURE IS POSSIBLE.
3011	006B	* SUM OF LOCATION 0000 THROUGH 0026 IS NOT 0000. * A REG CONTAINS THE DEVELOPED SUM. COMPARE THE * CARD READ WITH ITS IMAGE IN LOCATION 0000 THROUGH * 0027. IT MAY HAVE READ IN INCORRECTLY. THE SUM * ROUTINE MAY BE RUN BY STARTING AT LOCATION SUM1 * OF CARD 11. REFER TO LISTING. IF ERROR OCCURED ON * CARD 12, REFER TO ITS LISTING. CARD 12 IS USED * TO CHECK THE SUM ROUTINE.
3013		* CARD 13 PROGRAM HAS DETECTED AN ERROR OSM. THE ERROR * OSM IS IN THE A REG. ERROR COULD BE IN THE 1442 * READER, OR IN THE OSM CIRCUITRY, OR COULD BE CAUSED * BY AN INTERMITTENT FAILURE IN THE PROCESSOR. IF THE * SOURCE OF ERROR IS NOT EVIDENT, RUN ONE-CARD PRO- * GRAMS (CARDS 04 THROUGH 08).
3014		* THIS WAIT IS IN A LOCATION THAT SHOULD NOT BE * EXECUTED UNTIL CARD 15 IS READ. CARD 15 HAS ITS * FIRST WORD STORED IN THAT LOCATION BY CARD 13. THE * MOST LIKELY CAUSE OF THIS ERROR IS CARDS OUT OF * SEQUENCE. THE SEQUENCE NUMBER IS PUNCHED IN COLUMNS * 79 AND 80 IN HOLLERITH CODED HEXADECIMAL.

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WAIT		FAILURE / RECOMMENDED ACTION
B REGISTER	I REGISTER	
3015		* ERROR IN STORING THE PROGRAM FROM THE CARD IMAGE * AREA INTO ITS PROPER PLACE IN STORAGE. AFTER THE * NUMBER OF WORDS SPECIFIED BY THE WORD COUNT HAS * BEEN STORED, EACH STORED WORD IS COMPARED WITH ITS * CORRESPONDING IMAGE WORD TO CHECK FOR CORRECT * TRANSFER. THE FAILING ADDRESS CAN BE FOUND BY * REFERRING TO LISTING FOR CARD 15. (SWITCH TO * DISPLAY MODE AND LOOK AT THE ADDRESS POSITION OF * THE NEXT INSTRUCTION). * IF THE ERROR OCCURRED WHILE LOADING CARD 16, REFER * TO ITS LISTING. CARD 16 IS LOADED IN LOCATION 010D * AND ABOVE, AND IS DESIGNED TO AID IN DIAGNOSING * FAILURES IN THE ' MOVE ' SECTION OF THE LOADER.

3.2 ONE-CARD PROGRAMS TEST PROCEDURE

1800 BASIC DIAGNOSTIC LOADER CARDS 03 THROUGH 08 ARE THE ONE-CARD PROGRAMS. EXCEPT FOR CARD 03 WHICH IS BYPASSED, ONE-CARD PROGRAMS ARE EXECUTED BY THE LOADER IN THE NORMAL PROCESS OF BUILDING UP THE LOADER. EACH ONE-CARD PROGRAM CAN BE RUN INDIVIDUALLY BY LOADING INTO CORE STORAGE UNDER PROGRAM LOAD MODE.

PROVIDED NO ERRORS OCCUR, EACH ONE-CARD PROGRAM RUNS CONTINUOUSLY UNTIL STOPPED BY DEPRESSION OF STOP PUSH-BUTTON ON OPERATORS CONSOLE.

ERRORS ENCOUNTERED DURING EXECUTION ARE SIALEO BY PROGRAM STOPPING AT A UNIQUE ERROR WAIT WITH THE LAST 10 BITS OF B REGISTER CONTAINING THE PROGRAM NUMBER. FOR EXAMPLE, THE B REGISTER WILL HAVE A READING OF 300B IF CARD 0B PROGRAM STOPS AT AN ERROR WAIT. THE I REGISTER READING IS USED TO REFERENCE AN ERROR WAIT WHEN THERE IS MORE THAN ONE ERROR WAIT IN A PROGRAM.

3.2.1 CARD 03 PROGRAM TEST PROCEDURE

- EXECUTE ONE-CARD PROGRAMS 04 THROUGH 08 TO BECOME REASONABLY CERTAIN PROCESSOR IS OPERATING CORRECTLY. (PAR. 3.2.2).
- ON 1442 SERIAL CARD READ/PUNCH,
 - DEPRESS NPRO PUSHBUTTON TO CLEAR FEED.
 - PLACE CARD 03 FOLLOWED BY SUBSTANTIAL DECK OF BLANK CARDS IN MOPPER.
 - DEPRESS START PUSHBUTTON. READY INDICATOR SHOULD LIGHT.

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C. ON 1800 PROCESSOR OPERATOR'S CONSOLE.

1. SET CHECK STOP SWITCH TO ON.
2. DEPRESS RESET PUSHBUTTON.
3. DEPRESS PRG LOAD PUSHBUTTON. CARD 04 SHOULD FEED, LOAD, AND BEGIN EXECUTION.

D. PROGRAM SHOULD CAUSE ONE CARD TO FEED. CHECK THAT CARD HAS FED.

E. CHECK THAT PROGRAM HAS STOPPED AT WAIT WITH I REGISTER READING OF 0008, B REGISTER READING OF 3003, AND A REGISTER READING OF 0003.

IF PROGRAM DOES NOT STOP AT ABOVE MENTIONED WAIT, CARD 03 MAY NOT HAVE LOADED CORRECTLY. REFER TO PROGRAM LISTING AND DISPLAY EACH LOCATION.

IF A REGISTER IS NOT 0003 (CARD READER BUSY AND NOT READY) A DSW FAILURE IS INDICATED. REFER TO LISTING. SET UP APPLICABLE SCOPE LOOP TO AID IN DIAGNOSIS.

IF ALL REGISTERS MENTIONED ARE CORRECT DEPRESS START PUSHBUTTON.

F. CHECK THAT PROGRAM HAS STOPPED AT WAIT WITH I REGISTER READING OF 0008, B REGISTER READING OF 3003, AND A REGISTER READING OF 0800.

IF A REGISTER READING IS NOT 0800 (OP COMPLETE) AN ERROR DSW IS INDICATED. SET UP APPLICABLE SCOPE LOOP. UTILIZE AVAILABLE DIAGNOSTIC AIDS TO LOCATE THE PROBLEM. IF REGISTERS READ CORRECTLY AND IT IS DESIRED TO REPEAT STEPS D THROUGH F, DEPRESS RESET AND START PUSHBUTTONS IN SUCCESSION. IF NOT, PROCEED TO STEP G.

G. DEPRESS START PUSHBUTTON.

H. PROGRAM SHOULD FEED CARDS CONTINUOUSLY AND SHOULD NOT STOP UNLESS AN ERROR WAIT OCCURS WITH I REGISTER READING OF 0022, B REGISTER READING OF 3003. THIS WAIT STOP INDICATES THAT AN ERROR DSW HAS BEEN DETECTED. THE DSW IS DISPLAYED BY THE A REGISTER. REFER TO LISTING. DETERMINE THE DSW BITS THAT ARE IN ERROR. THERE ARE ONLY TWO LEGAL DSW READINGS, 0003 AND 0800. ANY OTHER DSW READINGS ARE CONSIDERED TO BE IN ERROR BY THE PROGRAM. SET UP APPLICABLE SCOPE LOOP.

SCOPING LOOPS MAY BE SET UP IN CARD 03 PROGRAM TO FACILITATE SCOPING OF XIO FUNCTIONS.

SCOPE LOOP SETUP

```
*****
* TO READ CARDS CONTINUOUSLY, INSERT LDX /0001 6001 , *
* AT LOCATION 0003.                                     *
*                                                       *
* TO READ CARD, SENSE AND RESET DSW, INSERT LDX /0001 *
* 6001 , AT LOCATION 0005.                             *
*                                                       *
* TO SENSE DSW CONTINUOUSLY WITHOUT CARD READING,     *
* INSERT LDX /0003 6003 , AT LOCATION 0005.           *
*****
```

BASIC DIAGNOSTIC LOADER (CARD)

3.2.2 CARD 04 - 08 PROGRAMS TEST PROCEDURE

THE FOLLOWING TEST PROCEDURE DESCRIPTION APPLIES TO ANY ONE-CARD PROGRAM FROM 04 TO 08.

A. ON 1442 SERIAL CARD READ/PUNCH,

1. DEPRESS NPRO PUSHBUTTON TO CLEAR FEED.
2. PLACE ONE-CARD PROGRAM FOLLOWED BY TWO BLANK CARDS IN HOPPER.
3. DEPRESS START PUSHBUTTON. READY INDICATOR SHOULD LIGHT.

B. ON 1800 PROCESSOR OPERATOR'S CONSOLE,

1. DEPRESS RESET PUSHBUTTON.
2. DEPRESS PRG LOAD PUSHBUTTON. CARD SHOULD FEED, LOAD, AND BEGIN EXECUTION.

THE PROGRAM WILL RUN CONTINUOUSLY UNLESS AN ERROR OCCURS, IN WHICH CASE PROGRAM STOPS AT ERROR WAIT. REFER TO PROGRAM LISTING AND TO TABLE 1 - ERROR WAIT DIAGNOSTIC GUIDE.

3.3 MANUAL ENTRY ADD TEST

THIS TEST HELPS LOCATE AN ADD FAILURE THAT CANNOT BE LOCATED WHEN RUNNING CARD 08 OF ONE-CARD PROGRAMS IN SI MODE BECAUSE OF THE DYNAMIC NATURE OF THE PROBLEM. IF THE CONTENTS OF SUMPL AND SUMHI DO NOT ADD TO 0000, THERE HAS BEEN A FAILURE IN ADDING 0001 TO SUMPL OR A FAILURE IN ADDING FFFF TO SUMHI. TO DETERMINE WHICH OF THE TWO SUMS IS IN ERROR, IT MUST BE ASSUMED THAT ONE OF THEM IS CORRECT IN ORDER TO ARRIVE AT THE VALUE OF THE OTHER PRIOR TO THE FAILURE. IN OTHER WORDS, TO DETERMINE VALUE OF SUMPL PRIOR TO FAILURE, IT MUST BE ASSUMED THAT PRESENT VALUE OF SUMHI IS CORRECT AND VICE VERSA.

EXECUTE ADD TEST PROGRAM AS FOLLOWS.

- A. OBTAIN VALUE OF SUMPL PRIOR TO FAILURE BY DETERMINING TWO'S COMPLEMENT OF (SUMHI - FFFF).
- B. OBTAIN VALUE OF SUMHI PRIOR TO FAILURE BY DETERMINING TWO'S COMPLEMENT OF (SUMPL - 0001).
- C. LOAD FOLLOWING PROGRAM BY MEANS OF CONSOLE ENTRY SWITCHES.

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NOTE

ALL NUMBERS SHOWN BELOW ARE IN HEXADECIMAL NOTATION.

LOCATION	CONTENTS	MNEMONIC	COMMENTS
0000	VALUE OF SUMPL PRIOR TO ERROR		WILL BE IN ACCUMULATOR WHEN ADD OCCURS.
0001	0001		WILL BE ADDED TO ACCUMULATOR DURING ADD.
0002	CORRECT SUM OF ADDITION		USED TO CHECK ADD OPERATION.
0003	COFC	LD	LOAD ACCUMULATOR FROM LOCATION 0000.
0004	80FC	A	ADD CONTENTS OF LOCATION 0001 TO ACCUMULATOR.
0005	FOFC	EOR	EOR ACCUMULATOR WITH CORRECT ANSWER.
0006	4B20	BSC Z	SKIP ON ZERO TO LOCATION 0008.
0007	3000	WAIT	WAIT. AN ERROR HAS OCCURED.
0008	6003	LOX	BRANCH TO LOCATION 0003.

D. LOAD I REGISTER WITH 0003.

E. RUN PROGRAM IN RUN MODE. AN ADD FAILURE WILL CAUSE PROGRAM TO STOP AT WAIT INSTRUCTION WITH I REGISTER INDICATOR INDICATING 0008.

F. IF PROGRAM RUNS CONTINUOUSLY WITHOUT ERRORS,

1. DEPRESS STOP PUSHBUTTON.

2. LOAD LOCATION 0000 WITH VALUE OF SUMMI PRIOR TO ERROR.

3. LOAD LOCATION 0001 WITH FFFF.

4. LOAD LOCATION 0002 WITH CORRECT SUM OF SUMMI PLUS FFFF.

5. RUN AGAIN IN RUN MODE.

3.4 MANUAL ENTRY DATA-PATH TEST

THIS TEST IS LOADED USING THE DATA ENTRY SWITCHES AND TESTS THE ABILITY OF THE 1800 PROCESSOR TO TRANSFER ONES AND ZEROS BETWEEN THE FOLLOWING REGISTERS.

A. FROM 8 TO 0 TO A TO M TO I REGISTER.

B. FROM A TO U TO A REGISTER.

C. FROM A REGISTER TO B REGISTER.

D. FROM I REGISTER TO B REGISTER.

E. FROM I REGISTER TO A REGISTER.

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TEST PROCEDURE

A. USING CONTROLS OF 1800 PROCESSOR, CLEAR STORAGE TO WAIT INSTRUCTION (33FF). SEE PARAGRAPH 3.1.

B. ENTER THE FOLLOWING PROGRAM USING DATA ENTRY SWITCHES.

LOCATION	CONTENTS	MNEMONIC	COMMENTS
FFFA	C006	LO	LOAD ACCUMULATOR WITH CONTENTS OF LOCATION 0001.
FFFB	44B0	BSI I	STORE CONTENTS OF I COUNTER (FFFF) AT ADDRESS STORED IN LOCATION FFF0. SET I COUNTER TO THAT ADDRESS AND ADD ONE TO I COUNTER.
FFFC	FFFO		ADDRESS POSITION OF BSI I INSTRUCTION
FFFD	FFFO		THIS IS THE ACTUAL BRANCH ADDRESS FOR THE BSI I INSTRUCTION AND IS REPLACED BY THE BSI I.
FFFE	D002	STO	STORE CONTENTS OF ACCUMULATOR AT LOCATION 0001 (SHOULD NOT CHANGE).
FFFF	COFC	LO	LOAD ACCUMULATOR WITH CONTENTS OF LOCATION FFFC.
0000	44B0	BSI I	STORE CONTENTS OF I COUNTER (0002) AT ADDRESS STORED IN LOCATION 0002. SET I COUNTER TO THAT ADDRESS AND ADD ONE TO I COUNTER.
0001	0002		THIS IS ADDRESS POSITION OF BSI I INSTRUCTION.
0002	0002		THIS IS THE ACTUAL BRANCH ADDRESS FOR THE BSI I INSTRUCTION AND IS REPLACED BY THE BSI I INSTRUCTION.
0003	D0F8	STO	STORE CONTENTS OF ACCUMULATOR AT LOCATION FFFC (SHOULD NOT CHANGE).
0004	70F5	MOX	BRANCH TO LOCATION FFFA.

C. LOAD I REGISTER WITH FFFA.

D. STEP THROUGH PROGRAM IN SI MODE, CHECKING THAT PROGRAM LOOPS PROPERLY. ANY DATA-PATH ERROR SHOULD RESULT IN THE IMPROPER BRANCHING OF A BSI I INSTRUCTION AND STOPPING AT A WAIT INSTRUCTION. THE LOCATION BEFORE THE WAIT INSTRUCTION SHOULD CONTAIN THE CONTENTS OF I REGISTER WHEN THE BRANCH OCCURRED. LOGICAL RECONSTRUCTION OF THE ERROR SHOULD ISOLATE A DATA-TRANSFER ERROR AND SUGGEST THE CIRCUIT CARD CAUSING THE ERROR.

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NOTE

A BRANCH OUTSIDE OF THE PROGRAM INTO A CORE LOCATION LOADED WITH 33FF INDICATES AN ERROR HAS OCCURRED. SUBTRACT TWO FROM I REGISTER INDICATOR READING AND DISPLAY THAT LOCATION. THE CONTENT OF LOCATION DISPLAYED IS THE I REGISTER SETTING WHEN THE ERRONEOUS BRANCH OCCURED. IF THE BRANCH WAS CAUSED BY A BSI I INSTRUCTION FAILURE, THE LOCATION JUST CHECKED WILL HAVE A VALUE, BY ONE, THAN THE ADDRESS OF THE SECOND WORD OF THE BSI I INSTRUCTION. IF THIS IS THE CASE, DISPLAY LOCATIONS WHERE PROGRAM IS STORED TO DETERMINE IF THE LOCATIONS HAVE CHANGED. THE ADDRESSES OF BSI I INSTRUCTION ARE STORED BY THE STO INSTRUCTIONS, AND THE LOCATIONS FFFD AND 00D2 ARE STORED BY THE BSI I INSTRUCTIONS. STATIC OR INTERMITTENT DATA-TRANSFER ERRORS SHOULD BE READILY DETECTED BY THIS MEANS, AND BE EASY TO ISOLATE BECAUSE OF THE UNIQUE FAILURE INDICATIONS.

ERRORS IN THE DATA PATH PROGRAM SHOULD BE CAUSED BY SINGLE BIT FAILURES, OR BY HALF-WORD FAILURES. THUS, DROPPED OR ADDED BITS CAN BE REFERENCED DIRECTLY TO A CIRCUIT CARD. SWAP INDICATED CIRCUIT CARD TO SEE IF FAILURE CHANGES.

THE Q, U, A, AND D REGISTERS CIRCUIT CARDS ARE LOCATED IN ROW 4 OF THE CARD GATE, AND ARE INTERCHANGEABLE.

THE I, B, AND M REGISTERS CIRCUIT CARDS ARE LOCATED IN ROW 6 OF THE CARD GATE, AND ARE INTERCHANGEABLE.

FALLING BIT- 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
COLUMN----- C D E F G H J K

THE FOLLOWING CARDS CONTROL HALF-WORD TRANSFERS AND ARE INTERCHANGEABLE.

M4, M5, M7, L5, AND L6.

PROGRAM DESCRIPTION

THE LD INSTRUCTION AT LOCATION FFFA PERFORMS THE FUNCTION OF SETTING THE ACCUMULATOR TO 0002 SO THAT WHEN THE FOLLOWING BSI I INSTRUCTION IS PERFORMED, A COMPLEMENT BIT PATTERN (FFFF) WILL BE SENT THROUGH THE A REGISTER, THUS TESTING THAT THE A REGISTER IS RETURNED TO 0002 AT THE END OF THE BSI I INSTRUCTION. THIS TEST IS ACCOMPLISHED BY STORING THE CONTENTS OF THE A REGISTER BACK INTO LOCATION D001 AFTER THE BSI I INSTRUCTION. THE SAME PHILOSOPHY IS USED DURING BSI I INSTRUCTION AT LOCATION 00DD BY SETTING THE A REGISTER TO FFFD WHILE 0002 IS SENT THROUGH IT DURING THE BSI I INSTRUCTION. A FAILURE OF EITHER BSI I INSTRUCTION THAT AFFECTS THE A REGISTER WILL CAUSE THE FOLLOWING BSI I INSTRUCTION TO TAKE ITS ADDRESS FROM THE WRONG LOCATION. THIS LOCATION WILL PROBABLY BE ONE OF THE CORE LOCATIONS LOADED WITH 33FF, THUS CAUSING THE PROGRAM TO STOP.

4. PRINTOUTS (NONE)

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BASIC DIAGNOSTIC LOADER (CARD)

5. COMMENTS

5.1 BASIC DIAGNOSTIC LOADER PHILOSOPHY

THE 1800 BASIC DIAGNOSTIC LOADER IS A SELF-CHECKING PROGRAM USED TO LOAD THE PROCESSOR DIAGNOSTIC PROGRAMS, AND TO VERIFY THEIR CORRECT LOADING. THE LOADER CONTAINS ONE-CARD PROGRAMS USED AS AIDS IN DIAGNOSIS OF BASIC FAILURES IN THE PROCESSOR. THESE ONE-CARD PROGRAMS NUMBERED 03 THROUGH 0B IN COLUMNS 79 AND 80 ARE NORMALLY EXECUTED WHILE IN THE PROCESS OF LOADING AND BUILDING OF THE LOADER. CARD 03 IS BYPASSED.

TABLE 3 CONTAINS A BREAKDOWN OF THE 1800 BASIC DIAGNOSTIC LOADER. CARD NUMBER, LOCATION IN STORAGE AND FUNCTION OF EACH CARD ARE SHOWN.

THE FINAL LOADER USED TO LOAD THE MAIN PROGRAM CONSISTS OF THE COMBINED PROGRAMS OF CARDS 02, 11, 14 AND 15. CARDS 01, 10, 12, 13, AND 16 ARE USED IN THE PROCESS OF BUILDING AND CHECKING THE LOADER. CARDS 03 THROUGH 0B ARE ONE-CARD PROGRAMS USED TO CHECK SPECIFIC PROCESSOR FUNCTIONS.

THE FINAL LOADER PERFORMS THE FOLLOWING FUNCTIONS.

- READS A CARD INTO LOCATION 0000 TO 0D27.
- CONTINUOUSLY CHECKS DSW WAITING FOR AN OP COMPLETE. ANY ERROR DSW IS SIGNALLED BY PROGRAM STOPPING AT A SPECIFIED ERROR WAIT INSTRUCTION.
- CHECKS WORD COUNT AFTER OP COMPLETE IS RECEIVED. WORD COUNT MUST NOT BE ZERO. IF A WORD COUNT OF ZERO IS DETECTED PROGRAM STOPS AT ERROR WAIT.
- FORMS CHECK SUM OF LOCATIONS 0D00 THROUGH 0026. THE DEVELOPED CHECK SUM MUST BE 0D00 OR PROGRAM STOPS AT ERROR WAIT.
- CHECKS LOCATION 0025 FOR STARTING ADDRESS WHERE WORDS MUST BE STORED INTO. IF THE ADDRESS IS 0D00 PROGRAM ASSUMES LAST CARD OF PROGRAM HAS BEEN READ AND CONSEQUENTLY BRANCHES TO LOCATION 0D0D TO BEGIN EXECUTION OF MAIN PROGRAM. IF THE ADDRESS IS NOT 0D0D PROGRAM MOVES THE NUMBER OF WORDS SPECIFIED BY THE WORD COUNT FROM IMAGE AREA (0000 - 0D27) TO ADDRESS SPECIFIED IN LOCATION 0025, AND ABOVE.
- COMPARES (EOR) EACH WORD MOVED FROM IMAGE AREA WITH THE CORRESPONDING WORD AT THE NEW LOCATION. FAILURE OF ANY ONE WORD TO COMPARE RESULTS IN ERROR WAIT INDICATING A TRANSFER ERROR.
- REPEATS ENTIRE PROCEDURE FOR EVERY CARD.

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0 0

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

2400 TIMING TEST

PART NO. 2196491
PAGE 1

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

2400 TIMING TEST

PART NO. 2196491
PAGE 1A

```
3001      ABS
          ORG   /3001
          *
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
3001 0 014C      OC      WAIT1&1      WAIT FOR DATA ENTRY
          *
          *          SWITCHES TO BE SET.
          *          PUSH START TO
          *          CONTINUE THE PROGRAM.
          *
          *****
3002 0 03B8      DC      WAIT2&1      WAIT BEFORE ROUTINE,
          *
          *          TERMINATE PROGRAM OR
          *          HALT ON ERROR. IF
          *          HALT ON ERROR OR
          *          TERMINATE PROGRAM,
          *          A PRINTOUT WILL OCCUR
          *          BEFORE THE WAIT.
          *          PUSH START TO CONTINUE
          *          OR RESTART.
          *
          *****
3003 0 047B      DC      WAIT3&1      1443 PRINTER IS NOT
          *
          *          READY. MAKE PRINTER
          *          READY AND PUSH START.
          *
          *****
3004 0 0627      DC      WAIT4&1      WAIT BECAUSE TYPE-
          *
          *          WRITER IS NOT READY.
          *          MAKE TYPEWRITER READY
          *          AND PUSH START TO
          *          CONTINUE PROGRAM.
          *
          *****
3005 0 07A8      OC      WAIT5&1      LOST INTRPT. AFTER
          *
          *          TIMEO WRT DR RD.
          *
          *****
3006 0 07DF      OC      WAIT6&1      LOST INTRPT. AFTER BSP
          *
          *          *****
3007 0 3007      DC      /3007      NDT USED
3008 0 3008      OC      /3008      NOT USED
          *
          *****
          *
          *          ON ALL LOST INTERRUPT
          *          WAITS, PUSH RESET
          *          AND START TO RESTART.
          *
          *****
          *
          *****
3009 0 0371      DC      WAIT9&1      NO LEGAL OSW BIT ON
          *
          *          AT INTERRUPT. PUSH
          *          RESET AND START TO
          *          RESTART PROGRAM.
          *
          *****
          *
          *****
300A 0 0373      OC      WAITA&1      BLANK ILSW AT
          *
          *          INTERRUPT. PUSH
          *          RESET AND START
          *          TO RESTART THE PROGRAM.
          *
          *****
          *
          *****
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
```

```
8B900020
8B900030
8B900040
8B900050
8B900060
8B900070
8B900080
8B900090
8B900100
8B900110
8B900120
8B900130
8B900140
8B900150
8B900160
8B900170
8B900180
8B900190
8B900200
8B900210
8B900220
8B900230
8B900240
8B900250
8B900260
8B900270
8B900280
8B900290
8B900300
8B900310
8B900320
8B900330
8B900340
8B900350
8B900360
8B900370
8B900380
8B900390
8B900400
8B900410
8B900420
8B900430
8B900440
8B900450
8B900460
8B900470
8B900480
8B900490
8B900500
8B900510
8B900520
8B900530
8B900540
8B900550
8B900560
8B900570
8B900580
8B900590
8B900600
8B900610
8B900620
8B900630
8B900640
8B900650
8B900660
8B900670
8B900680
8B900690
```

```
012C
012C 0 B900
012D 0 6700 0132
012F 0 6F00 0124
0131 0 6050
0132 0 63F9
0133 0 C30A
0134 0 0700 02ED
0136 0 7301
0137 0 70FB
0138 0 6700 0130
013A 0 6F00 0124
013C 0 6050
013D 0 631A
013E 0 C00F
013F 0 0700 0008
0141 0 73FF
0142 0 70FC
0143 0 CC00 02D8
0145 0 0C00 0000
0147 0 C400 02EE
0149 0 0480 02E7
014B 0 3001
014C 0 4C00 02EF
014E 0 0332
```

```
014F 0 0000
0150 0 6373
0151 0 C700
0153 0 D700 066C
0155 0 73FF
0156 0 70FA
0157 0 7073
0158 0 0308
0159 0 0712
015A 0 0308
015B 0 048B
015C 0 0080
015D 0 012F
015E 0 0080
015F 0 00C3
0160 0 00F8
0161 0 0131
0162 0 00F5
0163 0 012E
0164 0 01E0
0165 0 0003
0166 0 0040
0167 0 0002
0168 0 7100
0169 0 0001
016A 0 04C0
016B 0 0001
016C 0 3880
016D 0 0000
016E 0 9C40
016F 0 0000
0170 0 4E20
0171 0 0000
0172 0 3E80
```

```
*
DRG      300      PIO
DC      /B900
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
BEGN      LOX      L3      BEGN1      IX 3 # LOR RETURN
          STX      L3      /0124      STORE IN LOR
          LOX      X      /0050      GO TO LDA0ER
BEGN1      LOX      3      -7      IX # NO ENTRIES
BEGN2      LD      3      10      GET AN ENTRY
          STD      L3      EDIT&9      SET IN EDIT FIELD
          MOX      3      1      OECR IX
          MOX      BEGN2      LDOP
          LOX      L3      BEGAP      IX 3 # LDR RETURN
          STX      L3      /0124      STORE IN LOADER
          LDX      X      /0050      GO TO LDA0ER
BEGAP      LOX      3      26      IX # NO LVLS
          LD      BEGX3      GET COMMON INTR TRAP
BEGBA      STD      L3      8      SET
          MDX      3      -1      DECR IX REG
          MDX      BEGBA      LDOP
          LOD      L      BEGX4      SET RESTART
          STD      L      0      *
          LD      L      ACTI&1      GET INTRPT RTN ADRS
          STD      I      EDIT&3      SET FOR TAPES
          WAIT      WAIT      1      WAIT FOR SWS
          BSC      L      MQNT
          BEGX3      OC      SVINT      COMMON INTR TRAP
          *
          *          RESTORE PROGRAM TO 4
          *          MICROSEC MEM AND MOD 3 DRS
          *
          BEGIN      DC      0
          LOX      3      115      SE
          BEGAN      LO      L3      BEGX8-1      GET BASE CONSTANT
          STO      L3      CDN-1      SET
          MDX      3      -1      OECR IX
          MOX      BEGAN      LOOP
          MOX      BEGAC
          BEGX8      DC      776      RESTORE CONSTANTS
          OC      1810
          DC      776
          OC      1163
          DC      128
          OC      303
          OC      128
          DC      195
          OC      248
          OC      305
          OC      245
          OC      302
          OC      480
          DC      3
          DC      3392
          DC      2
          DC      28928
          DC      1
          DC      54464
          DC      1
          OC      14464
          OC      0
          OC      40000
          DC      0
          DC      20000
          OC      0
          DC      16000
```

```
8B900700
8B900710
8B900720
8B900730
8B900740
8B900750
8B900760
8B900770
8B900780
8B900790
8B900800
8B900810
8B900820
8B900830
8B900840
8B900850
8B900860
8B900870
8B900880
8B900890
8B900900
8B900910
8B900920
8B900930
8B900940
8B900950
8B900960
8B900970
8B900980
8B900990
8B901000
8B901010
8B901020
8B901030
8B901040
8B901050
8B901060
8B901070
8B901080
8B901090
8B901100
8B901110
8B901120
8B901130
8B901140
8B901150
8B901160
8B901170
8B901180
8B901190
8B901200
8B901210
8B901220
8B901230
8B901240
8B901250
8B901260
8B901270
8B901280
8B901290
8B901300
8B901310
8B901320
8B901330
8B901340
8B901350
8B901360
8B901370
```

DATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
EC NO. 415178 415233 411731 411857 411875 431319 431319A

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DATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
EC NO. 415178 415233 411731 411857 411875 431319 431319A

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2400 TIMING TEST

0173 0 0000	DC	0
0174 0 2EE0	DC	12000
0175 0 0000	DC	0
0176 0 1F40	DC	8000
0177 0 0000	DC	0
0178 0 0FA0	DC	4000
0179 0 0000	DC	0
017A 0 0C80	DC	3200
017B 0 0000	DC	0
017C 0 0960	DC	2400
017D 0 0000	DC	0
017E 0 0640	DC	1600
017F 0 0000	DC	0
0180 0 0578	DC	1400
0181 0 0000	DC	0
0182 0 0480	DC	1200
0183 0 0000	DC	0
0184 0 03E8	DC	1000
0185 0 0000	DC	0
0186 0 0384	DC	900
0187 0 0000	DC	0
0188 0 0320	DC	800
0189 0 0000	DC	0
018A 0 02BC	DC	700
018B 0 0000	DC	0
018C 0 0258	DC	600
018D 0 0000	DC	0
018E 0 01F4	DC	500
018F 0 0000	DC	0
0190 0 0190	DC	400
0191 0 0000	DC	0
0192 0 0168	DC	360
0193 0 0000	DC	0
0194 0 0140	DC	320
0195 0 0000	DC	0
0196 0 0118	DC	280
0197 0 0000	DC	0
0198 0 00F0	DC	240
0199 0 0000	DC	0
019A 0 00DC	DC	220
019B 0 0000	DC	0
019C 0 00C8	DC	200
019D 0 0000	DC	0
019E 0 0084	DC	180
019F 0 0000	DC	0
01A0 0 00A0	DC	160
01A1 0 0000	DC	0
01A2 0 008C	DC	140
01A3 0 0000	DC	0
01A4 0 0078	DC	120
01A5 0 0000	DC	0
01A6 0 006E	DC	110
01A7 0 0000	DC	0
01A8 0 0064	DC	100
01A9 0 0000	DC	0
01AA 0 005A	DC	90
01AB 0 0000	DC	0
01AC 0 0050	DC	80
01AD 0 0000	DC	0
01AE 0 0046	DC	70
01AF 0 0000	DC	0
01B0 0 003C	DC	60
01B1 0 0000	DC	0
01B2 0 0032	DC	50
01B3 0 0000	DC	0
01B4 0 002C	DC	44
01B5 0 0000	DC	0
01B6 0 0028	DC	40

88901380
88901390
88901400
88901410
88901420
88901430
88901440
88901450
88901460
88901470
88901480
88901490
88901500
88901510
88901520
88901530
88901540
88901550
88901560
88901570
88901580
88901590
88901600
88901610
88901620
88901630
88901640
88901650
88901660
88901670
88901680
88901690
88901700
88901710
88901720
88901730
88901740
88901750
88901760
88901770
88901780
88901790
88901800
88901810
88901820
88901830
88901840
88901850
88901860
88901870
88901880
88901890
88901900
88901910
88901920
88901930
88901940
88901950
88901960
88901970
88901980
88901990
88902000
88902010
88902020
88902030
88902040
88902050

2400 TIMING TEST

01B7 0 0000
01B8 0 0024
01B9 0 0000
01BA 0 0020
01BB 0 0000
01BC 0 001C
01BD 0 0000
01BE 0 0018
01BF 0 0000
01C0 0 0016
01C1 0 0000
01C2 0 0014
01C3 0 03C0
01C4 0 0780
01C5 0 4092
01C6 0 0003
01C7 0 0000
01C8 0 0025
01C9 0 0048
01CA 0 0070

OC	0
OC	36
OC	0
OC	32
DC	0
DC	28
DC	0
DC	24
DC	0
DC	22
DC	0
DC	20
DC	960
DC	1920
DC	19858
DC	3
DC	0
DC	37
DC	75
OC	112

88902060
88902070
88902080
88902090
88902100
88902110
88902120
88902130
88902140
88902150
88902160
88902170
88902180
88902190
88902200
88902210
88902220
88902230
88902240
88902250
88902260
88902270
88902280
88902290
88902300
88902310
88902320
88902330
88902340
88902350
\$ 88902360
\$ 88902370
\$ 88902380
\$ 88902390
\$ 88902400
\$ 88902410
\$ 88902420
\$ 88902430
\$ 88902440
\$ 88902450
\$ 88902460
\$ 88902470
\$ 88902480
\$ 88902490
\$ 88902500
\$ 88902510
\$ 88902520
\$ 88902530
\$ 88902540
\$ 88902550
\$ 88902555
\$ 88902560
\$ 88902570
\$ 88902580
\$ 88902590
\$ 88902600
\$ 88902610
\$ 88902620
\$ 88902630
\$ 88902640
\$ 88902650
\$ 88902660
\$ 88902670
\$ 88902680
\$ 88902690
\$ 88902700
\$ 88902710
\$ 88902720

```
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXX EDIT ROUTINE XXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
*
*          SET PROGRAM TO MEM SPEED
*
BEGAC LD L EDIT&8      GET MEM SPEED
      BSC L BEGAD-1,Z-  BR IF 4.00 US
      LDX 3 111
      SLT 32
      LD L3 SPEC        SETUP
      SRT 2              CONSTANTS
      M ARIA1+1          FOR
      D ARIA1+2          2.25 US MEMORY
      SLA 2
      STO L3 SPEC
      MDX 3 -1
      MDX BAK
      LD ARIA1+3
      STO L MLGX7
      MDX PART2          BR TO CONT. SETUP
      DC /FFFF          X
      DC /000F          T
      DC /0011          N
      DC /0025          T
      DC /FFFF          S
*
PART2 LDD ARIA5          SETUP
      STD L MT5X0        SPECIAL
      LDD ARIA5+2        DOUBLE
      STD L MT5X0+2      WORD
      LDD ARIA5+4        CONSTANTS
      STD L MT5X0+4
      LDD ARIA5+6
      STD L MT5X0+6
      LD ARIA5+8
      STO L GPHLM
      LDX L BEGAF
      BSS E 0
      DC /0002          D C
      DC /B150          O W O
      DC /0002          U O N
      DC /2778          B R S
      DC /0001          L D T
```



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PAGE 3

2400 TIMING TEST

01FD 0 9098	DC	/9098	E	A	\$ 88902730
01FE 0 0001	DC	/0001	N		\$ 88902740
01FF 0 1388	DC	/1388	T		\$ 88902750
0200 0 0002	DC	/0002	S		\$ 88902760
0201 0 FFFF	DC	/FFFF			\$ 88902770
					\$ 88902775
					\$ 88902780
0202 0 636F	LDX	3 111			88902790
0203 0 C700 066C	BEGAD	LD L3 CON-1	GET A CONSTANT		88902800
0205 0 1801	SRA	1	DVD BY 2		88902810
0206 0 0700 066C	STO	L3 CON-1	SET		88902820
0208 0 73FF	MOX	3 -1	OECR IX		88902830
0209 0 70F9	MOX	BEGAD	LOOP		88902840
020A 0 C400 066B	LD	L CONV1	GET CONV CONSTANT		88902850
020C 0 0400 060C	STO	L MLGX7	SET		88902860
020E 0 7004	MOX	BEGAF			88902870
					88902880
					88902890
					88902900
					88902910
					88902920
					88902930
					88902940
					88902950
					88902960
					88902970
					88902980
					88902990
					88903000
					88903010
					88903020
					88903030
					88903040
					88903050
					88903060
					88903070
					88903080
					88903090
					88903100
					88903110
					88903120
					88903130
					88903140
					88903150
					88903160
					88903170
					88903180
					88903190
					88903200
					88903210
					88903220
					88903230
					88903240
					88903250
					88903260
					88903270
					88903280
					88903290
					88903300
					\$ 88903310
					\$ 88903320
					\$ 88903330
					\$ 88903340
					\$ 88903350
					\$ 88903360
					\$ 88903370
					\$ 88903380
					\$ 88903390

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PAGE 3A

2400 TIMING TEST

0255 0 0400 0678	STO	L	MT5XA+3	MOD 1	\$ 88903400
0257 0 C008	LD		ARIA2+4	TAPE	\$ 88903410
0258 0 0400 06DB	STO	L	GPHLM	CONSTANTS	\$ 88903420
025A 0 4C00 0205	BSC	L	BEGAK	BR TO EXIT SETUP	\$ 88903430
					\$ 88903440
025C 0 029C	ARIA2	DC	/029C		\$ 88903450
025D 0 0310	DC		/0310	MOD 1	\$ 88903460
025E 0 0294	DC		/0294	TAPE	\$ 88903470
025F 0 0308	DC		/0308	CONSTANTS	\$ 88903480
0260 0 0008	DC		/0008	2.25 US	\$ 88903490
0261 0 FFFF	DC		/FFFF		\$ 88903500
0262 0 7402 0677	FWRD	MDX	L MT5XA+2,2	ADJ GRPH LIMITS	\$ 88903510
0264 0 74FF 0678	MDX	L	MT5XA&3,-1	*	\$ 88903520
0266 0 7401 0608	MDX	L	GPHLM,1	*	\$ 88903530
0268 0 706C	MDX		BEGAK	BR TO EXIT	\$ 88903535
					\$ 88903540
0269 0 C400 02EC	BEGAM	LD	L E0IT+8	GET P/C SPEED XTNT	\$ 88903550
026B 0 4828	BSC		Z+	SKIP IF NEG.	\$ 88903560
026C 0 7003	MOX		*+3	BR IF NOT 2.25 US	\$ 88903570
026D 0 C400 06DB	LD	L	GPHLM		\$ 88903580
026F 0 7017	B		FWD1	SETUP	\$ 88903590
0270 0 C010	LD		ARIA3	MOD 3	\$ 88903600
0271 0 0400 0675	STO	L	MT5XA	TAPE	\$ 88903610
0273 0 C00E	LD		ARIA3+1	CONSTANTS	\$ 88903620
0274 0 0400 0676	STO	L	MT5XA+1	FOR	\$ 88903630
0276 0 C00C	LD		ARIA3+2	2.25 US P/C	\$ 88903640
0277 0 0400 0677	STO	L	MT5XA+2		\$ 88903650
0279 0 C00A	LD		ARIA3+3		\$ 88903660
027A 0 0400 0678	STO	L	MT5XA+3		\$ 88903670
027C 0 C008	LD		ARIA3+4		\$ 88903680
027D 0 0400 0608	STO	L	GPHLM		\$ 88903690
027F 0 4C00 0205	BSC	L	BEGAK	BR TO EXIT	\$ 88903700
					\$ 88903710
0281 0 0000	ARIA3	OC	/0000		\$ 88903720
0282 0 00FB	DC		/00FB	MOD 3	\$ 88903730
0283 0 00CE	OC		/00CE	TAPE	\$ 88903740
0284 0 00F9	DC		/00F9	CONSTANTS	\$ 88903750
0285 0 0002	DC		/0002		\$ 88903755
0286 0 FFFF	OC		/FFFF		\$ 88903760
					\$ 88903770
0287 0 1801	FWD1	SRA	1	CK FOR 1	\$ 88903780
0288 0 4C20 0205	BSC	L	8EGAK,Z	BR # NOT 1	\$ 88903790
028A 0 7401 06DB	MDX	L	GPHLM,1	SET TO 2	\$ 88903800
028C 0 7407 0676	MDX	L	MT5XA&1,7	A0J GPH LMTS	\$ 88903810
028E 0 7406 0678	MDX	L	MT5XA&3,6		\$ 88903820
0290 0 7044	MDX		8EGAK		\$ 88903830
					\$ 88903840
					\$ 88903850
					\$ 88903860
					\$ 88903870
					\$ 88903880
					\$ 88903890
					\$ 88903900
					\$ 88903910
					\$ 88903920
					\$ 88903930
					\$ 88903940
					\$ 88903950
					\$ 88903960
					\$ 88903970
					\$ 88903980
					\$ 88903990
					\$ 88904000
					\$ 88904010
					\$ 88904020
					\$ 88904030
					\$ 88904040
					\$ 88904050

2400 TIMING TEST

02AD	0	1801		SRA	1	MUL BY .5		88904060	
02AE	0	8700	0670	A	L3	CON1-1	MUL BY 1	88904070	
02B0	0	D700	0670	STD	L3	CON1-1	SET	88904080	
02B2	0	73FF		MDX	3	-1	OECR IX	88904090	
02B3	0	70F7		MDX		BEGAS	LOOP	88904100	
02B4	0	7401	0677	MDX	L	MT5XA&2,1	ADJ GRPH LMT	88904110	
02B6	0	C035		L0		E01T&8	GET MEM SPEED	88904120	
02B7	0	4C18	0201	BSC	L	FWD2,+--	BR IF 2.00 US P/C	\$ 88904130	
02B9	0	4C10	02D5	BSC	L	BEGAK,-	BR IF 4.00 US P/C	\$ 88904140	
02B8	0	C00F		L0		ARIA4		\$ 88904150	
02BC	0	0400	0675	STD	L	MT5XA	SETUP	\$ 88904160	
02BE	0	C000		LD		ARIA4+1	FOR	\$ 88904170	
02BF	0	0400	0676	STD	L	MT5XA+1	2.25 US P/C	\$ 88904180	
02C1	0	C00B		LD		ARIA4+2	AND	\$ 88904190	
02C2	0	0400	0677	STD	L	MT5XA+2	MOO 2	\$ 88904200	
02C4	0	C009		L0		ARIA4+3	TAPE	\$ 88904210	
02C5	0	0400	0678	STD	L	MT5XA+3	CONSTANTS	\$ 88904220	
02C7	0	C007		L0		ARIA4+4		\$ 88904230	
02C8	0	0400	0679	STD	L	MT5XA+4		\$ 88904240	
02CA	0	700A		MOX		BEGAK	BR TO EXIT	\$ 88904250	
02CB	0	0148		DC		/0148		\$ 88904260	
02CC	0	0194		OC		/0194	MOD 2	\$ 88904270	
02CD	0	0144		OC		/0144	TAPE	\$ 88904280	
02CE	0	0190		OC		/0190	CONSTANTS	\$ 88904290	
02CF	0	0004		OC		/0004		\$ 88904300	
0200	0	FFFF		OC		/FFFF		\$ 88904310	
				*				\$ 88904315	
0201	0	74FF	0678	FW02	MOX	L	MT5XA+3,-1	ADJ GPH CNST	\$ 88904320
02D3	0	7401	060B		MDX	L	GPHLM,1	*	88904330
0205	0	4C80	014F	BEGAK	BSC	I	BEGIN	EXIT	88904340
				*					88904350
				*			GO TO PRDG INITILIZATION		88904360
				*					88904370
				*					88904380
				*					88904390
				*			CONSTANTS		88904400
				*					88904410
0208	0	0000		BSS	E	0			88904420
02D8	0	4C00	0130	BEGx4	BSC	L	BEGAP	RESTART CONSTANT	88904430
020A	0	02E1		BEGx5	OC		SW0	10CC-READ BIT SWS	88904440
020B	0	0240			DC		/0240		88904450
020C	0	02E2		BEGx7	OC		SW1	READ PRDG SWS IDCC	88904460
0200	0	0260			OC		/0260		88904470
				*					88904480
				*			XX		88904490
				*			XXXXXXXXXXXXXXXXXXXX PROGRAM STATUS TABLE XXXXXX		88904500
				*			XX		88904510
				*					88904520
020E	0	B900		P10	OC		/B900		88904530
020F	0	0000		R10	OC		0	ROUTINE NUMBER	88904540
02E0	0	0000		RA0	OC		0	ROUTINE ADDRESS	88904550
02E1	0	0000		SW0	OC		0	SWITCH ENTRY 1	88904560
02E2	0	0000		SW1	OC		0	SWITCH ENTRY 2	88904570
02E3	0	FFFF		TERM	OC		/FFFF	TERMINATOR	88904580
02E4	0	7000		E01T	OC		/7000	TAPE AREA CODE	88904590
02E5	0	3000			OC		/3000	1443 AREA CODE	88904600
02E6	0	0000			OC		0	ILSW BIT -TAPES	88

		*				88904730	
		*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX				88904740	
		*XXXXXXXXXXXXXXXXXX INITIALIZATION ROUTINE XXXX				88904750	
		*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX				88904760	
		*				88904770	
02EF	0	630B	MONT	LXD	3 11	CLEAR DST TABLE	88904780
02F0	0	1010		SLA	16		88904790
02F1	0	D700	06DF	MONT1	STO L3 OST-1		88904800
02F3	0	73FF		MDX	3 -1		88904810
02F4	0	70FC		MDX	MONT1		88904820
		*					88904830
		*			SET NECESSARY VALUES		88904840
		*					88904850
				SLA	16		88904860
02F5	0	1010		STO	R10	CLEAR RTN NO	88904870
02F6	0	D0E8		STO	L PGSW	CLEAR PROG SW	88904880
02F7	0	0400	093B	LO	EDIT	GET TAPE AREA CODE	88904890
02F9	0	C0EA		STD	ACT1	SET	88904900
02FA	0	D0F2		STO	L OST&8		88904910
02FB	0	0400	06E8	XID	UNMK3	UNMASK ALL LEVELS	88904920
02FD	0	0804		XID	UNMK4		88904930
02FE	0	0805		B5I	RDSWS	READ SWS	88904940
02FF	0	4006		BSC	L MONT4	BRANCH	88904950
0300	0	4C00	06F0	BSS	E 0		88904960
0302	0	0000		UNMK3	OC 0	IDCC-UNMASK LOWER	88904970
0302	0	0000		DC	/0480		88904980
0303	0	0480		UNMK4	DC 0	IDCC-UNMASK UPPER	88904990
0304	0	0000		DC	/0481		88905000
0305	0	0481		*			88905010
				RDSWS	DC 0		88905020
0306	0	0000		XIO	BEGX5	READ DATA SWS	88905030
0307	0	08D2		XIO	BEGX7	READ PROG SWS	88905040
0308	0	08D3		LD	SW1	AQJ PROG SWS	88905050
0309	0	C0D8		SLA	5		88905060
030A	0	1005		SRA	13		88905070
030B	0	1800		STO	SW1	*	88905080
030C	0	D0D5		BSC	I ROSWS	EXIT	88905090
0300	0	4C80	0306				88905100
				*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX			88905110
				*XXXXXXXXXXXXXXXXXXX INTERRUPT ROUTINE XXXXXXXXXX			88905120
				*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX			88905130
				*			88905140
030F	0	6600	0000	TAX1	LOX L2 0	RESTORE IX	88905150
0311	0	C856		LOO	TAAQ	RESTORE A ANO Q	88905160
0312	0	4C40	0000	INTR3	BOSC L 0	ENTRY ANO EXIT	88905170
0314	0	6AFB		STX	2 TAX1&1	SAVE IX 2	88905180
0315	0	0852		STO	TAAQ	SAVE A ANO Q	88905190
0316	0	084F		XIO	ILSW	SENSE ILSW BIT	88905200
0317	0	4418	0371	B5I	L TERR,&-	BRANCH ON BLANK ILSW	88905210
0319	0	E0CC		ANO	E0IT&2	CK IF TAPE	88905220
031A	0	4C20	031E	BSC	L INTRR,Z	BRANCH # TAPE	88905230
031C	0	4015		B5I	SVINT	BRANCH-NOT TAPE	88905240
0310	0	70F1		MOX	TAX1	GO EXIT	88905250
031E	0	C048		INTRR	LO OSW	BUILO SENSE OSW	88905260
031F	0	F0C0		EOR	ACT1	*	88905270
0320	0	004A		STO	OSW&1	*	88905280
0321	0	0848		XIO	OSW	SENSE-NO RESET	88905290
0322	0	C048		LO	OSW&1	BUILO RESET SENSE	88905300
0323	0	F04A		EOR	ONE	*	88905310
0324	0	0046		STO	OSW&1	*	88905320
0325	0	0844		XIO	OSW	SENSE-RESET	88905330
0326	0	0045		STO	TAOSW	SAVE SENSE W0	88905340
0327	0	6600	06E0	LOX	L2 DST	SET IX	88905350
0329	0	C042		LO	TAOSW	GET SENSE W0	889

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

2400 TIMING TEST

```
032F 0 70DF      MOX   TAX1   EXIT
0330 0000      BSS   E   0
*
0330 0 0701      OSWSP DC   /0701   SENSE-RESET IDCC
0331 0 0000      DC     0
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXX ROUTINE TO SERVICE   XXXXX
*XXXXXXXXXXXXXXXXXXXX NON-PROGRAM GENERATED XXXXX
*XXXXXXXXXXXXXXXXXXXX INTERRUPTS          XXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
0332 0 0000      SVINT OC     0
0333 0 0030      STD   SVID
0334 0 COAE      LO     TERM      SAVE ACCUMULATOR
0335 0 0400 06E7  STO   L   DST&7  SET ILLEGAL INTR
0337 0 082E      XIO   ILSW      RESET ILSW
0338 0 7402 0362  MOX   L   SV7,2  SET PASS SWITCH
033A 0 1010      SLA   16
033B 0 0023      STO   SV4      CLEAR AREA CODE CNTR
033C 0 CO20      LD     SV2
033D 0 0023      STD   SV6      SET IOCC IN USE SW
033E 0 CO10      SVINO LD   SV1
033F 0 0020      STO   SV5      SET MDOIFIER COUNTER
0340 0 CO1E      SVINI LO   SV4
0341 0 100B      SLA   11
0342 0 E810      OR     SV5      *BUILO IOCC
0343 0 E810      OR     SV6
0344 0 0020      STO   SVID&1
0345 0 081E      XIO   SVIO      SENSE OSW AND RESET
0346 0 74FF 0360  MDX   L   SV5,-1
0348 0 70F7      MOX   SVINI     BRANCH IF NOT ALL MO
0349 0 7401 035F  MOX   L   SV4,1  INCREMENT AREA CODE
034B 0 CO13      LO     SV4
034C 0 900E      S     SV0      CHECK IF ALL AC USED
034D 0 480B      BSC    &
034E 0 70EF      MOX   SVINO     GD SENSE WITH NXT AC
034F 0 74FF 0362  MOX   L   SV7,-1 SKIP IF SECOND PASS
0351 0 7001      MDX   *&1
0352 0 7005      MOX   SVEXT-1
0353 0 CO0A      LD     SV3
0354 0 000C      STO   SV6      SET IDCC FDR PI
0355 0 1010      SLA   16
0356 0 000B      STO   SV4      SET AC FOR NEXT
0357 0 70E6      MOX   SVINO     *PASS
0358 0 CO0B      LO     SVIO     RESTORE ACCUMULATOR
0359 0 4CC0 0332  SVEXT BOSC I SVINT EXIT
*
*          **  CONSTANTS  **
*
035B 0 001F      SV0   DC     /001F  NUMBER OF AREA CODES
035C 0 00FF      SV1   OC     /00FF  NUMBER OF MODIFIERS
035D 0 0701      SV2   OC     /0701  SENSE/RESET DSW
035E 0 0700      SV3   OC     /0700  SENSE/RESET PISW
035F 0 0000      SV4   OC     0      AREA CODE INDICATOR
0360 0 0000      SV5   DC     0      MODIFIER INOICATOR
0361 0 0000      SV6   OC     0      IDCC IN USE
0362 0 0000      SV7   DC     0      PASS SWITCH
0364 0000      BSS   E   0
0364 0 0000      SVIO  DC     0      SENSE OSW IOCC
0365 0 0000      OC     0
*
*          CONSTANTS
*
0366 0000      BSS   E   0
0366 0 0000      ILSW  OC     0      SENSE ILSW IOCC
0367 0 0300      DC     /0300
```

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PAGE 5

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

2400 TIMING TEST

```
0368 0 0000      TAAQ DC     0      A AND Q TEM STOR
0369 0 0000      DC     0
036A 0 0700      DSW   DC     /0700  USW IOCC
036B 0 0000      DC     0
036C 0 0000      TAOSW DC     0      TAPE DSW STDRAGE
036D 0 3040      TADWC OC     /3040  CK TAPE OSW FOR DK
036E 0 0001      ONE   OC     1      CDNSTANT
*
*          ERROR HANGS
*
036F 0 0000      ERRI DC     0
0370 0 3009      WAIT9 WAIT 9      ILLEGAL INTRPT
*
0371 0 0000      TERR DC     0
0372 0 300A      WAITA DC     /300A  BLANK ILSW
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXX COMMON DCC ROUTINE XXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
0373 0 0000      OCC   OC     0
0374 0 6A10      STX   2  OCC&1  SAVE IX 2
0375 0 6780 0373  LOX   13  OCC   IX3 # RETURN
*
0377 0 C300      LO     3  0      GET AORS DF STRING
0378 0 DOFA      STD   OCC
*
0379 0 6680 0373  LOX   12  OCC   IX2 # ADRS STRING
*
037B 0 C680 0000  LD     12  0      SET AREA COOE
037D 0 EE80 0001  DR     12  1      SET FUNC
037F 0 EE80 0002  OR     12  2      SET MDOIFIER
*
0381 0 1890      SRT    16      PUT IN Q
0382 0 C203      LD     2  3      GET I/O AORS
*
0383 0 0806      STO   OCC3     SET IOCC WD
0384 0 6600 0000  OCC2 LDX  L2  0      RESTORE IX 2
*
0386 0 0803      XIO   OCC3     00 CDMANO
*
0387 0 4F00 0001  BSC   L3  1      RETURN
038A 0002      OCC3 BSS   E   2      SX
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXX COMMON DELAY ROUTINE XXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
038C 0 0000      OELAY DC     0      SE
038D 0 6780 038C  LDX   13  OELAY  IX3 # RETURN
038F 0 CF80 0000  LDD   13  0      GET COUNT
0391 0 980C      JOLY2 SO     MONE  SU8 1
0392 0 080D      STD   MST      SAVE
0393 0 4C20 0398  BSC   L   JDLY3,Z  CK FOR ZERO
0395 0 18D0      RTE     16
0396 0 4C18 039A  BSC   L   JDLY4,&-  CK FOR ZERO
0398 0 C807      JOLY3 LDD   MST      GET COUNT
0399 0 70F7      MOX   JDLY2
039A 0 7401 038C  MOX   L   OELAY,1  &1 TO RETURN
039C 0 4C80 038C  BSC   I   OELAY  RETURN
039E 0000      BSS   E   0      SX
039E 0 0000      MONE DC     0
039F 0 0001      DC     1
03A0 0 0000      MST   OC     0
03A1 0 0000      DC     0
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXX COMMON SENSE ODEVICE RTN XXX
*
88906090
88906100
88906110
88906120
88906130
88906140
88906150
88906160
88906170
88906180
88906190
88906200
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88906670
88906680
88906690
88906700
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88906760
```

PART NO. 2196491
PAGE 5ADATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
C NO. 415178 415233 411731 411857 411875 431319 431319APROG IO 08B9-2
PAGE 5ATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
C NO. 415178 415233 411731 411857 411875 431319 431319APROG IO 08B9-2
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IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

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2400 TIMING TEST

```
*****
*
03A2 0 0000      DIND  DC      0
03A3 0 6780 03A2  LDX  I3 DIND      IX 3 # RETURN
03A5 0 C780 0000  LD  I3 0      LOAD AREA CDDE
03A7 0 F006      EOR  FNC      SET FUNCT
03A8 0 F780 0001  EOR  I3 1      SET MOD
03AA 0 D006      STD  IOCC1&1  SAVE
03AB 0 0804      XIO  IDCC1    SENSE
03AC 0 4F00 0003  BSC  L3 3      RETURN
03AE 0 0700      FNC  DC      /0700
03B0 0 0000      BSS  E 0
03B0 0 0000      IOCC1 DC 0
03B1 0 0000      DC      0
*
*****
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXX COMMON HALT ROUTINE XXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
03B2 0 0000      HALT  OC      0
03B3 0 0C00 043E  XIO  L  MK15    MASK ALL LEVELS
03B5 0 0C00 044D  XIO  L  MK27    *
03B7 0 3002      WAIT2 WAIT  2      COMMON WAIT
03B8 0 0C00 0302  XID  L  UNMK3    UNMASK
03BA 0 0C00 0304  XIO  L  UNMK4    UNMASK
03BC 0 4C80 03B2  BSC  I  HALT    RETURN
03BE 0 0000      WC  OC      0      WD CT STDRAE
*
*****
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXX ROUTINE TD CONTRDL XXXX
*XXXXXXXXXXXXXXXXXXXXX CONVERSION AND LDGGING XXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
D3BF 0 0D00      LOGC  DC      0
03C0 0 6D00 D564  STX  L1 LOGC7&1  SAVE IXING
03C2 0 6E00 0566  STX  L2 LDGC8&1  *
03C4 0 6F00 0568  STX  L3 LOGC9&1  *
03C6 0 6500 08F7  LDX  L1 MDD0      IX 1 # ADRS DF MSG
03C8 0 6600 0096  LOX  L2 PRA      IX 2 # ADR OF MSG
03CA 0 631F      LOX  3 31      IX 3 # LGTH/OUTPUT
03CB 0 1010      SLA  16      SET ACCUM # BLANK
03CC 0 0700 0091  STD  L3 PRA4-1  SET I/O AREA # BLANK
03CE 0 73FF      MOX  3 -1
03CF 0 70FC      MOX  LOGIC
03D0 0 6700 08F7  LOX  L3 MDD0      IX 3 # ADRS OF MSG
03D2 0 C100      LO  1 0      GET LINE NO/WD CT
03D3 0 1808      SRA  8      SAVE LINE NUMBER
03D4 0 4C18 0499  BSC  L  LOGD0,&-  BRANCH # LINE ZERO
03D6 0 7306      LOG2C MOX  3 6      SET IX3 # 2ND MDD
03D7 0 7210      MOX  2 16      SET IX2 # 2ND MDD
03D8 0 C100      LO  1 0      GET WD CT/LINE NO
03D9 0 1008      SLA  8
03DA 0 1808      SRA  8      SAVE WD CT
03DB 0 00E2      STO  WC
03DC 0 74FC 03BE  MDX  L  WC,-4    OECR WD CT
03DE 0 7001      MOX  LDGV1    NOT OONE
03DF 0 7039      MDX  LDG6C    CONV COMPLETE
03E0 0 1010      LOGV1 SLA  16    CLEAR OOD-EVEN SW
03E1 0 0400 0462  STD  L  SW      *
03E3 0 C400 020F  LO  L  RID      GET RTN ID
03E5 0 F070      EOR  K007    CK FOR RTN 7
03E6 0 4820      BSC  Z      SKIP = RTN 7
03E7 0 7016      MDX  LOG3C    BRANCH
03E8 0 6700 0930  LOX  L3 MTTV0    SET IX 3
03EA 0 C300      LDGV2 LO  3 0      GET WD
03EB 0 0400 05E5  STD  L  WOC0N    SET
03ED 0 4400 05A6  BSI  L  HEDEC    CONVERT TO OEC
03EF 0 C400 0508  LO  L  CODE&1  GET CONVERTED WD
```

88906770
88906780
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IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

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2400 TIMING TEST

```
03F1 0 D200      STO  2 0      SET IN MSG
03F2 0 7201      MDX  2 1      INCR IX
03F3 0 7301      MDX  3 1      *
03F4 0 7401 0462  MDX  L  SW,1    INCR SWITCH
03F6 0 C068      LD  SW      GET SWITCH
03F7 0 4804      BSC  E      SKIP IF EVEN
03F8 0 70F1      MDX  LDGV2    LOOP
03F9 0 7201      MDX  2 1      INCR IX
03FA 0 74FF C3BE  MDX  L  WC,-1    CK FOR DUNE
03FC 0 70ED      MDX  LOGV2    LDOP
03FD 0 7018      MDX  LOG6C    PRINT
03FE 0 C101      LDG3C LD  1 1      GET HEX/OEC SW
03FF 0 4C18 0411  BSC  L  LOG5C,&-  BRANCH # HEX
0401 0 C300      LD  3 0      GET WD TO CONV
0402 0 D400 05E5  STO  L  WDCDN    SET IN CONV RTN
0404 0 4400 05A6  BSI  L  HEDEC    GD CONV TD DEC
0406 0 CC00 05DA  LDD  L  CODE      GET PACKED WD
0408 0 D200      LOG4C STD  2 0      SET IN MSG
0409 0 18D0      RTE  16
040A 0 0201      STO  2 1
040B 0 7203      MDX  2 3
040C 0 7301      MOX  3 1      INCR IX 3
040D 0 74FF 03BE  MDX  L  WC,-1    CHECK FOR DONE
040F 0 70EE      MDX  LDG3C
0410 0 7008      MDX  LDG6C
0411 0 C300      LOG5C LO  3 0      GET WD TO CDNVERT
0412 0 D400 0606  STD  L  HEXWD    STD IN HEX CDNV RTN
0414 0 4400 05E6  BSI  L  HEXCV    GO CONVERT TO HEX
0416 0 CC00 060C  LDD  L  HEXCD    GET CONVERTED WD
0418 0 70EF      MDX  LOG4C    GD CK FOR DONE
0419 0 C400 02E3  LOG6C LD  L  TERM    GET A TERM
041B 0 D200      STO  2 0      SET IN I/O AREA
041C 0 C400 02DF  LD  L  RID      GET RTN NO
041E 0 9D45      S  K006    SUB 6
041F 0 4820      BSC  Z      SKIP IF RTN 6
0420 0 7D15      MDX  HERE
0421 0 C400 0D2E  LD  L  LOW      GET LDW CREEP SW
0423 0 4810      BSC  -      SKIP IF NEG
0424 0 7003      MDX  HERE1
0425 0 C03F      LO  K0020    GET NEG SIGN
0426 0 0400 0DA5  STO  L  PRA4&19  SET IN MSG
0428 0 C400 0030  HERE1 LO  L  AVG      GET AVG SW
042A 0 4810      BSC  -      SKIP IF NEG
042B 0 7003      MOX  HERE2
042C 0 C038      LO  K0020    GET NEG SIGN
042D 0 0400 0DA8  STO  L  PRA4&22  SET IN MSG
042F 0 C400 0D2F  HERE2 LO  L  HI      GET HI SW
0431 0 4810      BSC  -      SKIP IF NEG
0432 0 7003      MOX  HERE
0433 0 C031      LO  K0020    GET NEG SIGN
0434 0 D400 00AB  STO  L  PRA4&25  SET IN MSG
*
0436 0 C400 02E1  HERE  LO  L  SW0      GET SWS
0438 0 1806      SRA  6
0439 0 4C04 0466  BSC  L  LOGAC,E  BRANCH # USE PRINTER
043B 0 4C00 052A  BSC  L  LOGBC    USE TYPEWRITER
*
043E 0 0000      BSS  E 0
043E 0 FFFF      MK15 DC  /FFFF    MASK ALL LVLS IOCCS
043F 0 0480      OC  /0480
0440 0 FFFF      MK27 OC  /FFFF
0441 0 0481      DC  /0481
0442 0 0700      SNSPR OC /0700
0443 0 0700      DC  /0700
0444 0 6500 0000  LOG7C LDX L1 0      RESTORE IX 1
0446 0 6600 0000  LOG8C LOX L2 0      RESTORE IX 2
0448 0 6700 0000  LOG9C LOX L3 0      RESTORE IX 3
044A 0 4C80 03BF  BSC  I  LOGC    EXIT
```

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88907990
88908000
88908010
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88908080
88908090
88908100
88908110
88908120

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

2400 TIMING TEST

```
044C 0 0000      PCCO DC      0      SE 88908130
044D 0 69F7      STX 1 LOG7C&1  SAVE IX 1 88908140
044E 0 6AF8      STX 2 LOG8C&1  SAVE IX 2 88908150
044F 0 6BF9      STX 3 LOG9C&1  SAVE IX 3 88908160
0450 0 C010      LO  PCCX1      GET WD CT 88908170
0451 0 0400 08F7 STD L MDD0      SAVE      88908180
0453 0 C0F8      LO  PCCO      GET RETURN 88908190
0454 0 0400 03BF STO L LOGC      SAVE      88908200
0456 0 C400 02E3 LO L TERM      GET TERMINATOR 88908210
0458 0 D400 0DAE STO L PRA4&28 SET IN MSG 88908220
045A 0 C400 02E1 LO L SWO      GET SW FNC 0 88908230
045C 0 1806      SRA 6          88908240
045D 0 4C04 0466 BSC L LOGAC,E  BRANCH # USE PRINTER 88908250
045F 0 4C00 052A BSC L LOGBC      USE TYPEWRITER 88908260
0461 0 0009      PCCX1 DC 9      PCCO WO CT 88908270
0462 0 0000      SW DC 0      ODO-EVEN SW 88908280
0463 0 0007      K007 OC /0007  CONSTANT 7 88908290
0464 0 0006      K006 OC 6      CONSTANT 88908300
0465 0 0020      K0020 OC /0020 * 88908310
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXX PRINTER OUTPUT ROUTINE XXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
0466 0 6818      LOGAC STX 3 LOGAB&1  SAVE IX 3 88908320
0467 0 C400 08F7 LD L MDD0      GET WD CT/ LINE NO 88908330
0469 0 1008      SLA 8          SAVE WD CT 88908340
046A 0 1808      SRA 8          88908350
046B 0 D001      STO LOGAO&1  88908360
046C 0 6700 0000 LOGAD LOX L3 0      IX 3 # WO CT 88908370
046E 0 C700 048F LD L3 PRWC-2  GET FINAL WD CT 88908380
0470 0 D400 0092 STD L PRA4      SAVE      88908390
0472 0 C0CF      LD SNSPR      GET SENSE IOCC 88908400
0473 0 F400 02E5 EOR L EOIT&1  SET AREA CODE 88908410
0475 0 00C0      STO SNSPR&1  SAVE      88908420
0476 0 08C7      XIO MK15      MASK ALL LVLS 88908430
0477 0 08C8      XIO MK27      * 88908440
0478 0 08C9      XIO SNSPR      SENSE PRINTER 88908450
0479 0 4804      BSC E          IS PRINTER READY 88908460
047A 0 3003      WAIT3 WAIT 3  PRINTER NOT READY 88908470
*
*
*****
047B 0 4400 0373 BSI L DCC      GD LOG 88908480
0470 0 05A0      OC LOGX3      ADRS OF STRING * 88908490
*****
047E 0 6700 0000 LOGAB LOX L3 0      RESTORE IX 3 88908500
0480 0 08C1      LOGAE XIO SNSPR      SENSE PRINTER 88908510
0481 0 1002      SLA 2          88908520
0482 0 4810      BSC -          IS PRTR CMPL ON 88908530
0483 0 70FC      MDX LOGAE      NO 88908540
0484 0 C0BE      LD SNSPR&1  GET IOCC 88908550
0485 0 F400 036E EOR L ONE      SET BIT 15 88908560
0487 0 00BB      STO SNSPR&1  SAVE 88908570
0488 0 0889      LOGAF XIO SNSPR      SENSE PRINTER 88908580
0489 0 1801      SRA 1          88908590
048A 0 4804      BSC E          IS PRTR BUSY 88908600
048B 0 70FC      MDX LOGAF      YES 88908610
048C 0 0C00 0302 XIO L UNMK3  UNMASK ALL LEVELS 88908620
048E 0 0C00 0304 XIO L UNMK4  88908630
0490 0 70B3      MDX LOG7C      GD EXIT 88908640
0491 0 000E      PRWC OC 14      WDRO CTS FOR PRINTER 88908650
0492 0 0013      OC 19          88908660
0493 0 0016      DC 22          88908670
0494 0 0019      DC 25          88908680
0495 0 001C      DC 28          88908690
0496 0 001F      DC 31          88908700
0497 0 0022      DC 34          88908710
88908720
88908730
88908740
88908750
88908760
88908770
88908780
88908790
88908800
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ATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
C NO. 415178 415233 411731 411857 411875 431319 431319A
PROG IO 0889-2
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IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

2400 TIMING TEST

```
0498 0 001C      DC 28      88908810
0499 0 C102      LOGO0 LO 1 2      88908820
049A 0 D400 0606 STO L HEXWD      SET IN RTN 88908830
049C 0 4400 05E6 BSI L HEXCV      GO CONVERT SRC 88908840
049E 0 CC00 060C LDO L HEXCD      GET CONVERTED WO 88908850
04A0 0 DC00 0096 STO L PRA      SET IN MSG 88908860
04A2 0 C104      LO 1 4      GET MSG ID 88908870
04A3 0 D400 0606 STO L HEXWO      SET IN RTN 88908880
04A5 0 4400 05E6 BSI L HEXCV      GO CONVERT TO HEX SRC 88908890
04A7 0 CC00 060C LDO L HEXCD      GET CONVERTED WO 88908900
04A9 0 D400 0099 STO L PRA&3      SET IN MSG 88908910
04AB 0 18D0      RTE 16      MOVE Q TO A 88908920
04AC 0 0400 009A STO L PRA&4      SET IN MSG 88908930
04AE 0 C400 020F LD L RIO      GET RTN NUMBER 88908940
04B0 0 D400 0606 STO L HEXWD      SET IN RTN 88908950
04B2 0 4400 05E6 BSI L HEXCV      GO CONVERT TO HEX SRC 88908960
04B4 0 CC00 060C LDD L HEXCO      GET CONVERTED WO 88908970
04B6 0 DC00 009C STO L PRA&6      SET IN MSG 88908980
04B8 0 C400 02E0 LO L RAO      GET RTN AORS 88908990
04BA 0 0400 0606 STO L HEXWO      SET IN RTN 88909000
04BC 0 4400 05E6 BSI L HEXCV      GO CONVERT TO HEX SRC 88909010
04BE 0 CC00 060C LDD L HEXCD      GET CONVERTED WO 88909020
04C0 0 D400 0D9F STO L PRA&9      SET IN MSG 88909030
04C2 0 18D0      RTE 16      MOVE Q TO A 88909040
04C3 0 D400 0DA0 STO L PRA&10      SET IN MSG 88909050
04C5 0 C105      LD 1 5      GET ORIVE NO 88909060
04C6 0 D400 0606 STO L HEXWD      SET IN RTN 88909070
04C8 0 4400 05E6 BSI L HEXCV      GO CONVERT SRC 88909080
04CA 0 CC00 060C LDO L HEXCD      GET CONVERTED WO 88909090
04CC 0 0400 00A2 STO L PRA&12      SET IN MSG 88909100
04CE 0 1800      RTE 16      88909110
04CF 0 0400 00A3 STO L PRA&13      SET IN MSG 88909120
04D1 0 4C00 03D6 BSC L LOG2C 88909130
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXX 1443 PAGE CONTROL ROUTINE X
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
RST DC 0      88909140
LO L SWO      GET SWITCHES 88909150
SLA 9      CK FOR 1443 88909160
BSC L RST2,-  BRANCH # NOT 1443 88909170
XIO RSTX2     SKIP TO CHN 1 88909180
RST1 XIO RSTX3 SENSE 1443 88909190
SRA 2      CK FOR BUSY 88909200
8SC E      SKIP # NOT BUSY 88909210
MDX RST1     LOOP 88909220
LO L1 EDIT&4 GET NUMBER OF TRACKS 88909230
8SC L DR9,&- BRANCH # 9 TRACK 88909240
LO K0700     SET 7 TRACK 88909250
MOX DR9&1    BRANCH 88909260
LO K0900     GET 9 TRACK 88909270
STO L LN3B   SET IN MSG 88909280
LO L1 EOIT&6 GET DRIVE MODEL 88909290
8SC L MD3,&- BRANCH # MODEL 3 88909300
SLA 8      MOVE TO LHW 88909310
MDX M03&1    BRANCH 88909320
LO K0300     GET MOOEL 3 88909330
STO L LN3A   SET IN MSG 88909340
LD L EDIT&8  GET MEM SPEED 88909350
BSC L TMIC,+  BRANCH # 2 MIC 88909360
8SC L TMICA,- BRANCH IF 4 MIC 88909370
LO L H0238    SET MSG TO 2.25 88909380
STO L LN3C   * 88909390
LO L H0205    * 88909400
MOX TMICB     CONTINUE 88909410
TMICA LD L H0400 GET 4 MIC 88909415
MOX TMIC&1    BRANCH 88909420
88909430
```

ATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
C NO. 415178 415233 411731 411857 411875 431319 431319A
PROG IO 0889-2
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2400 TIMING TEST

2400 TIMING TEST

0500 0 C024	TMIC LD	K0200	GET 2 MIC	88909440
0501 0 D400 0E5A	STO L	LN3C	SET IN MSG	88909450
0503 0 C400 0529	LD L	H2020	SET DASHES	\$ 88909451
0505 0 D400 0E58	TMICB STO L	LN3C+1	*	\$ 88909452
0507 0 6913	STX 1	SVE&1	SAVE IX 1	88909460
0508 0 7100	MOX 1	0	CHECK OR NUMBER	88909470
0509 0 7003	MDX	SYDR	DRIVE 1	88909480
050A 0 6500 200A	LDX L1	/200A	IX # -0	88909490
050C 0 7002	MOX	SYDR1	BRANCH	88909500
0500 0 6500 2001	SYOR LDX L1	/2001	IX # -1	88909510
050F 0 6D00 0E58	SYOR1 STX L1	LN30	SET IN MSG	88909520
0511 0 6700 0E4C	LOX L3	LN3-1	IX # MSG AORS	88909530
0513 0 4400 0C21	BSI L	LOADV	SET MSG - PRINT	88909540
0515 0 1010	SLA	16	CLEAR ACC	88909550
0516 0 4400 0C19	BSI L	LOADK	SET BLANK LINE	88909560
0518 0 4400 044C	BSI L	PCCO	PRINT BLANK	88909570
051A 0 6500 0000	SVE LDX L1	*-*	RESTORE IX 1	88909580
051C 0 4C80 0403	BSC I	RST	EXIT	88909590
051E 0000	BSS E	0		88909600
051E 0 0100	RSTX2 OC	/0100	IOCC - CARRIAGE SKIP	88909610
051F 0 3400	OC	/3400	*	88909620
0520 0 0000	RSTX3 DC	/0000	IOCC - SENSE 1443	88909630
0521 0 3701	DC	/3701	*	88909640
0522 0 0007	K0700 OC	/0007	CONSTANTS	88909650
0523 0 0009	K0900 OC	/0009	*	88909660
0524 0 0300	K0300 OC	/0300	*	88909670
0525 0 0220	K0200 OC	/0220	*	88909680
0526 0 0420	H0400 DC	/0420	*	88909690
0527 0 0238	H0238 OC	/0238	*	\$ 88909691
0528 0 0205	H0205 DC	/0205	*	\$ 88909692
0529 0 2020	H2020 DC	/2020	*	\$ 88909693
	*			88909700
	XX			88909710
	XXXXXXXXXXXXXXXXXXXX ROUTINE TO CONVERT XXXXXXXX			88909720
	XXXXXXXXXXXXXXXXXXXX PRINTER CODE XXXXXXXX			88909730
	XXXXXXXXXXXXXXXXXXXX TO TYPEWRITER CODE XXXXXXXX			88909740
	XX			88909750
	*			88909760
052A 0 1010	LOGBC SLA	16		88909770
052B 0 003F	STO	LOX00	CLEAR HALF WO SW	88909780
052C 0 6937	STX 1	LOGC7&1	SAVE IX 1	88909790
052D 0 6A38	STX 2	LOGC8&1	SAVE IX 2	88909800
052E 0 6839	STX 3	LOGC9&1	SAVE IX 3	88909810
052F 0 C06F	LO	PRSP	GET CARRIAGE RETURN	88909820
0530 0 D400 0D92	STO L	PRA4	SET IN MSG	88909830
0532 0 6700 0D93	LOX L3	PRA4&1	IX 3 # AORS MSG	88909840
0534 0 C300	LOGC1 LO	3 0	GET WO TO CONVERT	88909850
0535 0 D036	STO	LOX02	SAVE	88909860
0536 0 F400 02E3	EOR L	TERM		88909870
0538 0 4818	BSC &-		IS IT A TERM	88909880
0539 0 7027	MOX	LOGCC		88909890
053A 0 C031	LOGC2 LD	LOX02	GET WO TO CONVERT	88909900
053B 0 4C18 0556	BSC L	LOGCA,&-	BRANCH IF ZERO	88909910
0530 0 180C	SRA	12	SAVE ZONE	88909920
053E 0 0001	STO	LOGC3&1		88909930
053F 0 6500 0000	LOGC3 LOX L1	0	IX 1 # ZONE	88909940
0541 0 C500 056E	LD L1	LOX04	GET ADRS OF ZONE	88909950
0543 0 0007	STO	LOGC5&1	SAVE	88909960
0544 0 C027	LO	LOX02	GET WO TO CONVERT	88909970
0545 0 1004	SLA	4	SAVE POSITION	88909980
0546 0 180C	SRA	12		88909990
0547 0 0001	STO	LOGC4&1		88910000
0548 0 6600 0000	LOGC4 LOX L2	0	IX 2 # POSITION	88910010
054A 0 C600 0000	LOGC5 LO	L2 0	GET TYPEWRITER CODE	88910020
054C 0 7400 056B	MOX L	LOX00,0	IS THIS FIRST HALF	88910030
054E 0 700B	MOX	LOGC6	NO	88910040
054F 0 0010	STO	LOX03	YES	88910050
0550 0 7401 056B	MOX L	LOX00,1	SET TO SECONO HALF	88910060

0552 0 C019	LD	LOX02	GET WO TO CONVERT	88910070
0553 0 1008	SLA	8	SET TO SECOND HALF	88910080
0554 0 D017	STO	LOX02	SAVE	88910090
0555 0 70E4	MOX	LOGC2	GO CONVERT	88910100
0556 0 D0F2	LOGCA STO	LOGC4&1		88910110
0557 0 C01A	LD	LOX04&4	GET ADRS	88910120
0558 0 D0F2	STO	LOGC5&1	SET	88910130
0559 0 70EE	MDX	LOGC4	GO SET BLANK	88910140
	*			88910150
	*		SECOND HALF WORD	88910160
	*			88910170
055A 0 1808	LOGC6 SRA	8	MOVE TO SECONO HALF	88910180
055B 0 F011	EOR	LOX03	COMBINE WITH FIRST	88910190
055C 0 0300	LOGCB STO	3 0	SET IN MSG	88910200
055D 0 1010	SLA	16		88910210
055E 0 D00C	STO	LOX00	SET TO FIRST HALF	88910220
055F 0 7301	MDX	3 1	IX 3 # NEXT WO	88910230
0560 0 70D3	MDX	LOGC1	CONVERT NEXT WO	88910240
	*			88910250
	*		FOUND A TERMINATOR	88910260
	*			88910270
0561 0 4400 061E	LOGCC BSI L	LOG	GO PRINT	88910280
0563 0 6500 0000	LOGC7 LDX L1	0	RESTORE IX 1	88910290
0565 0 6600 0000	LOGC8 LDX L2	0	RESTORE IX 2	88910300
0567 0 6700 0000	LOGC9 LDX L3	0	RESTORE IX 3	88910310
0569 0 4C00 0444	BSC L	LOG7C	GO EXIT	88910320
	*			88910330
	*		CONSTANTS	88910340
	*			88910350
056B 0 0000	LOX00 OC	0	HALF WORD SWITCH	88910360
056C 0 0000	LOX02 OC	0	TEMP STORAGE FOR	88910370
	*		WORD TO CONVERT	88910380
056D 0 0000	LOX03 OC	0	TEMP STORAGE FOR	88910390
	*		TYPEWRITER CODE	88910400
056E 0 0573	LOX04 DC	PR00	AORS OF ZONE 0	88910410
056F 0 057C	OC	PR01-2	ADRS OF ZONE 1	88910420
0570 0 0587	DC	PR02	AORS OF ZONE 2	88910430
0571 0 0593	OC	PR03-1	AORS OF ZONE 3	88910440
0572 0 0586	DC	PR02-1	ADRS OF BLANK	88910450
	*			88910460
	*		PRINTER CODE TO TYPEWRITER	88910470
	*		CODE CONVERSION TABLE	88910480
	*			88910490
0573 0 2100	PR00 DC	/2100	0	88910500
0574 0 FC00	OC	/FC00	1	88910510
0575 0 D800	OC	/D800	2	88910520
0576 0 DC00	DC	/DC00	3	88910530
0577 0 F000	DC	/F000	4	88910540
0578 0 F400	OC	/F400	5	88910550
0579 0 D000	DC	/D000	6	88910560
057A 0 D400	DC	/D400	7	88910570
057B 0 E400	OC	/E400	8	88910580
057C 0 E000	DC	/E000	9	88910590
057D 0 C400	OC	/C400	0	88910600
057E 0 9A00	PR01 OC	/9A00	S	88910610
057F 0 9E00	OC	/9E00	T	88910620
0580 0 B200	OC	/B200	U	88910630
0581 0 B600	DC	/B600	V	88910640
0582 0 9200	OC	/9200	W	88910650
0583 0 9600	OC	/9600	X	88910660
0584 0 A600	OC	/A600	Y	88910670
0585 0 A200	OC	/A200	Z	88910680
0586 0 2100	OC	/2100	BLANK	88910690
0587 0 8400	PR02 DC	/8400	-	88910700
0588 0 7E00	DC	/7E00	J	88910710
0589 0 5A00	DC	/5A00	K	88910720
058A 0 5E00	DC	/5E00	L	88910730
058B 0 7200	OC	/7200	M	88910740

2400 TIMING TEST

2400 TIMING TEST

```
058C 0 7600      DC      /7600      N
058D 0 5200      DC      /5200      O
058E 0 5600      DC      /5600      P
058F 0 6600      DC      /6600      Q
0590 0 6200      DC      /6200      R
0591 0 4200      DC      /4200
0592 0 4000      DC      /4000      $
0593 0 0600      DC      /0600      *
0594 0 3E00      PR03 DC      /3E00      A
0595 0 1A00      DC      /1A00      B
0596 0 1E00      DC      /1E00      C
0597 0 3200      DC      /3200      O
0598 0 3600      DC      /3600      E
0599 0 1200      DC      /1200      F
059A 0 1600      DC      /1600      G
059B 0 2600      DC      /2600      H
059C 0 2200      DC      /2200      I
059D 0 0200      DC      /0200
059E 0 0000      DC      /0000      .
059F 0 8121      PRSP DC      /8121      CARRIAGE RETURN
*
*              CONSTANTS
*
*              ADRS STRING FOR OCC CALL
*
05A0 0 02E5      LOGX3 DC      EDIT&1      ADRS OF AREA CODE
05A1 0 05A4      DC      LDGX8      ADRS OF FUNCTION
05A2 0 05A5      DC      LOGX9      ADRS OF MODIFIER
05A3 0 0D92      DC      PRA4       ADRS OF MSG
*
*              FUNCTION AND MODIFIER
*
05A4      0000      BSS E 0
05A4 0 0500      LDGX8 DC      /0500      FUNCTION
05A5 0 0000      LDGX9 DC      /0000      MODIFIER
*
*              MASK CONSTANTS
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXX HEX TO DECIMAL CONVERSION X
XXXXXXXXXXXXXXXXXXXXX RDUTINE X
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
05A6 0 0000      HEDEC DC      0
05A7 0 6B21      STX 3 HEDE4&1      SAVE IX 3
05A8 0 6A22      STX 2 HEDE5&1      SAVE IX 2
05A9 0 6923      STX 1 HEDE6&1      SAVE IX 1
05AA 0 6500 05E1      LDX L1 OPARA      OUTPUT AREA INDEX
05AC 0 6600 05DC      LOX L2 CVTBL      CONVERSION TABLE IX
*
05AE 0 C036      LD      WDCDN      SET WORD TO CONVERT
05AF 0 D028      STD      WRD      IN WRK AREA
*
05B0 0 6700 060E      HEDE1 LOX L3 CODEH      CODE TABLE INDEX
*
05B2 0 C200      LO      2 0      SET CONVERSION
05B3 0 D025      STO      CONVO      CONSTANT IN SW AREA
*
05B4 0 C023      HEDE2 LD      WORD      CHECK WORD AGAINST
05B5 0 9023      S      CONVO      CONVERSION CONSTANT
05B6 0 4C28 05BF      BSC L HEDE3,&Z      BRANCH IF MINUS
*
05B8 0 8020      A      CONVO      RESTORE NUMBER
05B9 0 D01E      STD      WRD
*
05BA 0 C01E      LD      CONVO      SET UP FOR NEXT
```

8B910750
8B910760
8B910770
8B910780
8B910790
8B910800
8B910810
8B910820
8B910830
8B910840
8B910850
8B910860
8B910870
8B910880
8B910890
8B910900
8B910910
8B910920
8B910930
8B910940
8B910950
8B910960
8B910970
8B910980
8B910990
8B911000
8B911010
8B911020
8B911030
8B911040
8B911050
8B911060
8B911070
8B911080
8B911090
8B911100
8B911110
8B911120
8B911130
8B911140
8B911150
8B911160
8B911170
8B911180
8B911190
8B911200
8B911210
8B911220
8B911230
8B911240
8B911250
8B911260
8B911270
8B911280
8B911290
8B911300
8B911310
8B911320
8B911330
8B911340
8B911350
8B911360
8B911370
8B911380
8B911390
8B911400
8B911410
8B911420

```
05BB 0 8200      A      2 0      CHECK
05BC 0 D01C      STD      CONVO
*
05BD 0 7301      MDX 3 1      CODE TABLE INDEX & 1
05BE 0 70F5      MDX      HEDE2
*
*              NEGATIVE RESULT
*
05BF 0 8200      HEDE3 A      2 0      RESTORE LAST NUMBER
05C0 0 D017      STD      WORD
*
05C1 0 C300      LD      3 0      SET 1443 CODE IN
05C2 0 D100      STD      1 0      OUTPUT AREA
*
05C3 0 7101      MDX 1 1      DUTPUT AREA INDEX &1
05C4 0 7201      MDX 2 1      CONVERSION TBL IX &1
*
05C5 0 C200      LD      2 0
05C6 0 4C20 05B0      BSC L HEDE1,&Z
*
05C8 0 6700 0000      HEDE4 LDX L3 0      RESTORE INDEX REG 3
05CA 0 6600 0000      HEDE5 LOX L2 0      RESTORE INDEX REG 2
05CC 0 6500 0000      HEDE6 LDX L1 0      RESTORE INDEX REG 1
05CE 0 C012      LD      OPARA      GET 1ST CODE AND
05CF 0 1008      SLA      8      PACK WITH 2ND
05D0 0 E811      DR      OPARA&1
05D1 0 D008      STO      CODE
05D2 0 C010      LD      OPARA&2      GET 3RD CODE AND
05D3 0 1008      SLA      8      PACK WITH 4TH
05D4 0 E80F      OR      OPARA&3
05D5 0 D005      STO      CODE&1
*
05D6 0 4C80 05A6      BSC I HEDEC      RETURN TO USER      SX
*
*              CONVERSION CONSTANTS
*
05D8 0 0000      WORD DC      0      WRK AREA
05D9 0 0000      CONVO DC      0      WORK AREA
*
05DA      0000      BSS E 0
*
05DA 0 0000      CODE DC      0      PACKED WORDS 1 AND 2
05DB 0 0000      DC      0      PACKED WORDS 3 AND 4
*
05DC 0 03E8      CVTBL DC      /03E8      1000
05DD 0 0064      DC      /0064      100
05DE 0 000A      DC      /000A      10
05DF 0 0001      DC      /0001      1
05E0 0 0000      DC      /0000      0
*
05E1 0 0000      OPARA DC      0      DUTPUT WORK AREA
05E2 0 0000      DC      0
05E3 0 0000      DC      0
05E4 0 0000      DC      0
*
05E5 0 0000      WDCDN DC      0      STORAGE/WD TO CONVRT
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXX HEX TO 1443 HEX XXXXXXXX
XXXXXXXXXXXXXXXXXXXXX CONVERSION ROUTINE XXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
05E6 0 0000      HEXCV DC      0
05E7 0 6B18      STX 3 HEXC3&1      SAVE IX 3
05E8 0 6A18      STX 2 HEXC2&1      SAVE IX 2
05E9 0 6204      LDX 2 4      CONVERSION INDEX
*
05EA 0 C01B      LD      HEXWD      GET WORD TO CONVERT
```

8B911430
8B911440
8B911450
8B911460
8B911470
8B911480
8B911490
8B911500
8B911510
8B911520
8B911530
8B911540
8B911550
8B911560
8B911570
8B911580
8B911590
8B911600
8B911610
8B911620
8B911630
8B911640
8B911650
8B911660
8B911670
8B911680
8B911690
8B911700
8B911710
8B911720
8B911730
8B911740
8B911750
8B911760
8B911770
8B911780
8B911790
8B911800
8B911810
8B911820
8B911830
8B911840
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8B911860
8B911870
8B911880
8B911890
8B911900
8B911910
8B911920
8B911930
8B911940
8B911950
8B911960
8B911970
8B911980
8B911990
8B912000
8B912010
8B912020
8B912030
8B912040
8B912050
8B912060
8B912070
8B912080
8B912090
8B912100

2400 TIMING TEST

```
05EB 0 1890      SRT      16      SET A IN 0      88912110
05EC 0 1010      SLA      16      88912120
05ED 0 1084      HEXC1 SLT      4      GET CHARACTER  88912130
05EE 0 0001      STD      HEXC1&3  88912140
05EF 0 6700 0000  LDX      L3 0      SET CODE TABLE INDEX 88912150
*               88912160
05F1 0 C700 060E  LD      L3 CDEEH  GET CODED CHARACTER 88912170
05F3 0 0600 0606  STD      L2 HEX00-1  AND SAVE          88912180
05F5 0 1010      SLA      16      88912190
*               88912200
05F6 0 72FF      MDX      2 -1     CHECK IF DDNE      88912210
05F7 0 70F5      MDX      2 -1     88912220
*               88912230
05F8 0 C011      LD      HEX00&3  PACK CODED WORDS  88912240
05F9 0 1008      SLA      8      88912250
05FA 0 E80E      DR      HEX00&2  88912260
05FB 0 D010      STD      HEXCD    88912270
05FC 0 C008      LD      HEX00&1  88912280
05FD 0 1008      SLA      8      88912290
05FE 0 E808      DR      HEX00    88912300
05FF 0 D000      STD      HEXCD&1  88912310
0600 0 6600 0000  HEXC2 LDX      L2 0      RESTORE IX 2      88912320
0602 0 6700 0000  HEXC3 LDX      L3 0      RESTORE IX 3      88912330
0604 0 4C80 05E6  BSC      I  HEXCV      RETURN TO USER      SX 88912340
*               88912350
*               88912360
*               88912370
*               88912380
0606 0 0000      HEXWD DC      0      WORD TO CONVERT  88912390
0607 0 0000      HEX00 DC      0      *               88912400
0608 0 0000      DC      0      * UNPACKED CODED  88912410
0609 0 0000      DC      0      * WORD          88912420
060A 0 0000      DC      0      *               88912430
*               88912440
060C 0000      BSS      E 0      88912450
*               88912460
060C 0 0000      HEXCD DC      0      * PACKED CODED WORD 88912470
060D 0 0000      DC      0      *               88912480
*               88912490
*               88912500
*               88912510
*               88912520
060E 0 000A      CDEEH DC      /000A 0      88912530
060F 0 0001      DC      /0001 1      88912540
0610 0 0002      DC      /0002 2      88912550
0611 0 0003      DC      /0003 3      88912560
0612 0 0004      DC      /0004 4      88912570
0613 0 0005      DC      /0005 5      88912580
0614 0 0006      DC      /0006 6      88912590
0615 0 0007      DC      /0007 7      88912600
0616 0 0008      DC      /0008 8      88912610
0617 0 0009      DC      /0009 9      88912620
0618 0 0031      DC      /0031 A      88912630
0619 0 0032      DC      /0032 B      88912640
061A 0 0033      DC      /0033 C      88912650
061B 0 0034      DC      /0034 D      88912660
061C 0 0035      DC      /0035 E      88912670
061D 0 0036      DC      /0036 F      88912680
*               88912690
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88912700
*XXXXXXXXXXXXXXXXXXXXX TYPEWRITER OUTPUT ROUTINE X 88912710
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88912720
*               88912730
061E 0 0000      LOG      OC      0      SE          88912740
*               88912750
061F 0 0C00 043E  XIO      L  MK15     MASK ALL LVLS  88912760
0621 0 0C00 0440  XIO      L  MK27     88912770
*               88912780
0623 0 0838      XIO      SENSE    SENSE FOR READY
0624 0 180A      SRA      10
```

2400 TIMING TEST

```
0625 0 4804      BSC      E      88912790
*               88912800
0626 0 3004      WAIT4 WAIT 4      TYPEWRTR NOT RDY  88912810
*               88912820
0627 0 1010      SLA      16      88912830
0628 0 D036      STD      WRDSW    CLEAR 1/2 WD SWITCH 88912840
*               88912850
0629 0 C036      LD      AORS      GET MESSAGE ADDRESS 88912860
062A 0 D010      STD      LOG01&1  88912870
062B 0 D001      STD      LOG04&1  88912880
062C 0 C400 0D92  LDG04 LD      L  PRA4      GET MSG WORD  88912890
062E 0 F400 02E3  EOR      L  TERM      CHECK FDR TERM  88912900
0630 0 4C18 0654  BSC      L  LOG02,&-  BRANCH IF TERM  88912910
0632 0 C480 062D  LD      I  LOG04&1  GET MSG WORD  88912920
0634 0 F02C      EOR      K2121   CK FDR BLANK  88912930
0635 0 4C20 063A  BSC      L  LOG01,2   BRANCH IF NOT BLANK 88912940
0637 0 7401 062D  LDG05 MDX      L  LOG04&1,1 INCR SCAN ADRS 88912950
0639 0 70F2      MDX      LDG04    LOOP          88912960
063A 0 C400 0D92  LDG01 LD      L  PRA4      GET WORD TO PRINT 88912970
063C 0 D021      STD      IDARA     SET IN OUTPUT AREA 88912980
*               88912990
*               88913000
*               88913010
*               88913020
*               88913030
063D 0 081C      XIDWR XID      WRITE      WRITE CHARACTER 88913040
*               88913050
063E 0 081D      XIOSN XIO      SENSE      CHECK BUSY  88913060
*               88913070
063F 0 1808      SRA      11      88913080
0640 0 4804      BSC      E      88913090
0641 0 70FC      MDX      XIOSN     BUSY          88913100
*               88913110
*               88913120
*               88913130
*               88913140
0642 0 C01C      LD      WRDSW    GET 1/2 WORD SWITCH 88913150
0643 0 4804      BSC      E      88913160
0644 0 7006      MDX      LOG03     GO SETUP FOR NEXT WD 88913170
*               88913180
*               88913190
*               88913200
*               88913210
0645 0 C018      LD      IOARA     GET WORD IN IO AREA 88913220
0646 0 1008      SLA      8      POSITION 2ND 1/2 WD  88913230
0647 0 D016      STD      IDARA     88913240
0648 0 7401 065F  MDX      L  WRDSW,1   BUMP WORD SWITCH  88913250
064A 0 70F2      MDX      XIDWR     GO WRITE 2ND 1/2 WD 88913260
*               88913270
*               88913280
*               88913290
*               88913300
064B 0 7401 063B  LDG03 MDX      L  LOG01&1,1 INCR ADRS TO PRINT 88913310
064D 0 7401 065F  MDX      L  WRDSW,1   INCR WORD SW      88913320
064F 0 C00D      LD      LOG04&1  GET SCAN ADRS  88913330
0650 0 90EA      S      LOG01&1  SUB PRINT ADRS  88913340
0651 0 4C10 063A  BSC      L  LDG01,-   BRANCH # MORE TO PRT 88913350
0653 0 70E3      MDX      LDG05     BRANCH TO SCAN  88913360
*               88913370
*               88913380
*               88913390
*               88913400
0654 0 0C00 0302  LDG02 XIO      L  UNMK3      UNMASK ALL INTERRUPT 88913410
0656 0 0C00 0304  XIO      L  UNMK4      LEVELS          88913420
*               88913430
0658 0 4C80 061E  BSC      I  LOG      EXIT          SX 88913440
*               88913450
*               88913460
*               LOG CONSTANTS
```


2400 TIMING TEST

```

06DA 0 4D92      MD1LM DC          19B58      MODEL 1 RO LIMIT
06DB 0 0003      GPHLM DC          3         LINE LIMIT MOOIFIER
06DC 0 0000      MLGX7 DC          0         CDNVERT MULTIPLIER
06DD 0 0025      MOD1S DC          37        MODEL 1 TAPE SPEED
06DE 0 0048      MDD2S DC          75        MOOEL 2 TAPE SPEED
06DF 0 0070      INPSE OC          112       MOOEL 3 TAPE SPEED
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX*
*XXXXXXXXXXXXXXXXXXXXX DEVICE STATUS TABLE XXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX*
*
06E0 0 0000      DST    DC          0         0     MAX TIME FOR WRT
06E1 0 0000              DC          0         1     MIN TIME FOR WRT
06E2 0 0000              OC          0         2     MAX TIME FOR RD
06E3 0 0000              OC          0         3     MIN TIME FOR RO
06E4 0 0000              DC          0         4     ACTUAL WRT TIME
06E5 0 0000              DC          0         5     ACTUAL RO TIME
06E6 0 0000              OC          0         6     WO CT
06E7 0 0000              OC          0         7     LAST OSW
06E8 0 0000              OC          0         8     AREA CODE
06E9 0 0000              DC          0         9     FUNCTION
06EA 0 0000              OC          0        10     MOOIFIER
06EB 0 06E8              OC          0ST&8    11     AORS OF AREA CUD
06EC 0 06E9              OC          0ST&9    12     AORS OF FUNCTION
06ED 0 06EA              OC          0ST&10   13     AORS OF MODIFIER
06EE 0 0E9C              DC          0A        14     AORS OF I/O AREA
06EF 0 0000              OC          0        15     NUMBER OF TRACKS
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXX SUPERVISOR ROUTINE XXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
*
*                                     CHECK FOR DRIVE READY
*
06F0 0 6100      MONT4 LOX    1 0           SET IXING FOR DR 0
06F1 0 6600 06E0 LOX    L2 DST
06F3 0 C400 02E1 LO    L SW0           GET CONTROL SWS
06F5 0 4828      BSC      &Z           IS OR O TO BE RUN
06F6 0 701A      MDX      MON25        NO
06F7 0 4400 0807 BSI    L OSW0        SENSE ORIVE             SRC
06F9 0 4804      BSC      E            IS ORIVE READY
06FA 0 706F      MOX      MON11        NO
06FB 0 4400 014F BSI    L BEGIN       SET CONSTANTS             SRC
*
*                                     SET DSTO TO DRIVE 0
*
06FD 0 4400 04D3 BSI    L RST           RESTORE PRINTER
06FF 0 1010      SLA      16
0700 0 D400 02OF STO    L RIO           ZERO RTN NO
0702 0 C400 02E8 LO     L EDIT&4        GET NO TRACKS
0704 0 D20F      STO     2 15         SET IN DST
0705 0 C400 02E4 LO     L EOIT          GET AREA COOE
0707 0 0208      STO     2 8          SET IN OST
0708 0 F500 075F EOR    L1 MONX0      SET OR SELECTION
070A 0 0400 02ED STO    L ACTI          SET
*
*                                     CHECK PROG HALT SW-BIT 15
*
070C 0 C400 02E1 MONT0 LD     L SW0           GET SW FNC 0
070E 0 4804      BSC      E            IS PROG HALT SW ON
070F 0 7006      MOX      MON23        YES
0710 0 7007      MOX      MON22        NO
*
0711 0 C400 0938 MON25 LD     L PGSW          GET PROG SW
0713 0 4820      BSC      Z            IS PROGRAM COMPLETE
0714 0 7070      MOX      MON24        YES
0715 0 7027      MOX      MONTC        NO

```

0716	0	4400	03B2	MON23 BSI L HALT	*

					*
					*
					* CHECK FOR ROUTINE SELECTED IN BIT SWITCHES *
					*
0718	0	C400	02E2	MON22 LD L SW1 GET SW FNC 1	
071A	0	4820		BSC Z IS A RTN SELECTED	
071B	0	7004		MDX MDNTF YES	
071C	0	C400	020F	MDNT6 LD L RID GET RTN NO	
071E	0	8400	0925	MDN10 A L MTTX1 ADD 1	
0720	0	000C		MONTF STO MONT7&1	
0721	0	0400	02DF	STO L RID SAVE	
0723	0	4400	07F0	BSI L RW0 GO REWIND	SRC
0725	0	630A		LDX 3 10 SET UP I/O AREA	
0726	0	C400	0927	LO L MTTX6	
0728	0	0700	0E9C	MONT8 STO L3 IOA	
072A	0	73FF		MOX 3 -1	
072B	0	70FC		MOX MONT8	
072C	0	6700	0000	MUNT7 LOX L3 0 IX 3 # RTN NUMBER	
072E	0	C700	0734	LO L3 MONT9 GET ROUTINE AORS	
0730	0	D400	02E0	STO L RAD SAVE FOR PRINT	
0732	0	4F80	0734	BSC I3 MONT9 TRANSFER TO RTN	
					*
					*
					* TABLE OF ROUTINE ADDRESSES *
					*
0734	0	0771		MONT9 OC MONR1 ERROR	
0735	0	093C		DC MTT01 ROUTINE NUMBER 1	
0736	0	0976		DC MTT02 2	
0737	0	09C2		DC MTT03 3	
0738	0	09C9		OC MTT04 4	
0739	0	09CF		OC MTT05 5	
073A	0	0C71		DC MTT07 6	
073B	0	0771		OC MONR1 PROGRAM COMPLETE	
073C	0	0007		PGCM OC PGCM-MONT9-1 NO OF ROUTINES	
					*
					*
					* SET UP TO CK DR 1 *
					*
0730	0	6101		MONTC LDX 1 1 SET IXING FOR OR 1	
073E	0	6600	06E0	LDX L2 OST	
0740	0	C400	02E1	LO L SW0 GET CONTROL SWS	
0742	0	1001		SLA 1	
0743	0	4828		BSC &Z IS OR 1 TO BE RUN	
0744	0	70CC		MOX MON25 NO	
0745	0	C400	02E9	LO L EDIT&5 GET NO TRKS-OR 1	
0747	0	4C28	0711	BSC L MON25,&Z BRANCH # NOT AVAIL	
0749	0	4400	0807	BSI L DSW0 GO SENSE OR	SRC
074B	0	4804		8SC E IS DRIVE READY	
074C	0	7014		MOX MONTE NO	
074D	0	4400	014F	BSI L BEGIN SET CONSTANTS	SRC
					*
					*
					* SET OSTO TO DRIVE 1 *
					*
074F	0	4400	0403	BSI L RST RESTORE PRINTER	
0751	0	C400	02E9	LO L EOIT&5 GET NO TRACKS	
0753	0	D20F		STO 2 15 SET IN OST	
0754	0	C400	02E4	LO L EDIT GET AREA COOE	
0756	0	D208		STO 2 8 SET IN OST	
0757	0	F500	075F	EOR L1 MONX0 SET OR SEL	
0759	0	0400	02E0	STO L ACTI	
075B	0	1010		SLA 16	
075C	0	D400	020F	STO L RIO SAVE	
075E	0	70AD		MOX MONT0 GO TEST OR 1	
075F	0	0000		MONX0 DC 0 OR 0 SELECTION	
0760	0	0020		OC /0020 DR 1 SELECTION	
					*

88915510
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88915990
88916000
88916010
88916020
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88916170
88916180

```

*
*
ORIVE 1 IS NOT REAOY
*
0761 0 4400 083C MONTE BSI L MLG OR 1 NOT READY
0763 0 0F10 OC MSG5&4
0764 0 0E4B OC NOTE1&26
0765 0 C001 C001 OC /C001 IO C1
0766 0 0000 OC /0000 LINE 0-FORM 0
0767 0 7401 093B MOX L PGSW,1
0769 0 70A7 MOX MON25
*
*
ORIVE 0 IS NOT REAOY
*
076A 0 4400 083C MON11 BSI L MLG OR 0 NOT REAOY
076C 0 0F10 OC MSG5&4
076D 0 0E4B OC NOTE1&26
076E 0 C000 C000 OC /C000 IO C0
076F 0 0000 OC /0000 LINE 0-FORM 0
0770 0 70A0 MOX MON25
*
*
RETURN FROM ROUTINES
CHECK FOR ALL RTNS RUN
*
0771 0 4400 0306 MONR1 BSI L ROSWS REAO SWS
0773 0 C400 020F LO L RIO GET RTN NO
0775 0 90C6 S PGCM SUB TOTAL RTNS
0776 0 4820 BSC Z ARE ALL RTNS RUN
0777 0 7094 MOX MONTO NO
*
*
ALL ROUTINES COMPLETE-CK
FOR PROGRAM COMPLETE
*
0778 0 4400 07F0 BSI L RW0 REWIND ORIVE
077A 0 7401 093B MOX L PGSW,1
077C 0 4400 083C BSI L MLG ALL RTNS RUN
077E 0 0F21 OC MSG6&4
077F 0 0F49 OC MSG15&8
0780 0 A000 A000 OC /A000 IO A0
0781 0 0000 OC /0000 LINE 0-FORM 0
0782 0 7100 MOX 1 0 WAS RUN ON ORIVE 0
0783 0 7080 MOX MON25
0784 0 70B8 MOX MONTC YES
*
*
PROGRAM IS COMPLETE
*
0785 0 4400 083C MON24 BSI L MLG PROGRAM COMPLETE
0787 0 0F21 OC MSG6&4
0788 0 0F51 OC MSG16&8
0789 0 A001 A001 OC /A001 IO A1
078A 0 0000 OC /0000 LINE 0-FORM 0
*****
078B 0 4400 0662 BSI L ENO TERMINATE *
*****
*
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXX TIME0 WRITE ROUTINE XXXXXXXX
XXXXXXXXXXXXXXXXXXXXX ROUTINE CALL XXXXXXXX
XXXXXXXXXXXXXXXXXXXXX BSI L TMWRT XXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
078D 0 0000 TMWRT OC 0 SE
078E 0 C030 LO TWRX3 MODIFY TIME RTN/WRT
078F 0 000F ST0 TWR9
0790 0 C500 07B0 TMR01 LO L1 TWRX1 GET SENSE WO CTR
0792 0 F400 02E4 EOR L E0IT SET AREA CODE
0794 0 0026 ST0 SNWC1 SET IN IOCC
0795 0 6B10 STX 3 TMWR0&1 SAVE IX 3
0796 0 1010 SLA 16

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88916190
88916200
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0797	0	D024	
0798	0	4400	0373
079A	0	06EB	
079B	0	0C00	07BA
0790	0	8400	07C7
079F	0	4C30	07A5
07A1	0	7401	07BC
07A3	0	4C00	079B
07A5	0	6700	0000
07A7	0	3005	
07A8	0	4C00	082A
07AA	0	C207	
07AB	0	E400	092B
07A0	0	4820	
07AE	0	7002	
07AF	0	4C80	0780
07B1	0	4400	083C
07B3	0	0F41	
07B4	0	0F09	
07B5	0	E004	
07B6	0	0002	
07B7	0	4C00	071C
07BA		0000	
07BA	0	0000	
0788	0	0000	
07BC	0	0000	
07BD	0	0710	
078E	0	0730	
07BF	0	4C30	
07C0	0	0000	
07C1	0	C006	
07C2	0	000C	
07C3	0	70CC	
07C4	0	C0FB	
07C5	0	00C7	
07C6	0	70E3	
07C7	0	000A	
07C8	0	4C10	

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STO          TWRX0      CLEAR COUNT
*****
BSI L OCC GO WRITE *
TMWR1 DC OST&11 AOR OF STRING *
*****
TIME A DOUBLE CHANGE IN
THE WORD COUNTER
TMWR2 XIO L SNWC SENSE WD CTR
A L TMRX2 AOO OESIREO
TMWR9 BSC L TMWR0,-Z HAS CTR CHANGED
TMWR3 MOX L TWRX0,1 NO-STEP COUNT
BSC L TMWR2 LOOP
TMWR0 LOX L3 0 RESTURE IX 3
WAIT5 WAIT 5 WAIT FOR R0 OR WRT
BSC L INTR
INTERRUPT RETURN
TMWR4 LO 2 7 GET LAST OSW
ANO L MTTY8 CK FOR CORRECT
BSC Z IS IT CORRECT
MOX TMWR6 NO
TMWR8 BSC I TMWRT RETURN
NOT CORRECT-ABORT THE TEST
TMWR6 BSI L MLG TEST ABORTEO
OC MSG14&4
OC MSG2&8
E004 OC /E004 IO E4
OC /0002 LINE 0- FORM 2
TMWR7 BSC L MONT6 GO RESTART THE RTN
SENSE WORD CTR IOCC
BSS E 0
SNWC OC 0
SNWC1 DC 0
WRITE TIME
TWRX0 OC 0
TWRX1 OC /0710 SENSE WO CTR-OR 0
TWR2 OC /0730 1
TWRX3 OC /4C30
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXX TIMED READ ROUTINE XXXXXXXXX
XXXXXXXXXXXXXXXXXXXX ROUTINE CALL XXXXXXXXX
XXXXXXXXXXXXXXXXXXXX BSI L TMROT XXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
TMROT OC 0 SE
LO TMRX3 MODIFY TIME RTN/R0
STO TMWR9
MOX TMRD1 GO TIME A READ
INTERRUPT RETURN
TMR04 LO TMROT GET RETURN
STO TMWRT SET
MDX TMWR4
TMRX2 OC /000A
TMRX3 OC /4C10
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2400 TIMING TEST

2400 TIMING TEST

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XXXXXXXXXXXXXXXXX CDMON BACKSPACE ROUTINE XX 88917550
XXXXXXXXXXXXXXXXX ROUTINE CALL XX 88917560
XXXXXXXXXXXXXXXXX BSI L 8SP XX 88917570
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88917580
* 88917590
BSP DC 0 SE 88917600
STX 3 BSP2&I SAVE IX 3 88917610
BSP3 BSI DSW0 SENSE DRIVE SRC 88917620
BSC L BSP3,E BRANCH # NOT READY 88917630
SRA 3 88917640
8SC L BSP2,E IS DR AT LD PT 88917650
MDX BSP4 NO 88917660
BSP2 LDX L3 0 RESTORE IX 3 88917670
BSC I BSP EXIT SX 88917680
* 88917690
* DRIVE IS NDT READY 88917700
* 88917710
* 88917720
* DRIVE IS NDT AT LOAO POINT 88917730
* 88917740
8SP4 LD L1 BSPX1 GET MODIFIER 88917750
STO 2 10 SET IN DST 88917760
LD RWDX0 GET FUNCTION 88917770
STO 2 9 SET IN DST 88917780
***** 88917790
8SI L DCC GD BACKSPACE * 88917800
BSP6 DC DST&11 ADRS DF STRING * 88917810
***** 88917820
WAIT6 WAIT 6 WAIT FOR BSP INTRPT 88917830
MDX BSP12 88917840
* 88917850
* BACKSPACE CONSTANTS 88917860
* 88917870
BSPX1 DC /000B 88917880
DC /002B 88917890
* 88917900
* CHECK DRIVE 88917910
* 88917920
BSP12 LD 2 7 GET LAST DSW 88917930
BSC E IS DRIVE READY 88917940
MDX BSP13 ND 88917950
SRA 6 88917960
BSC E IS DP COMPLETE ON 88917970
MDX BSP2 88917980
BSP13 BSI L MLG BACKSPACE ERROR 88917990
OC MSG13&4 88918000
OC MSG2&8 88918010
E003 OC /E003 ID E3 88918020
OC /0002 LINE 0-FORM 2 88918030
***** 88918040
BSI L ENO TERMINATE * 88918050
***** 88918060
* 88918070
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88918080
XXXXXXXXXXXXXXXXXXXX COMMON REWIND ROUTINE XXXX 88918090
XXXXXXXXXXXXXXXXXXXX ROUTINE CALL XXXXX 88918100
XXXXXXXXXXXXXXXXXXXX BSI L RWD XXXXX 88918110
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88918120
* 88918130
RWD OC 0 SE 88918140
RWD3 BSI OSW0 SENSE ORIVE SRC 88918150
LO RWOX0 GET FUNCTION 88918160
STO 2 9 SET IN OST 88918170
LO L1 RWOX1 GET MODIFIER 88918180
STO 2 10 SET IN DST 88918190
LD 2 7 GET LAST OSW 88918200
BSC L RWD3,E BRANCH-OR NOT READY 88918210
SRA 3 YES 88918220
07C9 0 0000
07CA 0 6B08
07CB 0 403B
07CC 0 4C04 07C8
07CE 0 1803
07CF 0 4C04 07D2
07D1 0 7004
07D2 0 6700 0000
07D4 0 4C80 07C9
07D6 0 C500 07E0
07D8 0 D20A
07D9 0 C02A
07DA 0 D209
07DB 0 4400 0373
07DD 0 06E8
07DE 0 3006
07DF 0 7002
07E0 0 000B
07E1 0 002B
07E2 0 C207
07E3 0 4804
07E4 0 7003
07E5 0 1806
07E6 0 4804
07E7 0 70EA
07E8 0 4400 083C
07EA 0 0F30
07EB 0 0F09
07EC 0 E003
07ED 0 0002
07EE 0 4400 0662
07F0 0 0000
07F1 0 4015
07F2 0 C011
07F3 0 0209
07F4 0 C500 0805
07F6 0 D20A
07F7 0 C207
07F8 0 4C04 07F1
07FA 0 1803
```

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07FB 0 4C04 07FE BSC L RWD2,E IS DR AT LOAD PT 88918230
07FD 0 7002 MDX RWD4 NO 88918240
07FE 0 4C80 07F0 RWD2 BSC I RWD RETURN TO PROG SX 88918250
* 88918260
* DRIVE IS NOT AT LD 88918270
* 88918280
***** 88918290
RWD4 8SI L DCC GO REWIND $ 88918300
DC DST&11 ADRS OF STRING * 88918310
***** 88918320
MDX RWD3 88918330
* 88918340
* REWIND CONSTANTS 88918350
* 88918360
RWDX0 DC /0400 FUNCTION CNTRL 88918370
RWDX1 DC /0004 RWD DR 0 88918380
DC /0024 RWD DR 1 88918390
* 88918400
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88918410
XXXXXXXXXXXXXXXXXXXX SENSE DEVICE ROUTINE X 88918420
XXXXXXXXXXXXXXXXXXXX ROUTINE CALL X 88918430
XXXXXXXXXXXXXXXXXXXX BSI L DSW X 88918440
XXXXXXXXXXXXXXXXXXXX DC 0 DR 1 X 88918450
XXXXXXXXXXXXXXXXXXXX 0 # RETURN WHEN READY X 88918460
XXXXXXXXXXXXXXXXXXXX 1 # RETURN WITH SENSE WD X 88918470
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88918480
* 88918490
DSW0 DC 0 SE 88918500
LDX 3 2 SET FOR DOUBLE SENSE 88918510
DSW5 LD L1 DSWX1 GET MODIFIER 88918520
STO 2 10 SET MDD 88918530
LD 2 11 GET ADR AREA CODE 88918540
STD DSW1 SET IN CALL 88918550
LD 2 13 GET MOD ADR 88918560
STD DSW2 SET IN CALL 88918570
DSW7 STX 3 DSW8&1 SAVE INDEX 3 88918580
***** 88918590
BSI L DIND GO SENSE * 88918600
DSW1 DC 0 AREA CODE ADRS * 88918610
DSW2 DC 0 MODIFIER * 88918620
DC 0 LOAD A RETURN * 88918630
***** 88918640
DSW8 LDX L3 0 RESTORE INOEX 3 88918650
MDX 3 -1 IS THIS THE SECOND 88918660
MOX DSW7 NO-GO SENSE AGAIN 88918670
STO 2 7 SET IN OST 88918680
BSC I DSW0 RETURN SX 88918690
* 88918700
* CONSTANTS USED BY OSW 88918710
* 88918720
OSWX1 DC 0 MODIFIER FOR DRIVE 0 88918730
DC /0020 MODIFIER FOR ORIVE 1 88918740
* 88918750
* 88918760
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88918770
XXXXXXXXXXXXXXXXXXXX ROUTINE TO SET NORMAL XX 88918780
XXXXXXXXXXXXXXXXXXXX INTERRUPT RETURN XX 88918790
XXXXXXXXXXXXXXXXXXXX ROUTINE CALL XX 88918800
XXXXXXXXXXXXXXXXXXXX BSI L INTRT XX 88918810
XXXXXXXXXXXXXXXXXXXX OC RETURN AORS XX 88918820
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88918830
* 88918840
INTRT OC 0 SE 88918850
STX L1 INTR&1 SAVE IX 1 88918860
LO 1 INTRT GET FINAL RETURN 88918870
STO INTR2 SAVE 88918880
MDX L INTRT,1 ADD 1 TO RETURN 88918890
BSC 1 INTRT EXIT SX 88918900
0800 0 4400 0373
0802 0 06EB
0803 0 70ED
0804 0 0400
0805 0 0004
0806 0 0024
0807 0 0000
0808 0 6302
0809 0 C500 081D
080B 0 D20A
080C 0 C20B
080D 0 D005
080E 0 C20D
080F 0 D004
0810 0 6B06
0811 0 4400 03A2
0813 0 0000
0814 0 0000
0815 0 0000
0816 0 6700 0000
0818 0 73FF
0819 0 70F6
081A 0 D207
081B 0 4C80 0807
0810 0 0000
081E 0 0020
081F 0 0000
0820 0 6D00 082B
0822 0 C480 081F
0824 0 D004
0825 0 7401 081F
0827 0 4C80 081F
```

0830	0	0000		PRDWT	DC	0				SE
0831	0	D209			STO	2	9		SET FUNC	
0832	0	C500	081D		LD	L1	DSWX1		GET MODIFIER	
0834	0	D20A			STO	2	10		SET IN DST	
0835	0	C400	0929		LD	L	MTTY4		GET WORD COUNT OF 5	
0837	0	D206			STO	2	6		SET WD CT	
0838	0	D400	0E9C		STO	L	IOA		SET IN IO AREA	
083A	0	4C80	0830		RSC	I	PRDWT			SY

083C	0	0000	MLG	DC	0	
083D	0	4400	0306	BSI	L	RDSWS
083F	0	C400	02E1	LD	L	SWO
0841	0	1802		SRA		2
0842	0	4C04	08C7	BSC	L	MLG18,E
0844	0	4400	0EC9	BSI	L	LDSP
0846	0	6780	083C	LDX	I3	MLG
0848	0	C300		LD	3	0
0849	0	D400	08FB	STO	L	MOD4
084B	0	C301		LD	3	1
084C	0	D400	0901	STO	L	MLGX0
084E	0	6D00	08B8	STX	L1	MLG05&1
0850	0	C400	02DF	LD	L	RID
0852	0	D400	08FA	STO	L	MOD3
0854	0	6D00	08FC	STX	L1	MOD5
0856	0	C400	02DE	LD	L	PID
0858	0	D400	08F9	STO	L	MOD2
085A	0	C400	0901	LD	L	MLGX0
085C	0	1008		SLA		8
085D	0	1808		SRA		8
085E	0	D001		STO		MLG00&1
085F	0	6700	0000	MLG00	LDX	L3 0
0861	0	4F80	0863	BSC	I3	MLG02
0863	0	0869		MLG02	DC	FORM0
0864	0	08D1		DC		FORM1
0865	0	08D9		DC		FORM2
0866	0	08DF		DC		FORM3
0867	0	08E5		OC		FORM4
0868	0	08EB		DC		FORM5

```
FORM0 LDX      3 0
```

SET LINE NO AND WD CT

```
086A 0 C500 02EA      MLG03 LD      L1 EDIT&6      GET DR MODEL
086C 0 4820           BSC      Z      IS THIS MODEL 3
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Address	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op417	Op418	Op419
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2400 TIMING TEST

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08CA 0 C400 02E1 CKHLT LO L SWO GET SWS 8B920270
08CC 0 1801 SRA 1 8B920280
08CO 0 4404 03B2 BSI L HALT,E BRANCH IF HLT ON ERR 8B920290
08CF 0 70F5 MOX MLGE CONTINUE 8B920300
0800 0 000E K000E OC /000E 8B920310
* FORM IS ONE 8B920320
* 8B920330
* 8B920340
0801 0 C201 FORM1 LO 2 1 GET MIN WRT TIME 8B920350
0802 0 002A STO M0000 SET IN MSG 8B920360
0803 0 C204 LO 2 4 GET ACTUAL WRT TIME 8B920370
0804 0 0029 STO M0001 SET IN MSG 8B920380
0805 0 C200 LO 2 0 GET MAX WRT TIME 8B920390
0806 0 0028 COM00 STO M0002 SET IN MSG 8B920400
0807 0 6303 LOX 3 3 SET IX FOR WO CT 8B920410
0808 0 7091 MOX MLG03 GO COMPLETE MSG 8B920420
* 8B920430
* FORM IS 2 8B920440
* 8B920450
0809 0 C207 FORM2 LO 2 7 GET OSW 8B920460
080A 0 0022 STO M0000 SET IN MSG 8B920470
0808 0 1010 SLA 16 ZERO ACCUM 8B920480
080C 0 001B STO M001 SET TO HEX 8B920490
0800 0 6301 COM01 LOX 3 1 SET IX FOR WO CT 8B920500
080E 0 7000 MOX MLG07 GO COMPLETE MESSAGE 8B920510
* 8B920520
* FORM IS 3 8B920530
* 8B920540
080F 0 C203 FORM3 LO 2 3 GET MIN RO TIME 8B920550
08E0 0 001C STO M0000 SFT IN MSG 8B920560
08E1 0 C205 LO 2 5 GET ACTUAL RO TIME 8B920570
08E2 0 001B STO M0001 SET IN MSG 8B920580
08E3 0 C202 LO 2 2 GET MAX RO TIME 8B920590
08E4 0 70F1 MOX COM00 GO COMPLETE MSG 8B920600
* 8B920610
* FORM IS 4 8B920620
* 8B920630
08E5 0 C048 FORM4 LO MTTYC GET 10 MSEC GOT AVG 8B920640
08E6 0 0016 STO M0000 SET IN MSG 8B920650
08E7 0 C048 LO MTTY0 GET VAR GOT AVG 8B920660
08E8 0 0015 STO M0001 SET IN MSG 8B920670
08E9 0 C048 LO MTTYE GET MIN GOT AVG 8B920680
08EA 0 70E8 MOX COM00 GO COMPLETE MSG 8B920690
* 8B920700
* FORM IS 5 8B920710
* 8B920720
08EB 0 C400 0089 FORM5 LO L MT7X5 GET AVERAGE CREEP 8B920730
08E0 0 0010 STO M0001 SET IN MSG 8B920740
08EE 0 C400 0090 LO L MT7Y1 GET LOW VALUE 8B920750
08F0 0 000C STO M0000 SET IN MSG 8B920760
08F1 0 C400 0091 LO L MT7Y2 GET HIGH VALUE 8B920770
08F3 0 0008 STO M0002 SET IN MSG 8B920780
08F4 0 6303 LOX 3 3 SET TO WORD COUNT 8B920790
08F5 0 4C00 086A BSC L MLG03 GO COMPLETE MSG 8B920800
* 8B920810
* LOG MESSAGE STORAGE 8B920820
* 8B920830
08F7 0 0000 M000 OC 0 LINE NO/WORD CT 8B920840
08F8 0 0001 M001 OC 1 HEX/OEC 8B920850
08F9 0 0000 M002 OC 0 PROGRAM NUMBER 8B920860
08FA 0 0000 M003 OC 0 ROUTINE NUMBER 8B920870
08F8 0 0000 M004 OC 0 MESSAGE IO 8B920880
08FC 0 0000 M005 OC 0 DRIVE NUMBER 8B920890
08F0 0 0000 M0000 OC 0 DATA WORD 0 8B920900
08FE 0 0000 M0001 OC 0 1 8B920910
08FF 0 0000 M0002 OC 0 2 8B920920
0900 0 0000 M0003 OC 0 3 8B920930
* 8B920940
```

2400 TIMING TEST

```
* ROUTINE CONSTANTS 8B920950
* 8B920960
0901 0 0000 MLGX0 OC 0 LINE AND FORM TEMP 8B920970
0902 0 0004 MLGX1 OC 4 WO CT # 4 8B920980
0903 0 0005 MLGX2 OC 5 5 8B920990
0904 0 0006 MLGX3 OC 6 6 8B921000
0905 0 0007 MLGX4 OC 7 7 8B921010
0906 0 0002 MLGX8 OC 2 8B921020
0907 0 03E8 MLGX9 OC 1000 8B921030
0908 0 0064 OC 100 8B921040
* 8B921050
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921060
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921070
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921080
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921090
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921100
* 8B921110
CN1 OC 0 SE 8B921120
LO RTN1X GET AORS 8B921130
STO STCN1&1 SET 8B921140
BSI STCN SET OST SRC 8B921150
BSC I CN1 EXIT SX 8B921160
RTN1X OC CON-1 OR 0 AORS 8B921170
* 8B921180
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921190
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921200
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921210
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921220
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921230
* 8B921240
CN2 OC 0 SE 8B921250
LO RTN2X GET AORS 8B921260
STO STCN1&1 SET 8B921270
BSI STCN SET OST SRC 8B921280
BSC I CN2 EXIT SX 8B921290
RTN2X OC CON1-1 8B921300
* 8B921310
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921320
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921330
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921340
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921350
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921360
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921370
* 8B921380
STCN OC 0 SE 8B921390
LOX 3 4 IX 3 # NUMBER WOS 8B921400
STCN1 LO L3 0 GET CONSTANT 8B921410
STO 2 0 SET IN OST 8B921420
MOX 3 -1 OECR IX 3 8B921430
MOX STCN2 8B921440
MOX STCN3 FINISHEO 8B921450
STCN2 MOX 2 1 INCR IX 2 8B921460
MOX STCN1 LOOP 8B921470
STCN3 LOX L2 OST RESTORE IX 2 8B921480
BSC I STCN EXIT SX 8B921490
* 8B921500
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921510
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921520
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921530
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B921540
* 8B921550
MTTX1 OC /0001 8B921560
MTTX3 OC 0 TEMP 8B921570
MTTX6 OC /FFFF 8B921580
MTTX9 OC /0500 FUNCTION # WRT 8B921590
MTTY4 OC /400A 8B921600
MTTY5 OC 47 CONSTANT 8B921610
MTTY8 OC /2003 OSW CK 8B921620
MTTY9 OC /0600 FUNCTION # READ
```

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```
0920 0 0E6C      MTTYB OC      TOTA&3      AORS OF COUNTS      88921630
092E 0 0000      BSS      E      0      88921640
092E 0 0000      MTTYC OC      0      10 MSEC AVG      88921650
092F 0 0000      OC      0      88921660
0930 0 0000      MTTYO OC      0      VARIABLE AVG      88921670
0931 0 0000      OC      0      88921680
0932 0 0000      MTTYE DC      0      MIN AVG      88921690
0933 0 0000      OC      0      88921700
0934 0 0000      MTTYA OC      0      88921710
0935 0 0000      OC      0      88921720
0936 0 4C00 0B2E  MTTYF BSC L MT51E      PROG MODIFIER      88921730
0938 0 00EB      MTTZO OC      235      CONSTANT      88921740
0939 0 9400 0C43  MTTZ1 S      L MT5XE      88921750
093B 0 0000      PGSW OC      0      88921760
*      88921770
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921780
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921790
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921800
*      88921810
MTT01 LO      MTTX1      GET 0001      88921820
      STO      MTTX3      SET RTN 1 SW      88921830
      BSI      CN1      GO SET CONSTANTS      SRC 88921840
*      88921850
*      PORTION COMMON TO RTNS 1-3      88921860
*      PREPARE TO WRITE      88921870
*      88921880
093F 0 00E8      MT101 LO      MTTX9      GET WRT FNC      88921890
0940 0 4400 0830  BSI L      PROWT      GO SET UP      SRC 88921900
*      88921910
*      DETERMINE RTN BEING RUN      88921920
*      88921930
      LO      MTTX3      GET RTN 1 SW      88921940
      BSC      Z      IS THIS RTN 1      88921950
      MOX      MT102      YES-SKIP WRT      88921960
*      88921970
*      ROUTINE 3 RUNNING      88921980
*      88921990
      BSI L      INTRT      GO SET INTR RETURN      SRC 88922000
      OC      TMWR4      88922010
      BSI L      TMWRT      WRITE      88922020
*      88922030
*      COMMON TO RTNS 1 AND 3      88922040
*      88922050
*      88922060
*      TIME A WRITE      88922070
*      88922080
094A 0 4400 081F  MT102 BSI L      INTRT      GO SET RETURN      SRC 88922090
094C 0 07AA      OC      TMWR4      88922100
0940 0 4400 0780  BSI L      TMWRT      GO TIME A WRT      SRC 88922110
*      88922120
*      CHECK THE TIMING      88922130
*      88922140
      LO      MTTX3      GET RTN 1 SW      88922150
      BSC      Z      IS THIS RTN 1      88922160
      MOX      MT105      YES      88922170
      LO L      TWRX0      GET TIME      88922180
      MT108 STO 2 4      SAVE      88922190
      S      2 0      SUB MAX TIME      88922200
      BSC      -Z      IS TIME TOO LONG      88922210
      MOX      MT104      YES      88922220
      LO      2 1      GET MIN TIME      88922230
      S      2 4      SUB ACTUAL TIME      88922240
      BSC      -Z      IS TIME TOO SHORT      88922250
      MOX      MT104      YES      88922260
*      88922270
*      CHECK PRINT REQUEST SW      88922280
*      88922290
095C 0 C400 02E1  LO L      SWO      GET SW FNC 0      88922300
```

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```
095E 0 1809      SRA      9      88922310
095F 0 4804      BSC      E      IS DATA PRINT REQ. 88922320
0960 0 700E      MDX      MT107      YES      88922330
*      88922340
*      ROUTINE IS COMPLETE      88922350
*      88922360
0961 0 4C00 0771  MT103 BSC L      MONR1      EXIT      88922370
0963 0 C400 078C  MT105 LO L      TWRX0      88922380
0965 0 9400 0679  S      L      MT1X0      SUB TURNAROUND 88922390
0967 0 70EC      MOX      MT108      88922400
*      88922410
*      ERROR FOUND IN TIMING      88922420
*      88922430
0968 0 4400 083C  MT104 BSI L      MLG      PRINT ERROR      88922440
096A 0 0F25      OC      MSG7&4      88922450
096B 0 0F11      OC      MSG3&8      88922460
096C 0 E005      E005 OC      /E005      10 E5      88922470
096D 0 0001      OC      /0001      LINE 0-FORM 1 88922480
096E 0 70F2      MOX      MT103      88922490
*      88922500
*      DATA PRINT IS REQUESTED      88922510
*      88922520
096F 0 4400 083C  MT107 BSI L      MLG      PRINT DATA      88922530
0971 0 0F25      OC      MSG7&4      88922540
0972 0 0F11      OC      MSG3&8      88922550
0973 0 A002      A002 OC      /A002      ID A2      88922560
0974 0 0001      OC      /0001      LINE 0-FORM 1 88922570
0975 0 70EB      MOX      MT103      88922580
*      88922590
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88922600
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88922610
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88922620
*      88922630
MTT02 LO      MTTX1      GET 0001      88922640
      STO      MTTX3      SET RTN 2 SW      88922650
      BSI L      CN1      GO SET CONSTANTS      SRC 88922660
*      88922670
*      COMMON TO RTNS 2 AND 4      88922680
*      88922690
097A 0 C0AD      MT200 LO      MTTX9      GET WRT FNC      88922700
097B 0 4400 0830  BSI L      PROWT      GO SET UP      SRC 88922710
0970 0 4400 081F  BSI L      INTRT      SET INTRPT RETURN 88922720
097F 0 07AA      OC      TMWR4      88922730
0980 0 4400 0780  BSI L      TMWRT      WRITE      88922740
*      88922750
*      DETERMINE RTN BEING RUN      88922760
*      88922770
      LO      MTTX3      GET RTN 2 SW      88922780
      BSC      Z      IS THIS RTN 2      88922790
      MOX      MT203      YES      88922800
*      88922810
*      THIS IS RTN 4      88922820
*      88922830
      BSI L      TMWRT      WRITE      88922840
*      88922850
*      WRITE IS COMPLETE      88922860
*      88922870
0987 0 4400 07F0  BSI L      RWO      GO REWIND      SRC 88922880
0989 0 C0A2      LO      MTTY9      GET READ FNCT      88922890
098A 0 4400 0830  BSI L      PROWT      GO SET UP      SRC 88922900
098C 0 4400 081F  BSI L      INTRT      GO SET TIME INT RET 88922910
      OC      TMR04      88922920
      LO L      MTTX3      GET RTN 2 SW      88922930
      BSC      Z      IS THIS RTN 2      88922940
      MOX      MT209      YES      88922950
      BSI L      TMR0T      GO READ      SRC 88922960
0992 0 7002      88922970
0993 0 4400 07C0  MT209 BSI L      TMR0T      GO REAO      SRC 88922980
0995 0 4400 07C0  *
```

DATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
C NO. 415178 415233 411731 411857 411875 431319 431319A
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C NU. 415178 415233 411731 411857 411875 431319 431319A
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2400 TIMING TEST

2400 TIMING TEST

```
* REAO IS COMPLETE
*
0997 0 C400 0926      LO  L  MTTX3   GET RTN 2 SW
0999 0 4820           BSC  Z       IS THIS RTN 2
099A 0 7011           MOX  MT201   YES
099B 0 C400 07BC      LO  L  TWRX0   GET TIME
0990 0 0205           MT202 STO 2 5   SAVE
099E 0 9202           S    2 2     SUB MAX TIME
099F 0 4830           BSC  -Z      IS TIME TOO LONG
09A0 0 7013           MOX  MT204   YES
09A1 0 C203           LO  2 3     GET MIN TIME
09A2 0 9205           S    2 5     SUB ACTUAL
09A3 0 4830           BSC  -Z      IS TIME TOO SHORT
09A4 0 700F           MOX  MT204   YES

* CHECK PRINT REQ SW
*
09A5 0 C400 02E1      LO  L  SW0     GET SW FNC 0
09A7 0 1809           SRA  9       IS SW SET
09A8 0 4804           BSC  E       YES
09A9 0 7011           MOX  MT205

* PRINT NOT REQUESTED
*
09AA 0 C400 0771      MT206 BSC L  MONR1   EXIT
09AC 0 C400 07BC      MT201 LO  L  TWRX0   GET TIME
09AE 0 9400 0679      S    L  MT1X0
09B0 0 70EC           MOX  MT202

* THIS IS RTN 2
*
09B1 0 4400 07F0      MT203 BSI L  RW0     REWINO ORIVE
09B3 0 7005           MOX  MT208      SRC

* TIMING ERROR
*
09B4 0 4400 083C      MT204 BSI L  MLG     PRINT ERROR
09B6 0 0F29           DC    MSG8&4
09B7 0 0F11           OC    MSG3&8
09B8 0 E006           E006 OC    /E006   IO E6
09B9 0 0003           OC    /0003   LINE 0-FORM 3
09BA 0 70EF           MOX  MT206

* PRINT REQUEST SW IS ON
*
09BB 0 4400 083C      MT205 BSI L  MLG     PRINT OATA
09B0 0 0F29           OC    MSG8&4
09BE 0 0F11           OC    MSG3&8
09BF 0 A003           A003 OC    /A003   IO A3
09C0 0 0003           OC    /0003   LINE 0-FORM 3
09C1 0 70E8           MOX  MT206

*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXX ROUTINE NUMBER THREE XXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
09C2 0 1010          MTT03 SLA 16      ZERO ACCUM      SE
09C3 0 0400 0926      STO L  MTTX3   CLEAR RTN 1 SW
09C5 0 4400 0910      BSI L  CN2     GO SET CONSTANTS  SRC
09C7 0 4C00 093F      BSC L  MT101   GO TO COMMON RTN

*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXX ROUTINE NUMBER FOUR XXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
09C9 0 1010          MTT04 SLA 16      ZERO ACCUM      SE
09CA 0 0400 0926      STO L  MTTX3   CLEAR RTN 2 SW
09CC 0 4400 0910      BSI L  CN2     GO SET CONSTANTS  SRC
```

```
09CE 0 70AB          MDX  MT200   GO TO COMMON RTN
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXX ROUTINE NUMBER FIVE XXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
* SET UP FOR WRITE
*
MTT05 SLA 16      ZERO ACCUM
STO L  MTTX3
RTE 16      CLEAR Q REG
SLA 16      CLEAR ACCUM
STO L  MTTYC   CLEAR ALL TOTALS
STO L  MTTY0
STO L  MTTYE
STO L  GRL1&1
STO L  MT526&1
STO L  LNSW
LOX 3 -47
MT573 STO L3 TOTA&50
MOX 3 1      OECR IX 3
MOX MT573    LOOP
LO L  MT730   RESTORE RTN CONSTANT
STO L  PRSW
LOO L  MT506-1
STO L  OLYC
LO L  AORS1
STO MT50A&1
LO L  K0086
STO MT509&1
LOO L  MT5Q6
STO MT5Q7
LO L  MT5X3
STO L  SW
SLA 16
STO L  LINE
STO L  MTTX3
STO L  MT5X4
LD L  MTTZ1
STO L  MT51D
LO L  MTTZ1&1
STO L  MT510&1
LO L  MTTX9
BSI L  PROWT   GO SET UP
BSI L  INTRT   GO SET TIME INT RET  SRC
OC TMWR4      SRC

* WRITE FIRST RECORO
*
MT502 BSI L  TMWRT   WRITE
*
* OELAY 10 MILLISEC
*
LO L  MT540   GET OELAY CT
STO L  OLY1   SET
*****
BSI L  OELAY   GO OELAY
OC OLY1-1     AORS OF COUNT
*****
* WRITE SECONO RECORO
*
MT503 BSI L  TMWRT   WRITE
MOX MT5Q7   BRANCH
*
* VARIABLE OELAY .5 MILLISEC
* TO 5 SECS.
```

8B923670
8B923680
8B923690
8B923700
8B923710
8B923720
8B923730
8B923740
8B923750
8B923760
8B923770
8B923780
8B923790
8B923800
8B923810
8B923820
8B923830
8B923840
8B923850
8B923860
8B923870
8B923880
8B923890
8B923900
8B923910
8B923920
8B923930
8B923940
8B923950
8B923960
8B923970
8B923980
8B923990
8B924000
8B924010
8B924020
8B924030
8B924040
8B924050
8B924060
8B924070
8B924080
8B924090
8B924100
8B924110
8B924120
8B924130
8B924140
8B924150
8B924160
8B924170
8B924180
8B924190
8B924200
8B924210
8B924220
8B924230
8B924240
8B924250
8B924260
8B924270
8B924280
8B924290
8B924300
8B924310
8B924320
8B924330
8B924340



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2400 TIMING TEST

2400 TIMING TEST

```
*
OA1C 0000
OA1C 0 1000
OA1D 0 1000
OA1E 0 4400 078D
OA20 0 CC00 0C36
OA22 0 DC00 0C34
OA24 0 4400 038C
OA26 0 0C34
OA27 0 6600 06E0
OA29 D 4400 078D
OA2B 0 4400 078D
OA2D 0 6700 0056
OA2F 0 CF00 067E
OA31 0 DC00 0C36
OA33 0 74FE 0A2E
OA35 0 70DA
OA36 0 C400 0C3A
OA38 0 D0F5
OA39 0 C400 0C38
OA3B 0 D0F4
OA3C 0 CC00 0606
OA3E 0 DC00 0C36
OA40 0 74FF 0462
OA42 0 7014
OA43 0 C400 0C3D
OA45 0 0400 0462
OA47 0 CC00 0C30
OA49 0 08D2
OA4A 0 7401 0926
OA4C 0 C400 0926
OA4E 0 9400 0C45
OA50 0 4820
OA51 0 70BE
OA52 0 0400 0926
OA54 0 4400 07F0
OA56 0 700A
OA57 0 C400 0C3B
OA59 0 0004
OA5A 0 C400 0C39
OA5C 0 D003
OA5D 0 CC00 0C2E
OA5F 0 08BC
OA60 0 70CC
MT507 BSS E 0
MT507 NDP 0
NUP 0
BSI L TMWRT
LDD L DLYC
STD L DLY2-1
*****
MT505 BSI L DELAY
DC DLY2-1
*****
MT508 LOX L2 DST
*****
WRITE FOURTH RECORD
BSI L TMWRT WRITE
*****
WRITE FIFTH RECORD
BSI L TMWRT WRITE
*****
PREPARE FOR NEXT SERIES
MT509 LDX L3 86
MT50A LDD L3 MT5X0&4
STD L DLYC
MDX L MT509&1,-2
MOX MT502
*****
LD L K0086
STD MT509&1
LO L ADRS1
STD MT50A&1
LDD L MT506-1
STD L DLYC
*****
MOX L SW,-1
MDX MT590
LD L MT5X3
STD L SW
LDD L MT5Q6
STD MT5Q7
MOX L MTTX3,1
LD L MTTX3
S L MT5XF
BSC Z
MDX MT502
STD L MTTX3
BSI L RWD
MOX MT583
*****
MT590 LO L K0010
STD MT509&1
LD L ADRS2
STD MT50A&1
LDD L KBSC
STD MT5Q7
MOX MT509
*****
SET UP TO READ
MT583 LD L MTTY9
BSI L PRDWT
BSI L INTRT
OC TMR04
MT500 BSI L CN2
*****
READ FIRST RECORD
```

8B924350
8B924360
8B924370
8B924380
8B924390
8B924400
8B924410
8B924420
8B924430
8B924440
8B924450
8B924460
8B924470
8B924480
8B924490
8B924500
8B924510
8B924520
8B924530
8B924540
8B924550
8B924560
8B924570
8B924580
8B924590
8B924600
8B924610
8B924620
8B924630
8B924640
8B924650
8B924660
8B924670
8B924680
8B924690
8B924700
8B924710
8B924720
8B924730
8B924740
8B924750
8B924760
8B924770
8B924780
8B924790
8B924800
8B924810
8B924820
8B924830
8B924840
8B924850
8B924860
8B924870
8B924880
8B924890
8B924900
8B924910
8B924920
8B924930
8B924940
8B924950
8B924960
8B924970
8B924980
8B924990
8B925000
8B925010
8B925020

```
OA6A 0 4400 07C0
OA6C 0 4400 07C0
OA6E 0 C400 07BC
OA70 0 D400 0E69
OA72 0 4400 07C0
OA74 0 4400 07C0
OA76 0 C400 07BC
OA78 0 D400 0E6A
OA7A 0 4400 07C0
OA7C 0 C400 07BC
OA7E 0 D400 0E68
OA80 0 C400 0E69
OA82 0 1890
OA83 0 8C00 092E
OA85 0 DC00 092E
OA87 0 C400 0E6A
OA89 0 1890
OA8A 0 8C00 0930
OA8C 0 DC00 0930
OA8E 0 6700 0E6C
OA90 0 C400 0E6A
OA92 0 8300
OA93 0 D300
OA94 0 7301
OA95 0 6BF9
OA96 0 C400 0E6B
OA98 0 1890
OA99 0 8C00 0932
OA9B 0 DC00 0932
OA9D 0 7401 0C3E
OA9F 0 C400 0C3E
OAA1 0 9400 092A
OAA3 0 4820
OAA4 0 70C3
OAA5 0 D400 0C3E
OAA7 0 C400 092D
OAA9 0 D0E5
OAAA 0 7401 0926
OAAE 0 C400 0926
OAAE 0 9400 0C45
OAB0 0 4820
OAB1 0 70B6
OAB2 0 D400 0926
OAB4 0 6306
OAB5 0 CF00 092C
OAB7 0 AC00 0938
OAB9 0 D700 092C
OABB 0 73FE
OABC 0 70F8
OABD 0 C400 02E1
OABF 0 1808
OAC0 0 4804
OAC1 0 7001
OAC2 0 7002
OAC3 0 4C00 0BC5
OAC5 0 6700 FF01
OAC7 0 C700 0E9B
BSI L TMRDT
GD READ
READ SECOND RECORD
BSI L TMRDT
LD L TWRX0
STD L TDTA
BSI L TMRDT
BSI L TMRDT
LO L TWRX0
STD L TOTA&1
READ FIFTH RECORD
BSI L TMRDT
LO L TWRX0
STD L TOTA&2
LD L TOTA
SRT 16
AD L MTTYC
STD L MTTYC
LD L TOTA&1
SRT 16
AD L MTTYC
STD L MTTYC
LDX L3 TOTA&3
LD L TOTA&1
A 3 0
STD 3 0
MOX 3 1
STX 3 MT50F&1
LD L TOTA&2
SRT 16
AD L MTTYE
STD L MTTYE
MDX L MT5X4,1
LD L MT5X4
S L MTTY5
BSC Z
MOX MT500
STD L MT5X4
LD L MTTYB
STD MT50F&1
MOX L MTTX3,1
LD L MTTX3
S L MT5XF
BSC Z
MDX MT500
STD L MTTX3
5 PASSES COMPLETE-- OUTPUT
A PLOT OF VAR DELAYS
LDX 3 6
MT524 LDD L3 MTTYC-2
D L MTTZ0
STD L3 MTTYC-2
MDX 3 -2
MDX MT524
LD L SWD
SRA 8
BSC E
MOX MT580
MDX MT525-2
MT580 BSC L MT521
LDX L3 -47
MT525 LO L3 TOTA&50
```

8B925030
8B925040
8B925050
8B925060
8B925070
8B925080
8B925090
8B925100
8B925110
8B925120
8B925130
8B925140
8B925150
8B925160
8B925170
8B925180
8B925190
8B925200
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8B925220
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8B925310
8B925320
8B925330
8B925340
8B925350
8B925360
8B925370
8B925380
8B925390
8B925400
8B925410
8B925420
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8B925450
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8B925470
8B925480
8B925490
8B925500
8B925510
8B925520
8B925530
8B925540
8B925550
8B925560
8B925570
8B925580
8B925590
8B925600
8B925610
8B925620
8B925630
8B925640
8B925650
8B925660
8B925670
8B925680
8B925690
8B925700

2400 TIMING TEST

0AC9 0 1890	SRT	16	8B925710
0ACA 0 AC00 0C45	O	L MT5XF	8B925720
0ACC 0 0700 0E9B	STO	L3 TOTA&50	8B925730
0ACE 0 7301	MOX	3 1	8B925740
0ACF 0 70F7	MOX	MT525	8B925750
0A00 0 6000 0BC4	STX	L1 MT520&1	8B925760
0A02 0 1010	SLA	16	8B925770
0A03 0 4400 0C19	BSI	L LOADK	8B925780
0A05 0 4400 044C	BSI	L PCCO	8B925790
0A07 0 6700 00B1	LOX	L3 PR3-1	8B925800
0A09 0 4400 0C21	BSI	L LOAOV	8B925810
0A0B 0 6204	LOX	2 -44	8B925820
0A0C 0 6580 06EF	LOX	I1 OST&15	8B925830
0A0E 0 C500 0675	LO	L1 MT5XA	8B925840
0AEO 0 0400 0C43	STO	L MT5XE	8B925850
0AE2 0 C500 0677	LD	L1 MT5XA&2	8B925860
0AE4 0 0400 0C44	STO	L MT5XE&1	8B925870
0AE6 0 C400 0C43	LO	L MT5XE	8B925880
0AEB 0 9400 060B	S	L GPHLM	8B925890
0AEA 0 0400 0C43	STO	L MT5XE	8B925900
0AEC 0 C400 0C44	LO	L MT5XE&1	8B925910
0AEE 0 9400 060B	S	L GPHLM	8B925920
0AF0 0 0400 0C44	STO	L MT5XE&1	8B925930
0AF2 0 7401 0C42	MOX	L LINE,1	8B925940
0AF4 0 C400 06EF	LO	L OST&15	8B925950
0AF6 0 4C18 0B04	BSC	L TRK9,&-	8B925960
0AF8 0 C400 0C42	LO	L LINE	8B925970
0AFA 0 9400 0461	S	L PCCX1	8B925980
0AFC 0 4C18 0B0B	BSC	L MT517,&-	8B925990
0AFE 0 9400 0C3C	NTOSH	S	8B926000
0B00 0 4C18 0B0B	BSC	L MT517,&-	8B926010
0B02 0 1010	SLA	16	8B926020
0B03 0 7009	MOX	MT517&2	8B926030
0B04 0 C400 0C42	TRK9	LD	8B926040
0B06 0 9400 0463	S	L K007	8B926050
0B08 0 4C1B 0B0B	BSC	L MT517,&-	8B926060
0B0A 0 70F3	MOX	NTOSH	8B926070
0B0B 0 C400 0C41	MT517	LO	8B926080
0B00 0 4400 0C19	BSI	L LOAOK	8B926090
0B0F 0 1010	SLA	16	8B926100
0B10 0 0400 0093	STO	L PRA3	8B926110
0B12 0 0400 0094	STO	L PRA1	8B926120
0B14 0 C400 0C41	LD	L KOASH	8B926130
0B16 0 1008	SLA	8	8B926140
0B17 0 0400 00B0	STO	L PRA&26	8B926150
0B19 0 C400 036E	LO	L ONE	8B926160
0B1B 0 0400 0BA0	STO	L CKHLW	8B926170
0B10 0 C400 066C	LD	L SPEC	8B926180
0B1F 0 0400 0095	STO	L PRA4&3	8B926190
0B21 0 63E5	LDX	3 -27	8B926200
0B22 0 6101	LDX	1 -47	8B926210
0B23 0 C500 0E9B	MT519	LO	8B926220
0B25 0 4C18 0B59	BSC	L MT51B,&-	8B926230
0B27 0 9400 0C43	MT51D	S	8B926240
0B29 0 4C28 0B3C	BSC	L MT51A,&Z	8B926250
0B2B 0 1010	SLA	16	8B926260
0B2C 0 0500 0E9B	STO	L1 TOTA&50	8B926270
0B2E 0 C071	MT51E	LO	8B926280
0B2F 0 4804	BSC	E	8B926290
0B30 0 7008	MOX	MT51F	8B926300
0B31 0 C400 0C3F	LO	L MT5X8	8B926310
0B33 0 1B0B	SRA	8	8B926320
0B34 0 EF00 00B1	OR	L3 PRA&27	8B926330
0B36 0 0700 00B1	MTAAA	STO	8B926340
0B38 0 7020	MOX	MT51B	8B926350
0B39 0 C400 0C3F	MT51F	LO	8B926360
0B3B 0 70F8	MOX	MTAAA-2	8B926370
	*		8B926380

2400 TIMING TEST

0B3C 0 C500 0E9B	MT51A	LO	8B926390
0B3E 0 9400 0C43	S	L MT5XE	8B926400
0B40 0 4830	BSC	-Z	8B926410
0B41 0 7017	MOX	MT51B	8B926420
0B42 0 C500 0E9B	LO	L1 TOTA&50	8B926430
0B44 0 9400 0C44	S	L MT5XE&1	8B926440
0B46 0 4B28	BSC	&Z	8B926450
0B47 0 7011	MOX	MT51B	8B926460
0B48 0 C057	LO	CKHLW	8B926470
0B49 0 4804	BSC	E	8B926480
0B4A 0 7008	MOX	MTAAB	8B926490
0B4B 0 C400 0C40	LO	L MT5X9	8B926500
0B40 0 1808	SRA	8	8B926510
0B4E 0 EF00 00B1	OR	L3 PRA&27	8B926520
0B50 0 0700 00B1	MTAAC	STO	8B926530
0B52 0 7003	MOX	MTAAD	8B926540
0B53 0 C400 0C40	MTAAB	LO	8B926550
0B55 0 70F8	MOX	MTAAC-2	8B926560
0B56 0 1010	MTAAO	SLA	8B926570
0B57 0 0500 0E9B	STO	L1 TOTA&50	8B926580
0B59 0 7401 0BA0	MT51B	MOX	8B926590
0B5B 0 C044	LO	CKHLW	8B926600
0B5C 0 4804	BSC	E	8B926610
0B50 0 7301	MOX	3 1	8B926620
0B5E 0 1000	NOP		8B926630
0B5F 0 7101	MOX	1 1	8B926640
0B60 0 70C2	MOX	MT519	8B926650
	*		8B926660
	*		8B926670
	*		8B926680
		A LINE IS SET UP-PRINT	8B926690
0B61 0 7401 0B9E	MOX	L LNSW,1	8B926700
0B63 0 6A25	STX	2 MT51C&1	8B926710
0B64 0 C039	LO	LNSW	8B926720
0B65 0 9039	S	PRSW	8B926730
0B66 0 4B20	BSC	Z	8B926740
0B67 0 7011	MOX	MT526	8B926750
0B6B 0 740A 0B9F	MOX	L PRSW,10	8B926760
0B6A 0 6600 0000	GRL1	LOX	8B926770
0B6C 0 C400 06EF	LO	L DST&15	8B926780
0B6E 0 4820	BSC	Z	8B926790
0B6F 0 7003	MOX	MT518	8B926800
0B70 0 C600 0BA1	MT572	LO	8B926810
0B72 0 7002	MOX	MT50B	8B926820
0B73 0 C600 0BA5	MT518	LO	8B926830
0B75 0 0400 0094	MT50B	STO	8B926840
0B77 0 7401 0B6B	MOX	L GRL1&1,1	8B926850
0B79 0 6600 0000	MT526	LDX	8B926860
0B7B 0 C600 0E06	LO	L2 PR8	8B926870
0B7D 0 0400 0093	STO	L PRA3	8B926880
0B7F 0 7401 0B7A	MOX	L MT526&1,1	8B926890
		*****	8B926900
0B81 0 4400 044C	BSI	L PCCO	8B926910
		*****	8B926920
0B83 0 1010	MT50C	SLA	8B926930
0B84 0 0400 0D93	STO	L PRA3	8B926940
0B86 0 0400 0D94	STO	L PRA1	8B926950
0B88 0 6600 0000	MT51C	LOX	8B926960
0B8A 0 7201	MOX	2 1	8B926970
0B8B 0 7010	MOX	MT584	8B926980
0B8C 0 C400 0926	LO	L MTTX3	8B926990
0B8E 0 F400 0925	EOR	L MTTX1	8B927000
0B90 0 0400 0926	STO	L MTTX3	8B927010
0B92 0 4C18 0BA9	BSC	L MT562,&-	8B927020
0B94 0 C400 0936	LO	L MTTYF	8B927030
0B96 0 0090	STO	MT510	8B927040
0B97 0 C400 0937	LO	L MTTYF&1	8B927050
0B99 0 008E	STO	MT510&1	8B927060
0B9A 0 6600 FFFF	LOX	L2 -1	

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

2400 TIMING TEST

```
0B9C 0 4C00 0AE6 MT584 BSC L MT516 LODP
0B9E 0 0000 LNSW DC 0
0B9F 0 0007 PRSW DC 7
0BA0 0 0000 CKHLW DC 0 HALF WD SW
0BA1 0 3B08 PR6 DC /3B08 PRINTER .8
0BA2 0 3B07 DC /3B07 PRINTER .7
0BA3 0 3B06 DC /3B06 PRINTER .6
0BA4 0 3B05 DC /3B05 PRINTER .5
*
0BA5 0 013B PR7 DC /013B PRINTER 1.
0BA6 0 3B09 DC /3B09 PRINTER .9
0BA7 0 3B08 DC /3B08 PRINTER .8
0BA8 0 3B07 DC /3B07 PRINTER .7
*
* GRAPH COMPLETE-PRINT AVGS
*
*****
0BA9 0 6700 0DB1 MT562 LDX L3 PR3-1 LOAD MSG ADRS
0BAB 0 4400 0C21 BSI L LOADV SET MSG -PRINT
0BAD 0 6700 0DC0 LDX L3 PR4-1 LOAD MSG ADRS
0BAF 0 4400 0C21 BSI L LOADV SET MSG - PRINT
0BB1 0 4C00 02E1 LD L SWO GET SWITCHES
0BB3 0 1005 SLA 5 CK HEADING BYPASS
0BB4 0 4C28 0BBE BSC L BY,&Z BCH IF ON
0BB6 0 6700 0DE9 LDX L3 NOTE-1 LOAD MSG ADRS
0BB8 0 4400 0C21 BSI L LOADV SET MSG - PRINT
0BBA 0 6700 0E30 LDX L3 NOTE1-1 IX # MSG ADRS
0BBC 0 4400 0C21 BSI L LOADV SET MSG - PRINT
0BBE 0 1010 BY SLA 16 CLEAS ACC
0BBF 0 4400 0C19 BSI L LOADK SET BLANK MSG
0BC1 0 4400 044C BSI L PCCO PRINT
0BC3 0 6500 0000 MT520 LDX L1 *- RESTORE IX 1
0BC5 0 6600 06E0 MT521 LDX L2 DST RESTORE IX 2
0BC7 0 1010 SLA 16 CLEAR A REG
0BC8 0 0400 0B6B STO L GRL1&1
0BCA 0 0003 STO LNSW
0BCB 0 00AE STO MT526&1
0BCC 0 0400 0D2D LD L MT730 GET 0007
0BCE 0 0000 STO PRSW
0BCF 0 4400 083C BSI L MLG PRINT AVERAGES
0BD1 0 0F35 DC MSG10&4
0BD2 0 0F19 DC MSG4&8
0BD3 0 A004 A004 DC /A004 ID A4
0BD4 0 0004 DC /0004 LINE 0-FDRM 4
*
* REWIND THE DRIVE-RESTORE
*
0BD5 0 0400 0939 LD L MTTZ1
0BD7 0 0400 0B27 STO L MT510 RESTORE PROG
0BD9 0 0400 093A LD L MTTZ1&1
0BDB 0 0400 0B28 STO L MT510&1 RESTORE PROG
0BDD 0 4400 07F0 MT581 BSI L RWD GO REWIND
0BDF 0 0400 02E1 LD L SWO GET SW FNC 0 SRC
0BE1 0 1008 SLA 8
0BE2 0 4810 BSC - IS LOOP RD ON
0BE3 0 702F MDX MT582 NO
0BE4 0 1010 SLA 16 RESTORE NECESSARY
0BE5 0 0400 0926 STO L MTTX3 *VALUES IN RTN
0BE7 0 005A STO LINE
0BE8 0 00B5 STO LNSW
0BE9 0 18D0 RTE 16
0BEA 0 1010 SLA 16
0BEB 0 0C00 092E STD L MTTYC
0BED 0 0C00 0930 STD L MTTYD
0BEF 0 0C00 0932 STD L MTTYE
0BF1 0 0400 0B6B STO L GRL1&1
0BF3 0 0400 0B7A STO L MT526&1
```

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IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

2400 TIMING TEST

```
0BF5 0 6700 FFD1 LDX L3 -47
0BF7 0 0700 0E9B MT585 STO L3 TOTA&50
0BF9 0 7301 MDX 3 1
0BFA 0 70FC MDX MT585
0BFB 0 0042 STO MT5X4
0BFC 0 0400 0D2D LD L MT730
0BFE 0 0400 0B9F STO L PRSW
0C00 0 0400 0939 LD L MTTZ1
0C02 0 0400 0B27 STO L MT510
0C04 0 0400 093A LD L MTTZ1&1
0C06 0 0400 0B28 STO L MT510&1
0C08 0 0827 LDD MT5Q6
0C09 0 0C00 0A1C STD L MT5Q7
0C0B 0 0C01 LD MT5X3
0C0C 0 0400 0462 STO L SW
0C0E 0 0C09 LD ADRS1
0C0F 0 0400 0A30 STO L MT50A&1
0C11 0 0400 0A61 BSC L MT583 LOOP READ
0C13 0 0400 0771 MT582 BSC L MONR1 EXIT
*
* INTRPT RETURN ON WRITE
*
0C15 0 0480 078D MT522 BSC I TMWRT GO TO CALLING RTN
*
* INTRPT RETURN ON TM READ
*
0C17 0 0480 07C0 MT523 BSC I TMRDT
*
* LOAD I/O AREA WITH CONSTANT
*
0C19 0 0000 LOADK DC *- RETURN ADRS
0C1A 0 631C LDX 3 28
0C1B 0 0700 0D92 LOAD1 STO L3 PRA3-1 STORE IN MSG AREA
0C1D 0 73FF MDX 3 -1
0C1E 0 70FC MDX LOAD1
0C1F 0 0480 0C19 BSC I LOADK RETURN TO MAINLINE
*
* LOAD I/O AREA WITH MESSAGE
*
0C21 0 0000 LOADV DC *- RETURN ADDR
0C22 0 6B02 STX 3 MT510&1
0C23 0 631C LDX 3 28
0C24 0 0700 0000 MT510 LD L3 *- LOAD MESSAGE
0C26 0 0700 0D92 STO L3 PRA3-1 STORE IN MSG AREA
0C28 0 73FF MDX 3 -1
0C29 0 70FA MDX MT510
0C2A 0 0400 044C BSI L PCCO PRINT MESSAGE
0C2C 0 0480 0C21 BSC I LOADV RETURN TO MAINLINE
*
* ROUTINE 5 CONSTANTS
*
0C2E 0 0000 BSS E 0
0C2E 0 4400 0C46 KBSC BSI L LDLFT BRANCH TO LFT COL LD
0C30 0 1000 MT5Q6 NOP 0 PROG RESTORE
0C31 0 1000 NOP 0
0C32 0 0000 DC 0
0C33 0 0000 DLY1 DC 0 DELAY COUNT 1
0C34 0 0000 DC 0 DELAY COUNT 2
0C35 0 0000 DLY2 DC 0
0C36 0 0000 DLYC DC 0 DELAY STORAGE
0C37 0 0000 DC 0
0C38 0 067E ADRS1 DC MT5X0&4 CONSTANTS
0C39 0 0676 ADRS2 DC MT5X0-4
0C3A 0 0056 K0086 DC 86
0C3B 0 000A K0010 DC 10
0C3C 0 001E MT5X2 DC 30
0C3D 0 0002 MT5X3 DC 2
```

PART NO. 2196491
PAGE 21AATE 01JUL66 01NOV66 15MAY67 01SEP67 01DCT67 14NOV69 30JAN70
C NO. 415178 415233 411731 411857 411875 431319 431319APRDG ID 08B9-2
PAGE 21ATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
C NO. 415178 415233 411731 411857 411875 431319 431319APRDG ID 08B9-2
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2400 TIMING TEST

ADDRESS	DATA	INSTR	OP	COND	COMMENT	PC
0C3E	0 0000	MT5X4	DC	0	SERIES COUNT	88928430
0C3F	0 3500	MT5X8	OC	/3500	PRINTER E	88928440
0C40	0 2600	MT5X9	OC	/2600	PRINTER O	88928450
0C41	0 2020	KOASH	DC	/2020	PRINTER OASH	88928460
0C42	0 0000	LINE	DC	0	LINE COUNT	88928470
0C43	0 0000	MT5XE	DC	0	UPPER LIMIT STORAGE	88928480
0C44	0 0000		OC	0	LOWER LIMIT STORAGE	88928490
0C45	0 0005	MT5XF	OC	5	NUMBER PASSES	88928500
		*				88928510
		+				88928520
		*			ROUTINE TO LOAO THE LEFT	88928530
		*			* COLUMN	88928540
		*				88928550
0C46	0 0000	LDLFT	DC	0		88928560
0C47	0 6822		STX	3	LOLFE&1	88928570
0C48	0 6323		LOX	3	35	88928580
0C49	0 4400 0780	LOLF A	BSI	L	TMWRT	88928590
0C4B	0 73FF		MDX	3	-1	88928600
0C4C	0 70FC		MOX		LOLF A	88928610
0C40	0 6323		LOX	3	35	88928620
0C4E	0 6600 06E0	LOLF B	LDX	L2	DST	88928630
0C50	0 4400 07C9		BSI	L	BSP	88928640
0C52	0 620A		LDX	2	10	88928650
0C53	0 C400 06B1		LO	L	MT591	88928660
0C55	0 D019		STO		LOLFX	88928670
0C56	0 6B06		STX	3	LOLFY&1	88928680
0C57	0 4400 038C	LOLF C	BSI	L	DELAY	88928690
0C59	0 0C6E		OC		LOLFX-1	88928700
0C5A	0 72FF		MOX	2	-1	88928710
0C5B	0 70FB		MDX		LDLFC	88928720
0C5C	0 6700 0000	LOLF Y	LDX	L3	0	88928730
0C5E	0 73FF		MDX	3	-1	88928740
0C5F	0 70EE		MDX		LDLFB	88928750
0C60	0 6600 06E0		LOX	L2	DST	88928760
0C62	0 C400 0928		LD	L	MTTX9	88928770
0C64	0 4400 0830		BSI	L	PRDWT	88928780
0C66	0 4400 081F		BSI	L	INTRT	88928790
0C68	0 07AA		OC		TMWR4	88928800
0C69	0 6700 0000	LDLFE	LDX	L3	0	88928810
0C6B	0 4C80 0C46		BSC	I	LDLFT	88928820
0C6E	0000		8SS	E	0	88928830
0C6E	0 0000		DC		0	88928840
0C6F	0 0000	LDLFX	DC		0	88928850
		*			* STDRAGE	88928860
						88928870
						88928880
						88928890
						88928900
0C70	0 7FFF	MT6X8	DC	/7FFF	CDNSTANT	88928910
0C71	0 1010	MTT07	SLA	16	ZERD ACCUM	88928920
0C72	0 D400 0926		STD	L	MTTX3	88928930
0C74	0 0400 0D87		STD	L	MT7X3	88928940
0C76	0 D400 0DB8		STD	L	MT7X4	88928950
0C78	0 D400 0DB9		STD	L	MT7X5	88928960
0C7A	0 D400 0DBA		STD	L	MT7X6	88928970
0C7C	0 D400 0D2E		STD	L	LDW	88928980
0C7E	0 0400 0D2F		STD	L	HI	88928990
0C80	0 D400 0D30		STD	L		

PREPARE TO WRITE										88929110
OC92 0 C400 0928	MT719	LD	L	MTTX9	GET WRITE FUNCTION					88929120
OC94 0 4400 0830		BSI	L	PRDWT	GD SET UP		SRC			88929130
OC96 0 4400 081F		BSI	L	INTRT	GD SET INTR RETURN		SRC			88929140
OC98 0 0C15		OC		MT522						88929150
	*									88929160
	*				WRITE FIRST RECORD					88929170
	*									88929180
OC99 0 4400 078D		BSI	L	TMWRT	WRITE					88929190
	*									88929200
	*				WRITE SECONO RECORO					88929210
	*									88929220
OC9B 0 4400 0780		BSI	L	TMWRT	WRITE					88929230
	*									88929240
	*				WRITE THIRD RECORD					88929250
	*									88929260
OC9D 0 4400 078D		BSI	L	TMWRT	WRITE					88929270
	*									88929280
	*				WRITE LONG RECDRO					88929290
	*									88929300
OC9F 0 C400 008C		LD	L	MT7XA						88929310
OCA1 0 0206		STD	2	6	SET WD CT					88929320
OCA2 0 D400 0E9C		STD	L	IDA	SET IN I/O AREA					88929330
OCA4 0 4400 078D		BSI	L	TMWRT	WRITE					88929340
OCA6 0 4400 0910		BSI	L	CN2	GO SET CDNSTANTS		SRC			88929350
	*									88929360
	*				POSITION TAPE					88929370
	*									88929380
OCA8 0 6304	MT702	LDX	3	4						88929390
OCA9 0 6803	MT703	STX	3	MT704&1	SAVE IX 3					88929400
OCAA 0 4400 07C9		BSI	L	BSP	GD BACKSPACE		SRC			88929410
OCAC 0 6700 0000	MT704	LOX	L3	0	RESTDRE IX 3					88929420
OCAE 0 73FF		MDX	3	-1	IS TAPE PDSITIDNED					88929430
OCAF 0 70F9		MDX		MT703	ND-LDOP					88929440
	*									88929450
	*				SET UP TD REAO					88929460
	*									88929470
OCB0 0 C400 092C		LD	L	MTTY9	GET READ FUNCTIDN					88929480
OCB2 0 4400 0830		BSI	L	PRDWT	GD SET UP		SRC			88929490
OCB4 0 4400 081F		BSI	L	INTRT	GD SET TIME INT RET		SRC			88929500
OCB6 0 07C4		DC		TMRD4						88929510
	*									88929520
	*				REAO FIRST RECDRD					88929530
	*									88929540
OCB7 0 4400 07C0		BSI	L	TMRDT	GD REAO		SRC			88929550
	*									88929560
	*				READ SECONO RECDRD					88929570
	*									88929580
OCB9 0 4400 07C0		BSI	L	TMRDT						

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2400 TIMING TEST

```
OCCE 0 7001          MDX      MT705      YES
OCCF 0 700D          MDX      MT70F      NO
OC00 0 4400 07C9    MT705 BSI L  BSP      GO BACK SPACE      SRC
*
*                PREPARE TO WRITE
*
OC02 0 C400 0928    MT70B LD  L  MTTX9      GET WRITE FUNCTION
OC04 0 4400 0830          BSI L  PROWT      GO SET UP          SRC
OC06 0 4400 07C0          BSI L  TMR0T      GO WRT              SRC
OC08 0 1010          SLA      16           ZERO ACCUM
OC09 0 0400 0D86          STO L  MT7X2      CLEAR RD/RERO SW
OC0B 0 6303          LDX      3 3          SET TO BACKSPACE 3
OC0C 0 70CC          MDX      MT703
*
*                READ/REREAD SW # REREAD
*
OC00 0 C400 0D8A    MT7DF LO  L  MT7X6      GET REF TIME
OC0F 0 9400 0087          S      L  MT7X3      SUB REREAD TIME
OCE1 0 0400 0D8F          STO L  MT7Y0      SAVE
OCE3 0 8400 0D89          A      L  MT7X5      AOD TO TOTAL
OCE5 0 0400 0089          STO L  MT7X5      SAVE
OCE7 0 C400 0087          LD  L  MT7X3      GET NEW COUNT
OCE9 0 9400 009D          S      L  MT7Y1      SUB LOW VALUE
OCEB 0 4810          BSC      -           IS NEW COUNT LOW
OCEC 0 7004          MOX      MT750      NO
OCED 0 C400 0087          LO  L  MT7X3      SET NEW CT AS LOW
OCEF 0 0400 0090          STO L  MT7Y1
OCF1 0 C400 0087    MT750 LO  L  MT7X3      GET NEW CT
OCF3 0 9400 0091          S      L  MT7Y2      SUB HIGH
OCF5 0 4808          BSC      &           IS NEW COUNT HIGH
OCF6 0 7004          MOX      MT751      NO
OCF7 0 C400 0087          LD  L  MT7X3      SET NEW CT AS HIGH
OCF9 0 0400 0091          STO L  MT7Y2      *
OCFB 0 74FF 0085    MT751 MDX L  MT7X1,-1    DECR LOOP CONTROL
OCFO 0 7079          MOX      MT718      LOOP CNTRL NOT ZERO
*
*                OUTPUT RESULTS
*
OCFE 0 1010    MT744 SLA      16           CLEAR SWITCH
OCCF 0 0400 0088          STO L  MT7X4      *
OD01 0 C400 0D8A          LD  L  MT7X6      GET REF TIME
OD03 0 9400 0D90          S      L  MT7Y1      SUB LOW COUNT
OD05 0 0400 0D90          STO L  MT7Y1      SET AS LOW
OD07 0 4810          BSC      -           SKIP IF NEG
OD08 0 7029          MDX      ERR          BRANCH TO ERROR
OD09 0 1010          SLA      16           CLEAR A REG
OD0A 0 9400 0D90          S      L  MT7Y1      MAKE POSITIVE
OD0C 0 0400 0090          STO L  MT7Y1      SAVE
OD0E 0 C400 0091    MT754 LD  L  MT7Y2      GET HIGH COUNT
OD10 0 9400 008A          S      L  MT7X6      SUB REG TIME
OD12 0 0400 0091          STO L  MT7Y2      SET AS HIGH
OD14 0 4C10 0D1A          BSC L  MT755,-    BRANCH IF POS
OD16 0 1010          SLA      16           CLEAR A REG
OD17 0 9079          S      MT7Y2      MAKE POSITIVE
OD18 0 0078          STO      MT7Y2      SAVE
OD19 0 701E          MOX      ERR1          BRANCH
OD1A 0 C06E          LD      MT7X5      GET TDIAL CREEP
OD1B 0 1890          SRT      16           CALCULATE AVG CREEP
OD1C 0 A871          D      MT7XE
OD10 0 4820          BSC      Z
OD1E 0 7001          MDX      MT715
OD1F 0 1800          RTE      16
OD20 0 D068          STO      MT7X5
OD21 0 4C28 0045          BSC L  MT714,&Z    IS CREEP NEG
OD23 0 4820          BSC      Z           IS CREEP ZERO
OD24 0 7016          MOX      ERR2          NO
OD25 0 4400 083C          BSI L  MLG          PRINT ZERO CREEP
OD27 0 0F39          OC      MSG11&4
```

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```
OD28 0 0F01          DC      MSG1&8      8B930470
OD29 0 E007          E007  OC      /E007      ID E7      8B930480
OD2A 0 0005          DC      /0005      LINE 0- FORM 5 8B930490
OD2B 0 4C00 0771    MT712 BSC L  MONR1      EXIT      8B930500
OD2D 0 0007          MT730 DC      7          8B930510
OD2E 0 0000          LOW DC      0          NEG CREEP SWS 8B930520
OD2F 0 0000          HI  OC      0          *          8B930530
OD30 0 0000          AVG OC      0          *          8B930540
OD31 0 8000          K8000 DC      /8000      CONSTANT 8B930550
*
*                SET ERROR SWITCH
*
OD32 0 C0FE          ERR  LO      K8000      SET LOW SW 8B930570
OD33 0 D0FA          STD      LDW          *          8B930580
OD34 0 C400 036E          LD  L  ONE          GET ONE      8B930600
OD36 0 D051          STD      MT7X4      SET NEG CREEP SW 8B930610
OD37 0 7006          MDX      MT754      BRANCH      8B930620
OD38 0 C0F8          ERR1 LD      K8000      SET HI SW    8B930630
OD39 0 00F5          STO      HI          *          8B930640
OD3A 0 70DF          MDX      MT755          8B930650
OD3B 0 C0F5          ERR2 LO      K8000      SET AVG SW 8B930660
OD3C 0 D0F3          STO      AVG          *          8B930670
OD30 0 7032          MOX      MT716          8B930680
*
*                FIRST PASS SW IS ON
*
OD3E 0 C400 078C    MT711 LD  L  TWRX0      GET THE TIME 8B930700
OD40 0 D049          STO      MT7X6      SAVE          8B930710
OD41 0 1010          SLA      16           ZERO ACCUM 8B930720
OD42 0 D048          STO      MT7X7      CLEAR 1ST PASS SW 8B930730
OD43 0 4C00 0CC3          BSC L  MT713          8B930740
*
*                CREEP IS FORWARD PRINT FFF
*
OD45 0 1010    MT714 SLA      16           8B930750
OD46 0 9042          S      MT7X5          8B930760
OD47 0 D041          STO      MT7X5          8B930770
OD48 0 7400 0088          MDX L  MT7X4,0    CK NEG CREEP SW 8B930780
OD4A 0 7025          MDX      MT716      BRANCH IF SET 8B930790
OD4B 0 C500 02EA          LD  L1 EDIT&6      GET OR MODEL 8B930800
OD4D 0 D005          STO      MT760&1      SET IN IX    8B930810
OD4E 0 C400 02EC          LD  L  E0IT&8      GET MEM SPEED 8B930820
OD50 0 4C30 0D60          BSC L  MT761,Z-    BRANCH IF 4 MIC $ 8B930830
OD52 0 6700 0000    MT760 LDX      L3 0      IX # MODEL 8B930840
OD54 0 C700 0063          LO  L3 MT762      GET CONSTANT 8B930850
OD56 0 9039          S      MT7Y1      SUB LOW CREEP 8B930860
OD57 0 4C30 0D69          BSC L  MT763,-Z    BRANCH IF TOO LOW 8B930870
OD59 0 4400 083C          BSI L  MLG          PRINT CREEP 8B930880
OD5B 0 0F39          DC      MSG11&4      8B930890
OD5C 0 0F01          OC      MSG1&8      8B930900
OD5D 0 A007          A007  OC      /A007      8B930910
OD5E 0 0005          DC      /0005      LINE 0- FORM 5 8B930920
OD5F 0 70CB          MOX      MT712      BRANCH      8B930930
OD60 0 7403 0D53    MT761 MDX L  MT760&1,3  INCR IX TO 4 MIC 8B930940
OD62 0 70EF          MOX      MT760          8B930950
OD63 0 000E          MT762 DC      14          CONSTANTS 8B930960
OD64 0 0D2A          DC      42          *          8B930970
OD65 0 0015          OC      21          *          8B930980
OD66 0 0007          DC      7          *          8B930990
OD67 0 0015          OC      21          *          8B931000
OD68 0 000A          DC      10          *          8B931010
OD69 0 4400 083C    MT763 BSI L  MLG          CREEP TOO LOW 8B931020
OD6B 0 0F39          DC      MSG11&4      8B931030
OD6C 0 0F01          OC      MSG1&8      8B931040
OD6D 0 E001          E001  OC      /E001      8B931050
OD6E 0 0005          OC      /0005      LINE 0- FORM 5 8B931060
OD6F 0 70BB          MDX      MT712      BRANCH      8B931070
*
*                8B931080
*                8B931090
*                8B931100
*                8B931110
*                8B931120
*                8B931130
*                8B931140
```

DATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
LC NO. 415178 415233 411731 411857 411875 431319 431319APROG ID 08B9-2
PAGE 23DATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
LC NO. 415178 415233 411731 411857 411875 431319 431319APROG ID 08B9-2
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2400 TIMING TEST

```
*          CREEP IS BCKWARD PRINT BBB
*
OD70 0 4400 083C MT716 BSI L MLG          BACKWARD CREEP
OD72 0 DF39      DC MSG11&4
OD73 0 0F01      DC MSG1&8
OD74 0 ED08      DC /E008      ID E8
OD75 0 0005      DC /0005      LINE 0- FDRM 5
OD76 0 70B4      MDX MT712
OD77 0 C400 0925 MT718 LD L MTTX1      GET 0001
OD79 0 D00C      STD MT7X2      SET RD/RERD # RD
OD7A 0 6303      LDX 3 3
OD7B 0 6B03      MT746 STX 3 MT745&1
OD7C 0 4400 07C9 BSI L BSP          GD BACKSPACE      SRC
OD7E 0 6700 0000 MT745 LDX L3 0
OD80 0 73FF      MDX 3 -1
OD81 0 70F9      MDX MT746
OD82 0 4C00 0C92 BSC L MT719

*          RDUTINE CDNSTANTS
*
OD84 0 000A      MT7X0 DC 10          CDNSTANT
OD85 0 0000      MT7X1 DC 0          LDDP CDNTRDL
OD86 0 0000      MT7X2 DC 0          RD/RERD SW
OD87 0 0000      MT7X3 DC 0          2ND READ TIME
OD88 0 0000      MT7X4 DC 0          1ST READ TIME
OD89 0 0000      MT7X5 DC 0          TDIAL CREEP
OD8A 0 0000      MT7X6 DC 0          REFERENCE TIME
OD8B 0 0001      MT7X7 DC 1          FIRST PASS SW
OD8C 0 40C8      MT7XA DC /40C8      WD CT # 200
OD8D 0 0003      MT7XC DC 3
OD8E 0 00DA      MT7XE DC 10
OD8F 0 0000      MT7Y0 DC 0          TEMP STDRAGE
OD90 0 7FFF      MT7Y1 DC /7FFF      LDW VALUE STDRAGE
OD91 0 0000      MT7Y2 DC 0          HIGH VALUE STDRAGE

*
*          PRINT AREA
*
OD92 0 0000      BSS E 0
OD92 0 0000      PRA4 DC /0000
OD93 0 0000      PRA3 DC /0000
OD94 0 0000      PRA1 DC /0000
OD95 0 2C00      DC /2C00      *
OD96 0 001B      PRA BSS 27      PRINT AREA
ODB1 0 FFFF      DC /FFFF

*
*          SPECIAL MESSAGES
*
ODB2 0 0000      PR3 DC /0000
ODB3 0 0000      DC /0000
ODB4 0 002C      DC /002C      *
ODB5 0 2020      DC /2020      --
ODB6 0 2020      DC /2020      --
ODB7 0 2020      DC /2020      --
ODB8 0 0120      DC /0120      1-
ODB9 0 2020      DC /2020      --
ODBA 0 2002      DC /2002      -2
ODBB 0 2020      DC /2020      --
ODBC 0 2003      DC /2003      -3
ODBD 0 2004      DC /2004      -4
ODBE 0 2005      DC /2005      -5
ODBF 0 2006      DC /2006      -6
ODC0 0 2008      DC /2008      -8
ODC1 0 2001      DC /2001      -1
ODC2 0 0A20      DC /0A20      0-
ODC3 0 2002      DC /2002      -2
```

2400 TIMING TEST

```
ODC4 0 0A20      DC /0A20      0-
ODC5 0 030A      DC /030A      30
ODC6 0 2006      DC /2006      -6
ODC7 0 0A20      DC /0A20      0-
ODC8 0 3B02      DC /3B02      .2
ODC9 0 2020      DC /2020      --
ODCA 0 0120      DC /0120      1-
ODCB 0 0320      DC /0320      3-
ODCC 0 052C      DC /052C      5*
ODCD 0 FFFF      DC /FFFF
ODCE 0 0000      PR4 DC /0000
ODCF 0 0000      DC /0000
ODD0 0 002C      DC /002C      *
ODD1 0 1629      DC /1629      WR
ODD2 0 3913      DC /3913      IT
ODD3 0 3500      DC /3500      E
ODD4 0 3726      DC /3726      GD
ODD5 0 0034      DC /0034      D
ODD6 0 2616      DC /2616      DW
ODD7 0 2500      DC /2500      N
ODD8 0 1339      DC /1339      TI
ODD9 0 2435      DC /2435      ME
ODDA 0 0000      DC /0000
ODDB 0 0000      DC /0000
ODDC 0 0000      DC /0000
ODDD 0 0024      DC /0024      M
ODDE 0 3923      DC /3923      IL
ODDF 0 2339      DC /2339      LI
ODE0 0 1235      DC /1235      SE
ODE1 0 3326      DC /3326      CD
ODE2 0 2534      DC /2534      ND
ODE3 0 1200      DC /1200      S
ODE4 0 2C12      DC /2C12      *S
ODE5 0 3533      DC /3533      EC
ODE6 0 2625      DC /2625      DN
ODE7 0 3412      DC /3412      DS
ODE8 0 002C      DC /002C      *
ODE9 0 FFFF      DC /FFFF
ODEA 0 2526      NDTE DC /2526      ND
ODEB 0 1335      DC /1335      TE
ODEC 0 2023      DC /2023      -L
ODED 0 3613      DC /3613      FT
ODEE 0 0033      DC /0033      C
ODEF 0 2623      DC /2623      DL
ODF0 0 0039      DC /0039      I
ODF1 0 1200      DC /1200      S
ODF2 0 2326      DC /2326      LD
ODF3 0 3134      DC /3134      AD
ODF4 0 3534      DC /3534      ED
ODF5 0 0034      DC /0034      D
ODF6 0 1429      DC /1429      UR
ODF7 0 3925      DC /3925      IN
ODF8 0 3700      DC /3700      G
ODF9 0 1338      DC /1338      TH
ODFA 0 3500      DC /3500      E
ODFB 0 2331      DC /2331      LA
ODFC 0 1213      DC /1213      ST
ODFD 0 0004      DC /0004      4
ODFE 0 0037      DC /0037      G
ODFF 0 2600      DC /2600      D
OE00 0 3426      DC /3426      DD
OE01 0 1625      DC /1625      WN
OE02 0 0013      DC /0013      T
OE03 0 3924      DC /3924      IM
OE04 0 3512      DC /3512      ES
OE05 0 FFFF      PR8 DC /FFFF
OE06 0 0000      DC /0000
OE07 0 0000      DC /0000
```



0E08 0 0000	DC	/0000	
0E09 0 0000	OC	/0000	
0E0A 0 3900	DC	/3900	I
0E0B 0 2500	DC	/2500	N
0E0C 0 1300	DC	/1300	T
0E0D 0 3500	DC	/3500	E
0E0E 0 2900	DC	/2900	R
0E0F 0 0000	DC	/0000	
0E10 0 2900	DC	/2900	R
0E11 0 3500	DC	/3500	E
0E12 0 3300	DC	/3300	C
0E13 0 2600	DC	/2600	O
0E14 0 2900	DC	/2900	R
0E15 0 3400	DC	/3400	D
0E16 0 0000	DC	/0000	
0E17 0 3700	DC	/3700	G
0E18 0 3100	DC	/3100	A
0E19 0 2700	DC	/2700	P
0E1A 0 0000	DC	/0000	
0E1B 0 3900	DC	/3900	I
0E1C 0 2500	DC	/2500	N
0E1D 0 0000	DC	/0000	
0E1E 0 3800	DC	/3800	H
0E1F 0 2500	DC	/2500	N
0E20 0 3400	DC	/3400	D
0E21 0 2900	DC	/2900	R
0E22 0 1300	DC	/1300	T
0E23 0 3800	DC	/3800	H
0E24 0 1200	OC	/1200	S
0E25 0 0000	DC	/0000	
0E26 0 2600	DC	/2600	O
0E27 0 3600	DC	/3600	F
0E28 0 0000	DC	/0000	
0E29 0 3900	DC	/3900	I
0E2A 0 2500	DC	/2500	N
0E2B 0 3300	DC	/3300	C
0E2C 0 3800	DC	/3800	H
0E2D 0 3500	DC	/3500	E
0E2E 0 1200	DC	/1200	S
0E2F 0 0000	DC	/0000	
0E30 0 0000	DC	/0000	
0E31 0 0000	DC	/0000	
0E32 0 0000	DC	/0000	
0E33 0 0020	DC	/0020	-
0E34 0 2020	DC	/2020	--
0E35 0 3431	DC	/3431	DA
0E36 0 1238	DC	/1238	SH
0E37 0 3512	DC	/3512	ES
0E38 0 0039	OC	/0039	I
0E39 0 2534	DC	/2534	ND
0E3A 0 3933	DC	/3933	IC
0E3B 0 3113	DC	/3113	AT
0E3C 0 3500	DC	/3500	E
0E3D 0 3929	DC	/3929	IR
0E3E 0 3700	DC	/3700	G
0E3F 0 2339	DC	/2339	LI
0E40 0 2439	DC	/2439	MI
0E41 0 1312	DC	/1312	TS
0E42 0 0000	DC	/0000	
0E43 0 0000	DC	/0000	
0E44 0 0000	DC	/0000	
0E45 0 0000	DC	/0000	
0E46 0 0000	DC	/0000	
0E47 0 0000	DC	/0000	
0E48 0 0000	DC	/0000	
0E49 0 0000	DC	/0000	
0E4A 0 0000	DC	/0000	
0E4B 0 0000	DC	/0000	

NOTE1

8B932510
8B932520
8B932530
8B932540
8B932550
8B932560
8B932570
8B932580
8B932590
8B932600
8B932610
8B932620
8B932630
8B932640
8B932650
8B932660
8B932670
8B932680
8B932690
8B932700
8B932710
8B932720
8B932730
8B932740
8B932750
8B932760
8B932770
8B932780
8B932790
8B932800
8B932810
8B932820
8B932830
8B932840
8B932850
8B932860
8B932870
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8B932900
8B932910
8B932920
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8B932960
8B932970
8B932980
8B932990
8B933000
8B933010
8B933020
8B933030
8B933040
8B933050
8B933060
8B933070
8B933080
8B933090
8B933100
8B933110
8B933120
8B933130
8B933140
8B933150
8B933160
8B933170
8B933180

0E4C 0 FFFF
0E4D 0 2C2C
0E4E 0 2426
0E4F 0 3420
0E50 0 0000
0E51 0 0000
0E52 0 2013
0E53 0 2922
0E54 0 0000
0E55 0 1218
0E56 0 1200
0E57 0 3429
0E58 0 0000
0E59 0 0000
0E5A 0 0000
0E5B 0 0000
0E5C 0 0024
0E5D 0 3933
0E5E 0 0012
0E5F 0 3533
0E60 0 0024
0E61 0 3524
0E62 0 0000
0E63 0 3431
0E64 0 1335
0E65 0 2000
0E66 0 0000
0E67 0 0000
0E68 0 FFFF
0E69 0033
0E9C 0020

LN3	DC	/2C2C	**
	DC	/2426	MD
	DC	/3420	D-
LN3A	DC	/0000	MODEL
LN3B	DC	/0000	TRACK
	DC	/2013	-T
	DC	/2922	RK
	DC	/0000	
	DC	/1218	SY
	DC	/1200	S
	DC	/3429	DR
LN3D	DC	/0000	DRIVE NUMBER
	DC	/0000	
LN3C	DC	/0000	MEM SPEED
	DC	/0000	*
	DC	/0024	M
	DC	/3933	IC
	DC	/0012	S
	DC	/3533	EC
	DC	/0024	M
	DC	/3524	EM
	DC	/0000	
	DC	/3431	DA
	DC	/1335	TE
	DC	/2000	-
	DC	/0000	
	DC	/0000	
	DC	/FFFF	

TOTA BSS 51 VAR TOTAL STDRAGE
IDA BSS 45 INPUT/OUTPUT AREA
*XX
*XXXXXXXXXXXXXXXXXXXX SET UP AND PRINT HDINGS XXX
*XX
LDSP DC 0
LD L 1 MLG GET TYPE ADRS
STD L0 L0SP1&1 SET
MDX L MLG,1 INCR RETURN
LD I MLG GET HDING ADRS
STD L0 L0SP3&1 SET
MDX L MLG,1 INCR RETURN
STX 2 L0SP2&1 SAVE IX 2
LD L SWO GET SWS
SRA 10
BSC L L0SP2,E BRANCH # BYPASS
LDX 3 28 CLEAR MSG AREA
SLA 16
LDSP5 STO L3 PRA4
MDX 3 -1
MDX L0SP5
LDX 3 -8 SET MESSAGE
LDX 2 -4
LDSP1 LD L2 *-
STD L2 PRA&4
LDSP3 LD L3 *-
STO L3 PRA3&27
LD L3 MSG9&8
STD L3 PRA&14
MDX 2 1
MDX L0SP4
LDX 2 -4
LDSP4 MDX 3 1
MDX L0SP1
BSI L PCCD PRINT MESSAGE
LDSP2 LDX L2 *- RESTORE IX 2
BSC I L0SP EXIT

*
*
HEADING MESSAGES

8B933190
8B933200
8B933210
8B933220
8B933230
8B933240
8B933250
8B933260
8B933270
8B933280
8B933290
8B933300
8B933310
8B933320
8B933330
8B933331
8B933332
8B933333
8B933334
8B933335
8B933336
8B933337
8B933338
8B933339
8B933340
8B933341
8B933342
8B933343
8B933344
8B933347
8B933348
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8B933351
8B933352
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8B933354
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8B933356
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8B933358
8B933359
8B933360
8B933361
8B933362
8B933363
8B933364
8B933365
8B933366
8B933367
8B933368
8B933369
8B933370
8B933371
8B933372
8B933373
8B933374
8B933375
8B933376
8B933377
8B933378
8B933379
8B933380
8B933381
8B933382
8B933383
8B933384
8B933385
8B933386

2400 TIMING TEST

```
*
MSG1 DC /2439 MI
      DC /2500 N
      DC /0000
      OC /3115 AV
      DC /3700 G
      DC /0000
      DC /2431 MA
      DC /1700 X

*
MSG2 DC /3412 DS
      DC /1600 W
      DC /0000
      DC /0000
      DC /0000
      DC /0000
      DC /0000
      DC /0000
      DC /0000
      DC /0000

*
MSG3 OC /2439 MI
      DC /2500 N
      DC /0000
      OC /3133 AC
      OC /1323 TL
      DC /0000
      DC /2431 MA
      DC /1700 X

*
MSG4 DC /010A 10
      OC /2412 MS
      DC /0000
      DC /1531 VA
      DC /2900 R
      DC /0000
      DC /2439 MI
      DC /2500 N

*
MSG5 DC /2526 ND
      DC /1300 T
      DC /2934 RD
      OC /1800 Y

*
MSG6 DC /3326 CO
      DC /2427 MP
      OC /2335 LE
      DC /1335 TE

*
MSG7 OC /1629 WR
      DC /1300 T
      DC /3423 DL
      DC /1800 Y

*
MSG8 DC /2935 RE
      DC /3134 AD
      OC /0034 O
      OC /2318 LY

*
MSG9 DC /2939 RI
      OC /3400 D
      DC /0000
      DC /2931 RA
      DC /3400 D
      DC /0000
      DC /1425 UN
      OC /3913 IT

*
MSG10 DC /3115 AV
```

8B933870
8B933880
8B933890
8B933900
8B933910
8B933920
8B933930
8B933940
8B933950
8B933960
8B933970
8B933980
8B933990
8B934000
8B934010
8B934020
8B934030
8B934040
8B934050
8B934060
8B934070
8B934080
8B934090
8B934100
8B934110
8B934120
8B934130
8B934140
8B934150
8B934160
8B934170
8B934180
8B934190
8B934200
8B934210
8B934220
8B934230
8B934240
8B934250
8B934260
8B934270
8B934280
8B934290
8B934300
8B934310
8B934320
8B934330
8B934340
8B934350
8B934360
8B934370
8B934380
8B934390
8B934400
8B934410
8B934420
8B934430
8B934440
8B934450
8B934460
8B934470
8B934480
8B934490
8B934500
8B934510
8B934520
8B934530
8B934540

2400 TIMING TEST

```
OF32 0 3700 DC /3700 G
OF33 0 3929 DC /3929 IR
OF34 0 3700 DC /3700 G

*
MSG11 DC /3329 CR
      DC /3535 EE
      DC /2700 P
      DC /0000

*
MSG13 DC /3212 BS
      DC /2700 P
      DC /3529 ER
      DC /2900 R

*
MSG14 DC /3132 AB
      DC /2629 OR
      DC /1335 TE
      DC /3400 O

*
MSG15 DC /1425 UN
      DC /3913 IT
      DC /0033 C
      DC /2624 OM
      DC /2723 PL
      DC /3513 ET
      DC /3500 E
      DC /0000

*
MSG16 DC /2729 PR
      DC /2637 OG
      DC /2931 RA
      DC /2400 M
      DC /3326 CO
      DC /2427 MP
      DC /2335 LE
      DC /1335 TE

*
OF52 0120 END BEGN
ND STATEMENTS FLAGGED IN THE ABDVE ASSEMBLY
```

8B934550
8B934560
8B934570
8B934580
8B934590
8B934600
8B934610
8B934620
8B934630
8B934640
8B934650
8B934660
8B934670
8B934680
8B934690
8B934700
8B934710
8B934720
8B934730
8B934740
8B934750
8B934760
8B934770
8B934780
8B934790
8B934800
8B934810
8B934820
8B934830
8B934840
8B934850
8B934860
8B934870
8B934880
8B934890
8B934900
8B934910
8B934920



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ACTI 02ED 0147 02FA 031F 070A 0759
ADRS 0660 0629
ADRS1 0C38 09ED 0A39 0C0E
ADRS2 0C39 0A5A
ARIA1 01E1 01D6 01D7 01D0
ARIA2 025C 0248 024E 0251 0254 0257
ARIA3 0281 0270 0273 0276 0279 027C
ARIA4 02CB 02BB 02BE 02C1 02C4 02C7
ARIA5 01F8 01E6 01E9 01EC 01EF 01F2
AVG 0D30 0428 0C80 0D3C
A000 0780
A001 0789
A002 0973
A003 098F
A004 08D3
A007 0D5D
BAK 01D2 01DC
BEGAC 01C8 0157
BEGAD 0203 01CD 0209
BEGAE 020F 01CF
BEGAF 0213 01F5 020E
BEGAG 0228 0236
BEGAI 0291 021A
BEGAJ 02A2 02A9
BEGAK 02D5 0249 025A 0268 027F 0288 0290 0296 02B9 02CA
BEGAM 0269 0216
BEGAN 0151 0156
BEGAP 013D 0138 02D8
BEGAR 023C 0244
BEGAS 02AB 02B3
BEGBA 013F 0142
BEGIN 014F 02D5 06FB 074D
BEGN 012D 0F52
BEGN1 0132 012D
BEGN2 0133 0137
BEGX3 014E 013E
BEGX4 02D8 0143
BEGX5 02DA 0307
BEGX7 02DC 0308
BEGX8 0158 0151
BSP 07C9 07D4 0C50 0CAA 0CDO 0D7C
BSP12 07E2 07DF
BSP13 07E8 07E4
BSPX1 07E0 07D6
BSP2 07D2 07CA 07CF 07E7
BSP3 07CB 07CC
BSP4 07D6 07D1
BSP6 07DD
BY 0BBE 0BB4
CKERR 08C0
CKHLT 08CA 08C4
CKHLW 08A0 0818 082E 0848 0859 085B
CN1 0909 09D0 093E 0978
CN2 0910 0914 09C5 09CC 0A68 0CA6
CODE 05DA 03EF 0406 05D1 05D5
CODEH 060E 0293 0580 05F1
COM00 08D6 08E4 08EA
COM01 08DD
CON 066D 0153 0203 0206 022C 0233 0239 02A3 02A6 090F
CONV 066A 020F
CONV0 05D9 05B3 05B5 05B8 05BA 05BC
CONV1 066B 020A
CON1 0671 023C 023F 0241 02AB 02AE 02B0 0916
CVTBL 05DC 05AC
C000 076E
C001 0765
DCC 0373 0375 0378 0379 047B 0798 07DB 0800
DCC2 0384 0374

DCC3 038A 0383 0386
DELAY 038C 038D 039A 039C 0A16 0A24 0C57
DIND 03A2 03A3 0811
DLYC 0C36 09EB 0A20 0A31 0A3E
DLY1 0C33 0A14 0A18
DLY2 0C35 0A22 0A26
UR9 04E4 04E0 04E3
UST 06E0 02F1 02FB 0327 0335 06EB 06EC 06ED 06F1 073E 079A 07DD 0802 082C
08B9 0921 0A27 0ADC 0AF4 086C 0BC5 0C4E 0C60
USW 036A 031E 0320 0321 0322 0324 0325
USWSP 0330
DSWX1 081D 0809 0832
DSW0 0807 06F7 0749 07CB 07F1 081B
DSW1 0813 080D
DSW2 0814 080F
DSW5 0809
DSW7 0810 0819
DSW8 0816 0810
EDIT 02E4 0134 0149 01CB 0213 0245 0269 0291 02B6 02F9 0319 0473 04DE 04E7
04F0 05A0 0702 0705 0745 0751 0754 0792 086A 0D4B 0D4E
END 0662 078B 07EE
ERR 0D32 0D08
ERR1 036F 032C
ERR1 0D38 0D19
ERR2 0D3B 0D24
E001 0D6D
E003 07EC
E004 07B5
E005 096C
E006 0988
E007 0D29
E008 0D74
FNC 03AE 03A7
FORM0 0869 0863
FORM1 08D1 0864
FORM2 08D9 0865
FORM3 08DF 0866
FORM4 08E5 0867
FORM5 08EB 0868
FWD1 0287 026F
FWD2 02D1 02B7
FWRD 0262 0247
GPHLM 06D8 01F3 0223 0226 0228 0258 0266 026D 027D 028A 029F 02D3 0AE8 0AEE
GRL1 086A 09DA 0877 08C8 08F1
HALT 03B2 03BC 0666 0716 08CD
HEDEC 05A6 03ED 0404 05D6
HEDE1 05B0 05C6
HEDE2 05B4 05BE
HEDE3 05BF 05B6
HEDE4 05C8 05A7
HEDE5 05CA 05A8
HEDE6 05CC 05A9
HERE 0436 0420 0432
HERE1 0428 0424
HERE2 042F 0428
HEXCD 060C 0416 049E 04A7 04B4 04BE 04CA 05FB 05FF
HEXCV 05E6 0414 049C 04A5 04B2 04BC 04C8 0604
HEXC1 05ED 05EE 05F7
HEXC2 0600 05E8
HEXC3 0602 05E7
HEXWD 0606 0412 049A 04A3 04B0 04BA 04C6 05EA
HEX00 0607 05F3 05F8 05FA 05FC 05FE
HI 0D2F 042F 0C7E 0D39
H0205 0528 04FA
H0238 0527 04F6
H0400 0526 04FD
H2020 0529 0503
ILSW 0366 0316 0337

INPSE 06DF 0221 029D 0884 08A8
INTR 082A 07A8 0820
INTRR 031E 031A
INTRT 081F 0822 0825 0827 0945 094A 097D 098C 0A0D 0A65 0C66 0C96 0CB4
INTR2 0829 0824 082E
INTR3 0312 02EE
IOA 0E9C 06EE 072B 0838 0CA2
IOARA 065E 063C 0645 0647 065A
IOCC1 03B0 03AA 03AB
JDLY2 0391 0399
JDLY3 0398 0393
JDLY4 039A 0396
KBSC 0C2E 0A50
KDASH 0C41 0B0B 0B14
K000E 0B00 0BC2
K0010 0C3B 0A57
K0020 0465 0425 042C 0433
K006 0464 041E
K007 0463 03E5 0B06
K0086 0C3A 09F0 0A36
K0200 0525 0500
K0300 0524 04ED
K0700 0522 04E2
K0900 0523 04E4
K2121 0661 0634
K8000 0D31 0D32 0D38 0D3B
LDLFA 0C49 0C4C
LDLFB 0C4E 0C5F
LDLFC 0C57 0C5B
LDLFE 0C69 0C47
LDLFT 0C46 0C2E 0C6B
LDLFX 0C6F 0C55 0C59
LDLFY 0C5C 0C56
LOSP 0EC9 0844 0EF7
LDSP1 0EE2 0ECC 0EF2
LDSP2 0EF5 0ED4 0ED8
LDSP3 0EE6 0ED1
LDSP4 0EF1 0EEF
LDSP5 0EDC 0E0F
LINE 0C42 09FB 0AF2 0AF8 0B04 0BE7
LNSW 0B9E 090E 0B61 0B64 0BCA 0BE8
LN3 0E40 0511
LN3A 0E50 04EE
LN3B 0E51 04E5
LN3C 0E5A 04F8 0501 0505
LN3D 0E58 050F
LOADK 0C19 0516 0AD3 0B00 0BBF 0C1F
LOADV 0C21 0513 0AD9 0BAB 0BAF 0BB8 0BBC 0C2C
LOAD1 0C1B 0C1E
LOG 061E 0561 0658
LOGAB 047E 0466
LOGAC 0466 0439 045D
LOGA0 046C 046B
LOGAE 0480 04B3
LOGAF 04B8 04BB
LOGBC 052A 043B 045F
LOGC 03BF 044A 0454 08B5
LOGCA 0556 053B
LOGCB 055C
LOGCC 0561 0539
LOGC1 0534 0560
LOGC2 053A 0555
LOGC3 053F 053E
LOGC4 0548 0547 0556 0559
LOGC5 054A 0543 0558
LOGC6 055A 054E
LOGC7 0563 03C0 052C
LOGC8 0565 03C2 052D

LOGC9 0567 03C4 052E
LOGD0 0499 03D4
LOGIC 03CC 03CF
LOGV1 03E0 03DE
LOGV2 03EA 03F8 03FC
LOGX3 05A0 047D
LOGX8 05A4 05A1
LOGX9 05A5 05A2
LOG01 063A 062A 0635 064B 0650 0651
LOG02 0654 0630
LOG03 064B 0644
LOG04 062C 062B 0632 0637 0639 064F
LOG05 0637 0653
LOG2C 03D6 04D1
LOG3C 03FE 03E7 040F
LOG4C 0408 0418
LOG5C 0411 03FF
LOG6C 0419 03DF 03FD 0410
LOG7C 0444 044D 0490 0569
LOG8C 0446 044E
LOG9C 0448 044F
LDW 0D2E 0421 0C7C 0D33
LUX00 056B 052B 054C 0550 055E
LUX02 056C 0535 053A 0544 0552 0554
LUX03 056D 054F 055B
LUX04 056E 0541 0557
MD1LM 06DA 0237
MD3 04ED 04E9 04EC
MK15 043E 03B3 0476 061F
MK27 0440 03B5 0477 0621
MLG 083C 0761 076A 077C 0785 07B1 07E8 0846 08BB 08C5 08C7 0968 096F 09B4
09BB 08CF 0D25 0D59 0D69 0D70 0ECA 0ECD 0ECF 0E02
MLGE 08C5 08CF
MLGX0 0901 084C 085A 08AF
MLGX1 0902 087C 08A1 08B2
MLGX2 0903
MLGX3 0904
MLGX4 0905
MLGX7 060C 01DE 020C 0211 0872 0896
MLGX8 0906
MLGX9 0907 0886 08AA
MLG0A 08BB 08C9
MLG00 085F 085E
MLG02 0863 0861
MLG03 086A 08D8 08F5
MLG04 08B5
MLG05 08B7 084E
MLG06 08B9
MLG07 08AF 087F 088C 08A3 08DE
MLG10 086F 0879
MLG11 088D 086D
MLG12 088E 089E
MLG15 08A5 08AE
MLG16 0881 088B
MLG18 08C7 0842
MOD0 08F7 03C6 03D0 0451 0467 08B4
MOD00 08F0 0870 0876 0882 0888 088F 089B 08A6 08AB 08D2 08DA 08E0 08E6 08F0
MOD01 08FE 08D4 08E2 08E8 08ED
MOD02 08FF 08D6 08F3
MOD03 0900
MOD1 08F8 08BF 080C
MOD1S 06D0 021F
MOD2 08F9 0858
MOD2S 06DE 029B
MOD3 08FA 0852
MOD4 08FB 0849 08C0
MOD5 08FC 0854
MONE 039E 0391



2400 TIMING TEST

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MDNR1 0771 0734 073B 0961 09AA 0C13 0D2B
MDNT 02EF 014C 0668
MDNTC 073D 0715 0784
MDNTD 070C 075E 0777
MDNTE 0761 074C
MDNTF 0720 071B
MDNT1 02F1 02F4
MDNT4 06F0 0300
MDNT6 071C 07B7
MDNT7 072C 0720
MDNT8 0728 072B
MDNT9 0734 072E 0732 073C
MDNX0 075F 0708 0757
MDN10 071E
MDN11 076A 06FA
MDN22 0718 0710
MDN23 0716 070F
MDN24 0785 0714
MDN25 0711 06F6 0744 0747 0769 0770 0783
MSG1 0EF9 0D28 0D5C 0D6C 0D73
MSG10 0F31 0BD1
MSG11 0F35 0D27 0D5B 0D6B 0D72
MSG13 0F39 07EA
MSG14 0F3D 07B3
MSG15 0F41 077F
MSG16 0F49 0788
MSG2 0F01 07B4 07EB
MSG3 0F09 096B 0972 09B7 09BE
MSG4 0F11 0BD2
MSG5 0F19 0763 076C
MSG6 0F1D 077E 0787
MSG7 0F21 096A 0971
MSG8 0F25 09B6 09BD
MSG9 0F29 0EEA
MST 03A0 0392 0398
MTAAA 0B36 0B3B
MTAAB 0B53 0B4A
MTAAC 0B50 0B55
MTAAD 0B56 0B52
MTTX1 0925 071E 093C 0976 088E 0C82 0D77
MTTX3 0926 093D 0942 094F 0977 0982 098F 0997 09C3 09CA 09D0 09FD 0A4A 0A4C
0A52 0AAA 0AAC 0AB2 0B8C 0B90 0BE5 0C72
MTTX6 0927 0726
MTTX9 0928 093F 097A 0A09 0C62 0C92 0CD2
MTTYA 0934
MTTYB 092D 0AA7
MTTYC 092E 08E5 09D4 0A83 0A85 0AB5 0AB9 0BEB
MTTYD 0930 03E8 08E7 09D6 0A8A 0A8C 0BED
MTTYE 0932 08E9 09D8 0A99 0A9B 0BEF
MTTYF 0936 0B94 0B97
MTTY4 0929 0835
MTTY5 092A 0AA1
MTTY8 092B 07AB
MTTY9 092C 0989 0A61 0CB0
MTTZ0 0938 0AB7
MTTZ1 0939 0A01 0A05 0BD5 0BD9 0C00 0C04
MTT01 093C 0735
MTT02 0976 0736
MTT03 09C2 0737
MTT04 09C9 0738
MTT05 09CF 0739
MTT07 0C71 073A
MT1X0 0679 021D 0231 0299 0965 09AE
MT101 093F 09C7
MT102 094A 0944
MT103 0961 096E 0975
MT104 0968 0957 095B
MT105 0963 0951

MT107 096F 0960
MT108 0954 0967
MT200 097A 09CE
MT201 09AC 099A
MT202 099D 0980
MT203 09B1 0984
MT204 0984 09A0 09A4
MT205 0988 09A9
MT206 09AA 09BA 09C1
MT208 0989 09B3
MT209 0995 0992
MT5Q6 0C30 09F3 0A47 0C08
MT5Q7 0A1C 09F5 0A1B 0A49 0A5F 0C09
MT5XA 0675 024C 024F 0252 0255 0262 0264 0271 0274 0277 027A 028C 028E 02B4
02BC 028F 02C2 02C5 02C8 02D1 0ADE 0AE2
MT5XE 0C43 0939 0AE0 0AE4 0AE6 0AEA 0AEC 0AF0 0B27 0B3E 0B44
MT5XF 0C45 0A4E 0AAE 0ACA
MT5X0 067A 01E7 01EA 01ED 01F0 0A2F 0C38 0C39
MT5X2 0C3C 0AFE
MT5X3 0C3D 09F6 0A43 0C0B
MT5X4 0C3E 09FF 0A9D 0A9F 0AA5 0BFB
MT5X8 0C3F 0831 0839
MT5X9 0C40 0B4B 0B53
MT50A 0A2F 09EF 0A3B 0A5C 0C0F
MT50B 0B75 0B72
MT50C 0B83
MT50D 0A68 0AA4 0AB1
MT50F 0A8E 0A95 0AA9
MT502 0A10 0A35 0A51
MT503 0A19
MT505 0A24
MT506 06D7 09E9 0A3C
MT508 0A27
MT509 0A2D 09F2 0A33 0A38 0A59 0A60
MT51A 0B3C 0B29
MT51B 0B59 0B25 0B38 0B41 0B47
MT51C 0B88 0B63
MT51D 0B27 0A03 0A07 0B96 0B99 0BD7 0BDB 0C02 0C06
MT51E 0B2E 0936
MT51F 0B39 0B30
MT510 0C24 0C22 0C29
MT516 0AE6 0B9C
MT517 0B08 0AFC 0B00 0B03 0B08
MT518 0B73 0B6F
MT519 0B23 0B60
MT520 0BC3 0AD0
MT521 0B05 0AC3
MT522 0C15 0C98
MT523 0C17
MT524 0A85 0ABC
MT525 0AC7 0AC2 0ACF
MT526 0B79 09DC 0B67 0B7F 0BCB 0BF3
MT540 06A5 0A12
MT562 0BA9 0B92
MT572 0B70
MT573 09E1 09E4
MT580 0AC3 0AC1
MT581 0BDD
MT582 0C13 0B83
MT583 0A61 0A56 0C11
MT584 0B9C 0B8B
MT585 0BF7 0BFA
MT590 0A57 0A42
MT591 06B1 0C53
MT6X8 0C70 0C88
MT7XA 0D8C 0C9F
MT7XC 0D8D
MT7XE 0D8E 0D1C

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MT7X0 0D84 022E 0874 0892 0899 0C8E
MT7X1 0D85 0C90 0CFB
MT7X2 0D86 0C86 0CC3 0CD9 0D79
MT7X3 0D87 0C74 0CC8 0CDF 0CE7 0CED 0CF1 0CF7
MT7X4 0D88 0C76 0CFF 0D36 0D48
MT7X5 0D89 08EB 0C78 0CE3 0CE5 0D1A 0D20 0D46 0D47
MT7X6 0D8A 0C7A 0CDD 0D01 0D10 0D40
MT7X7 0D8B 0C84 0CBD 0D42
MT7Y0 0D8F 0CE1
MT7Y1 0D90 08EE 0C89 0CE9 0CEF 0D03 0D05 0D0A 0D0C 0D56
MT7Y2 0D91 08F1 0C8C 0CF3 0CF9 0D0E 0D12 0D17 0D18
MT70B 0CD2
MT70F 0CDD 0CCF
MT700 0C8E
MT702 0CA8
MT703 0CA9 0CAF 0CDC
MT704 0CAC 0CA9
MT705 0CDD 0CCE
MT710 0CC6 0CC5
MT711 0D3E 0CC1
MT712 0D2B 0D5F 0D6F 0D76
MT713 0CC3 0CBF 0D43
MT714 0D45 0D21
MT715 0D20 0D1E
MT716 0D70 0D3D 0D4A
MT718 0D77 0CFD
MT719 0C92 0D82
MT730 0D2D 09E5 0BCC 0BFC
MT744 0CFE
MT745 0D7E 0D7B
MT746 0D7B 0D81
MT750 0CF1 0CEC
MT751 0CFB 0CF6
MT754 0D0E 0D37
MT755 0D1A 0D14 0D3A
MT760 0D52 0D4D 0D60 0D62
MT761 0D60 0D50
MT762 0D63 0D54
MT763 0D69 0D57
NOTE 0DEA 0BB6
NOTE1 0E31 0764 076D 0BBA
NTDSH 0AFE 0B0A
ONE 036E 0217 0323 04B5 08BD 0B19 0D34
OPARA 05E1 05AA 05CE 05D0 05D2 05D4
PART2 01E6 01E0
PCCD 044C 0453 0518 0AD5 0B81 0BC1 0C2A 0EF3
PCCX1 0461 0450 0AFA
PGCM 073C 073C 0775
PGSW 093B 02F7 0711 0767 077A
PID 02DE 0856
PRA 0D96 03C8 04A0 04A9 04AC 04B6 04C0 04C3 04CC 04CF 0B17 0B34 0B36 0B4E
0B50 0EE4 0EEC
PRA1 0D94 0B12 0B75 0B86
PRA3 0D93 0B10 0B7D 0B84 0C1B 0C26 0EE8
PRA4 0D92 03CC 0426 042D 0434 0458 0470 0530 0532 05A3 062C 063A 066D 0B1F
0EDC
PRDWT 0830 083A 0940 097B 098A 0A0B 0A63 0C64 0C94 0CB2 0CD4
PRD2 0587 0570 0572
PRSP 059F 052F
PRSW 0B9F 09E7 0B65 0B68 0BCE 0BFE
PRWC 0491 046E
PR00 0573 056E
PR01 057E 056F
PR03 0594 0571
PR3 0DB2 0AD7 0BA9
PR4 0DCE 0BAD
PR6 0BA1 0B70
PR7 0BA5 0B73

2400 TIMING TEST

PR8 0E06 0B7B
RAD 02E0 04B8 0730
RDSWS 0306 02FF 030D 0771 083D
RID 02DF 02F6 03E3 041C 04AE 0700 071C 0721 075C 0773 0850 087A 089F
RST 04D3 051C 06FD 074F
RSTX2 051E 04D9
RSTX3 0520 04DA
RST1 04DA 04DD
RST2 04DE 04D7
RTN1X 090F 090A
RTN2X 0916 0911
RWD 07F0 0723 0778 07FE 0987 09B1 0A54 0BDD
RWDX0 0804 07D9 07F2
RWDX1 0805 07F4
RWD2 07FE 07FB
RWD3 07F1 07F8 0803
RWD4 0800 07FD
SENSE 065C 0623 063E
SNSPR 0442 0472 0475 0478 0480 0484 0487 0488
SNWC 07BA 079B
SNWC1 07BB 0794
SPEC 066C 01D3 01D9 0B1D
STCN 0917 090C 0913 0923
STCN1 0919 090B 0912 0920
STCN2 091F 091D
STCN3 0921 091E
SVE 051A 0507
SVEXT 0359 0352
SVINT 0332 014E 031C 0359
SVIN0 033E 034E 0357
SVIN1 0340 0348
SVIO 0364 0333 0344 0345 0358
SV0 035B 034C
SV1 035C 033E
SV2 035D 033C
SV3 035E 0353
SV4 035F 033B 0340 0349 034B 0356
SV5 0360 033F 0342 0346
SV6 0361 033D 0343 0354
SV7 0362 0338 034F
SW 0462 03E1 03F4 03F6 09F8 0A40 0A45 0C0C
SW0 02E1 02DA 0436 045A 04D4 0663 06F3 070C 0740 083F 08CA 095C 09A5 0ABD
0BB1 0BDF 0ED5
SW1 02E2 02DC 0309 030C 0718
SYDR 050D 0509
SYDR1 050F 050C
TAAQ 0368 0311 0315
TADSW 036C 0326 0329 032D
TADWC 036D 032A
TAX1 030F 0314 031D 032F
TERM 02E3 0334 0419 0456 0536 062E
TERR 0371 0317
TMIC 0500 04F2 04FF
TMICA 04FD 04F4
TMICB 0505 04FC
TMRDT 07C0 07C4 0993 0995 0A6A 0A6C 0A72 0A74 0A7A 0C17 0CB7 0CB9 0CBB 0CD6
TMRD1 0790 07C3
TMRD4 07C4 098E 0A67 0CB6
TMRX2 07C7 079D
TMRX3 07C8 07C1
TMWRT 078D 07AF 07C5 0948 094D 0980 0985 0A10 0A19 0A1E 0A29 0A2B 0C15 0C49
0C99 0C9B 0C9D 0CA4
TMWR0 07A5 0795 079F
TMWR1 079A
TMWR2 079B 07A3
TMWR3 07A1
TMWR4 07AA 07C6 0947 094C 097F 0A0F 0C68
TMWR6 07B1 07AE



TMWR7 07B7
TMWR8 07AF
TMWR9 079F 078F 07C2
TOTA 0E69 092D 09E1 0A70 0A78 0A7E 0A80 0A87 0A8E 0A90 0A96 0AC7 0ACC 0B23
0B2C 0B3C 0B42 0B57 0BF7
TRK9 0B04 0AF6
TURA1 06D9 021B
TURA2 06D8 0297
TWRX0 07BC 0797 07A1 0952 0963 099B 09AC 0A6E 0A76 0A7C 0CC8 0D3E
TWRX1 07BD 0790
TWRX2 07BE
TWRX3 07BF 078E
UNMK3 0302 02FD 03B8 048C 0654
UNMK4 0304 02FE 03BA 048E 0656
WAITA 0372 300A
WAIT1 014B 3001
WAIT2 03B7 3002
WAIT3 047A 3003
WAIT4 0626 3004
WAIT5 07A7 3005
WAIT6 07DE 3006
WAIT9 0370 3009
WC 03BE 03DB 03DC 03FA 040D
WDCON 05E5 03EB 0402 05AE
WORD 05D8 05AF 05B4 05B9 05C0
WRDSW 065F 0628 0642 0648 064D
WRITE 065A 063D
XIOSN 063E 0641
XIQWR 063D 064A
END OF ASSEMBLY

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1. PURPOSE

THE MAGNETIC TAPE TIMING TEST (MTTIM) IS DESIGNED TO TEST WRITE AND READ DELAY, INTER-RECORD GAP, CREEP AND ERASE HEAD ON THE 2400 MAGNETIC TAPE UNIT SERIES FOR COMPLIANCE WITH THE PRODUCT SPECIFICATIONS. THE PROGRAM IS ABLE TO TEST,

- 1. SYSTEMS WITH ONE OR TWO TAPE DRIVES.
- 2. DRIVES WITH 9 TRACK OR 7 TRACK READ-WRITE HEADS.
- 3. MODELS 1, 2, OR 3 WITH 2 OR 4 USEC STORAGE.

IF SYSTEM HAS TWO DRIVES, BOTH DRIVES MAY BE SEQUENTIALLY TESTED IN ONE CONTINUOUS RUN OF THE PROGRAM.

2. PREREQUISITES

THIS PROGRAM ASSUMES THAT THE 2400 MAGNETIC TAPE FUNCTION TEST RUNS AND NO TAPE CONTROL ERRORS EXIST. EQUIPMENT REQUIRED CONSISTS OF,

- 1. 1442 CARD READ/PUNCH OR 1054 PAPER TAPE READER.
- 2. 1053 OR 1816 TYPEWRITER, OR 1443 PRINTER.
 - A. IF 1443 IS USED - A CARRIAGE TAPE WITH AT LEAST CHANNEL 1 PUNCHED SHOULD BE USED.
- 3. 1800 PROCESSOR CONTROLLER.
- 4. ONE OR TWO 2400 SERIES MAGNETIC TAPE DRIVES.
- 5. THIS PROGRAM REQUIRES THE RELOCATABLE DIAGNOSTIC LOADER.

3. USE PROCEDURE

3.1 PROGRAM LOADING

3.1.1 ON TAPE DRIVE(S) TO BE TESTED,

- 1. LOAD TAPE REEL.
- 2. DEPRESS LOAD-REWIND KEY.
- 3. DEPRESS START KEY. AFTER TAPE REWINDS TO LOAD POINT, DRIVE(S) SHOULD BECOME READY.

3.1.2 REFER TO RELOCATABLE DIAGNOSTIC LOADER DOCUMENTATION FOR LOADING PROCEDURE.

3.1.3 IF OPTIONS ARE DESIRED, GO TO 3.2.2.
IF NO OPTIONS ARE DESIRED, GO TO 3.2.1.

3.2 OPERATION

3.2.1 TYPICAL OPERATING PROCEDURE

IF NO OPTIONS ARE SET THE PROGRAM ASSUMES,

- 1. BOTH DRIVES ARE TO BE RUN.
- 2. OUTPUT DEVICE IS TO BE 1053/1816 TYPEWRITER.

TO EXECUTE PROGRAM DEPRESS THE START BUTTON.

3.2.2 OPERATING OPTIONS

IF OPTIONS ARE DESIRED, SET SWITCHES DESIRED FROM TABLES 1 AND 2 AND DEPRESS THE START BUTTON.

TABLE 1 GENERAL CONTROL

- 1. SWITCHES MAY BE SET PRIOR TO PROGRAM LOADING OR AT WAIT 1.
- 2. SWITCHES 0-1 MAY BE CHANGED ONLY BY A RESET-START OPERATION.
- 3. SWITCHES 5-15 MAY BE CHANGED ANYTIME.

*****																*****													
* DATA ENTRY SWITCHES * DESCRIPTION *																													
* 0	* 1	* 2	* 3	* 4	* 5	* 6	* 7	* 8	* 9	* 10	* 11	* 12	* 13	* 14	* 15														
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1..HALT BEFORE EACH ROUTINE													
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1.....HALT ON ERROR													
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1.....BYPASS ALL PRINTOUTS EXCEPT GRAPH													
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1.....LDDP PROGRAM													
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1.....USE 1443 AS OUTPUT DEVICE													
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1.....LDDP RTN5 IN READ, AFTER INITIAL WRITE													
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1.....BYPASS PRINTING RTN 5 GRAPH													
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1.....REQUEST RTNS 1-4 TO PRINT TIME FOUND													
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1.....BYPASS ALL HEADING PRINTOUTS													
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1.....DO NOT RUN DRIVE 1													
* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	* .	1.....DO NOT RUN DRIVE 0													
																NOTE 1 - DELAY TIMES ALWAYS PRINT IF TIME IS OUTSIDE ALLOWABLE LIMITS.													
																NOTE 2 - IF DRIVE 1 IS EDITED AS NOT AVAILABLE - SWITCH 1 IS NOT USED.													

TABLE 2 LDDP ROUTINE

- 1. THESE SWITCHES CAN BE CHANGED AT ANY TIME.
- 2. IF ZERO IS ENTERED, THE PROGRAM WILL NOT LOOP BUT WILL RUN ALL ROUTINES IN SEQUENCE.
- 3. IF IT IS DESIRED TO START ON A ROUTINE OTHER THAN ROUTINE 1, AND CONTINUE

THE TEST FROM THAT POINT,

- A. SET STARTING ROUTINE PER TABLE 2.
- B. START PROGRAM.
- C. WHILE PROGRAM IS RUNNING SELECT ROUTINE ZERO.
- D. PROGRAM WILL COMPLETE THE SELECTED ROUTINE AND THEN RUN THE REMAINING ROUTINES IN THEIR NORMAL SEQUENCE.

* PROGRAM SWITCHES * DESCRIPTION *
* 5 6 7 *
* X X X.....ENTER A ROUTINE NUMBER FROM 0 TO 7. *

3.3 TERMINATING PROCEDURE

1. THE PROGRAM WILL TERMINATE WHEN ALL DRIVES WHICH ARE SELECTED HAVE BEEN TESTED.
2. THE PROGRAM WILL TERMINATE IF ERROR PRINTOUT E003 OCCURS. (SEE SEC. 4.3)

3.4 RESTART PROCEDURE

PRESS THE STOP, RESET AND START BUTTONS. THE PROGRAM SHOULD GO TO WAIT 1. IF THIS DOES NOT OCCUR, THE PROGRAM MUST BE RELOADED.

3.5 PROGRAM HALTS

PROGRAM WAITS ARE USED IN THIS PROGRAM, AND ARE IDENTIFIED BY REFERENCING THE B REG AND I REG.

A PROGRAM WAIT IS OF THE FORM,

30XX, (B REG).

A DESCRIPTION OF THE INDIVIDUAL PROGRAM WAITS CAN BE FOUND AT THE BEGINNING OF THE PROGRAM LISTING. A TYPICAL WAIT DESCRIPTION FOLLOWS. IT IS INCLUDED TO SHOW THE FORMAT OF THE LISTING, AND IT IS NOT NECESSARILY A DESCRIPTION OF AN ACTUAL WAIT.

3001 0 014C OC WAIT1+1 WAIT FOR DATA ENTRY
* SWITCHES TO BE SET.
* PUSH START TO
* CONTINUE THE PROGRAM.

B REG, (FIRST 4 DIGIT GROUP) CORRESPONDS TO B REG READING.

I REG, (SECOND 4 DIGIT GROUP) CORRESPONDS TO I REG READING.

4. PRINTOUTS
PID AND MID AS SEEN IN DESCRIPTIONS BELOW WILL NOT BE FOUND IN PRINTED HEADINGS. INSTEAD, A DESCRIPTION OF THE MESSAGE WILL BE PRINTED.

4.1 COMMAND MESSAGES

PID MID RID RAD UNIT
NO.
B900 C000 XXXX XXXX 000X
DRIVE 0 IS NOT READY

B900 C001 XXXX XXXX 000X
DRIVE 1 IS NOT READY

4.2 INFORMATION PRINTOUTS

PID MID RID RAD UNIT
NO.
B900 A000 000B XXXX 000X
ALL ROUTINES ARE COMPLETE

B900 A001 000B XXXX 000X
PROGRAM IS COMPLETE.

MIN TIME MAX
LIM FND LIM
B900 A002 XXXX XXXX 000X XXXX XXXX XXXX
WRITE DELAY TIMING, PRINTED IF BIT 6 OF DATA ENTRY SWITCHES IS ON. (IF RID IS 0001-TIME FND IS FOR A WRITE WHEN AT LOAD POINT. IF RID IS 0003-TIME FND IS FOR A WRITE WHEN NOT AT LOAD POINT.

MIN TIME MAX
LIM FND LIM
B900 A003 XXXX XXXX 000X XXXX XXXX XXXX
READ DELAY TIMING, PRINTED IF BIT 6 OF DATA ENTRY SWITCHES IS ON. (IF RID IS 0002-TIME FND IS FOR A READ WHEN AT LOAD POINT. IF RID IS 0004-TIME FND IS FOR A READ WHEN NOT AT LOAD POINT.

IO
MSEC VAR MIN
AVG AVG AVG
B900 A004 0005 XXXX 000X XXXX XXXX XXXX
INTERRECORD GAP AVERAGES FOUND BY ROUTINE 5. (INCHES)

MIN AVG MAX
CREEP CREEP CREEP
FND FND
B900 A007 0006 XXXX 000X XXXX XXXX XXXX
FORWARD CREEP FOUND. (INCHES).

4.3 ERROR PRINTOUTS

PID MID RID RAD UNIT MIN AVG MAX
NO. CREEP CREEP CREEP
FND FND
B900 E001 0006 XXXX 000X XXXX XXXX XXXX
CREEP WAS LESS THAN .05 (INCHES).

DSW
RECEIVED
B900 E003 XXXX XXXX 000X XXXX
DSW WRONG AFTER BACKSPACE, PUSH START TO RESTART PROGRAM.

DSW
RECEIVED



B900 E004 XXXX XXXX 000X XXXX
TEST ABORTED DUE TO DSW OR UNEXPECTED INTERRUPT.
IF DSW IS-FFFF-INTERRUPT OCCURRED ON A LEVEL OR ILSW
BIT OTHER THAN THAT EDITED FOR MAGNETIC TAPE.

MIN TIME MAX
LIM FND LIM
B900 E005 XXXX XXXX 000X XXXX XXXX XXXX
WRITE DELAY TIMING ERROR. (MSEC)
IF RID IS 0001-ERROR IS FOR A WRITE WHEN AT LOAD POINT.
IF RID IS 0003-ERROR IS FOR A WRITE WHEN NOT AT LOAD POINT.

MIN TIME MAX
LIM FND LIM
B900 E006 XXXX XXXX 000X XXXX XXXX XXXX
READ DELAY TIMING ERROR. (MSEC)
IF RID IS 0002-ERRCR IS FOR A READ WHEN AT LOAD POINT.
IF RID IS 0004-ERROR IS FOR A READ WHEN NOT AT LOAD POINT.

MIN AVG MAX
CREEP CREEP CREEP
FND FND
B900 E007 0006 XXXX 000X XXXX 0000 XXXX
CREEP WAS ZERO. (INCHES).
NOTE ANY NEGATIVE CREEP VALUE IS PRECEDED BY A MINUS SIGN.

MIN AVG MAX
CREEP CREEP CREEP
FND FND
B900 E008 0006 XXXX 000X XXXX XXXX XXXX
CREEP WAS NEGATIVE. (INCHES)
NOTE EACH NEGATIVE CREEP VALUE IS PRECEDED BY A MINUS SIGN.

NOTE

PROGRAM ID, ROUTINE NUMBER, ROUTINE ADDRESS, MESSAGE ID, UNIT
NUMBER AND DSW ARE ALWAYS PRINTED IN HEXADECIMAL. ALL
OTHER WORDS OF ANY MESSAGE ARE PRINTED IN DECIMAL. ON
DECIMAL PRINTOUTS, ASSUME A DECIMAL POINT AS SHOWN BELOW.
WHERE PRINTOUT IS IN INCHES XX.XX
WHERE PRINTOUTS ARE IN TIME MODEL 3 MODEL 1 AND 2
XX.XX XXX.X

IN ADDITION TO THE ABOVE PRINTOUTS, ROUTINE 5 PRINTS A PLOT
OF RECORD GAP VS WRITE GO DOWN TIME.

5. COMMENTS

A. MTTIM CONSISTS OF A MAGNETIC TAPE TIMING MONITOR ROUTINE, A SERIES OF
COMMON MAGNETIC TAPE SUBROUTINES, AND A SERIES OF TESTS. SECTION 5.8
GIVES A DESCRIPTION OF EACH OF THE COMMON SUBROUTINES AND THEIR
CALLING SEQUENCES. SECTION 5.C GIVES A DESCRIPTION OF EACH OF THE
TEST ROUTINES.

THERE IS ONE TABLE AROUND WHICH ALL ROUTINES ARE ORIENTED. THIS
TABLE IS THE DEVICE STATUS TABLE, CALLED DST. INDEX REGISTER 1
ALWAYS CONTAINS THE NUMBER OF THE TAPE DRIVE BEING USED AND INDEX
REGISTER 2, THE BASE ADDRESS OF THE DST TABLE. THE DST TABLE IS THE
BASIC MEANS OF COMMUNICATION BETWEEN ROUTINES.

B. COMMON SUBROUTINES

EACH SUBROUTINE ASSUMES THAT INDEX REGISTER 1 CONTAINS THE UNIT
IDENTIFICATION AND INDEX REGISTER 2 CONTAINS THE BASE ADDRESS OF THE
DST TABLE.

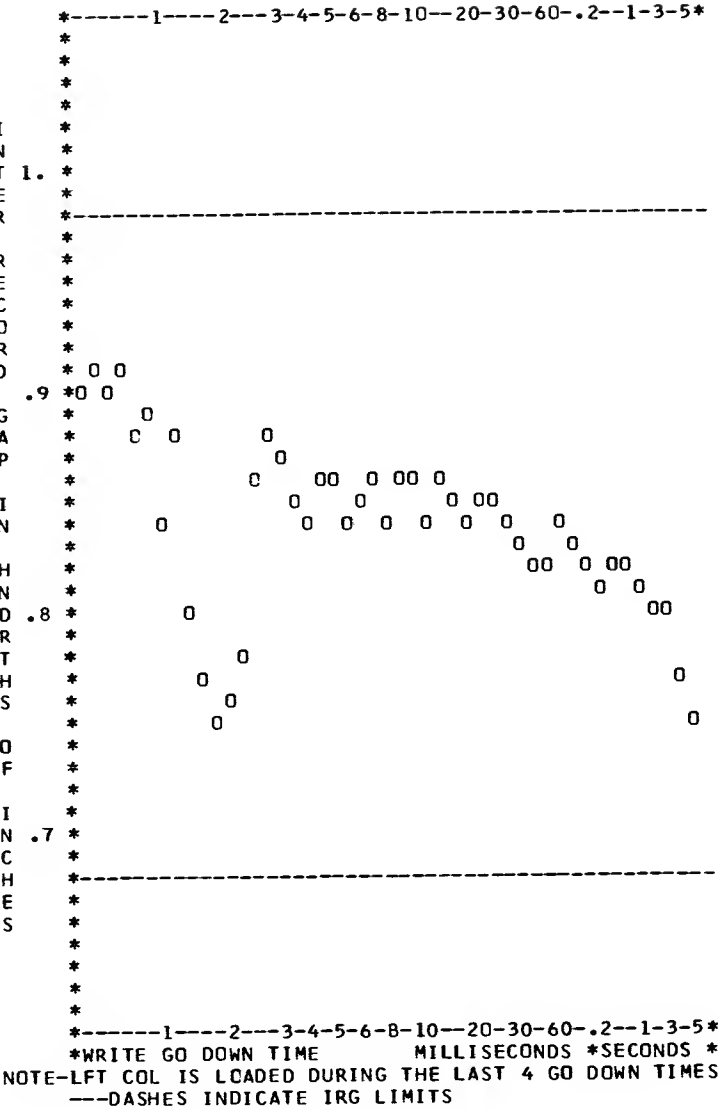
CALL NAME
BSI L BSP
USE- BACKSPACE CNE RECORD.
BSI L DCC
DC ADRS. OF CALL STRING
USE- BUILD THE PROPER IOCC WORDS FROM THE CALL STRING AND ISSUE THE XIO
COMMAND.
BSI L DELAY
DC NUMBER OF LOOPS.
USE- DELAY 25 USEC FOR EACH LOOP SPECIFIED.
BSI L DIND
DC ADRS. OF AREA CODE
DC ADRS. OF MODIFIER
USE- BUILD THE PROPER IOCC WORDS AND SENSE THE DEVICE.
BSI L DSWO
USE- CALLS ON SUBROUTINE DIND AND RETURNS WITH THE DSW STORED IN THE
DSW TABLE AND IN THE A REGISTER.
BSI L HALT
USE- MASKS ALL INTERRUPT LEVELS AND WAITS FOR OPERATOR ACTION.
BSI L INTRT
DC RETURN ADDRESS
USE- SAVES FOR USE AFTER THE NEXT INTERRUPT THE RETURN ADDRESS
SPECIFIED IN THE CONSTANT.
BSI L LOG
USE- DETERMINE THE DESIRED OUTPUT DEVICE, CONVERT AND PRINT THE MESSAGE
CODE SET UP BY SUBROUTINE MLG.
BSI L MLG
DC ADRS. OF LEFT HALF OF HEADING
DC ADRS. OF RIGHT HALF OF HEADING
DC MESSAGE ID
DC LINE AND FORMAT NUMBER.
USE-1. SETS UP THE HEADING TO BE PRINTED AND CALLS ON PCCO.
2. SETS UP THE MESSAGE TO BE PRINTED AND CALLS ON LOG.
BSI L PCCO
USE- DETERMINES THE DESIRED OUTPUT DEVICE AND PRINTS A PRESET MESSAGE
WITH NO CONVERSION.
BSI L RWD

USE- REWINDS THE DRIVE SPECIFIED BY XR1.
BSI L TMRDT
USE- READS THE UNIT SPECIFIED BY XR1 AND TIMES THE LENGTH OF TIME TO A
CHANGE IN THE WORD COUNTER.
BSI L TMRWT
USE- WRITES ON THE UNIT SPECIFIED BY XR1 AND TIMES THE LENGTH OF TIME
UNTIL THE WORD COUNTER HAS CHANGED TWICE.

C. TEST ROUTINES

RTN. NO. DESCRIPTION

- 1 THRU 4 THESE ROUTINES TIME READ AND WRITE DELAYS AND CHECK FOR TIME
BEING WITHIN LIMITS. IF TIME IS OUTSIDE LIMITS AN ERROR PRINTOUT
IS GIVEN. TIME WHICH IS WITHIN LIMITS IS PRINTED ONLY IF
REQUESTED. (SEE TABLE 1)
- 1 WRITE DELAY AT LOAD POINT.
2 READ DELAY AT LOAD POINT.
3 WRITE DELAY NOT AT LOAD POINT.
4 READ DELAY NOT AT LOAD POINT.
5 INTERRECORD GAP TEST
- THIS ROUTINE WRITES A SERIES OF RECORDS WITH CONTROLLED GO LINE
DOWN TIME BETWEEN RECORDS. THE SEQUENCE IS --
- A. WRITE A RECORD
B. DELAY 10 MILLISECOND
C. WRITE A RECORD
D. LOAD THE LEFT COLUMN IF NEXT VARIABLE DELAY IS 2.0,3.0,4.0, OR
5.0 SECONDS.
E. WRITE A RECORD
F. DELAY A VARIABLE TIME (0.5MILLISECOND TO 5 SECONDS)
G. WRITE A RECORD
H. WRITE A RECORD
- THE ABOVE SERIES IS REPEATED 47 TIMES WITH THE VARIABLE DELAY
INCREASING EACH TIME.
- WHEN VARIABLE DELAY REACHES 5 SECONDS, THE SERIES IS RESTARTED FOR
A TOTAL OF FIVE PASSES.
- THE TAPE IS THEN REWOUND AND ALL RECORDS READ AND ALL GAPS
CHECKED FOR LENGTH.
- A GRAPH OF INTERRECORD GAP VERSUS VARIABLE GO LINE DOWN TIME IS
THEN PRINTED. A SUMMARY IS ALSO PRINTED SHOWING AVERAGE GAP
LENGTH WITH 10 MILLISECOND DELAY, VARIABLE DELAY AND NO DELAY.
- 6 WRITE-BACKSPACE-WRITE CREEP TEST.
- THIS ROUTINE CHECKS FOR TAPE CREEP BY WRITING SEVERAL RECORDS,
BACKSPACING OVER THE LAST RECORD WRITTEN AND REWRITING IT. THE
LENGTH OF THE RESULTING GAP IS THEN CHECKED AND COMPARED TO THE
ORIGINAL GAP.



TYPICAL 7 TRACK GRAPH

0 0

6 APPENDIX

6.1 EDIT PROCEDURE

THE FOLLOWING EDIT PROCEDURE IS FOR CARD INPUT. THE EDIT PROCEDURE FOR PAPER TAPE INPUT IS LOCATED IN THE PAPER TAPE EDIT UTILITY PROGRAM DOCUMENTATION. THE PROPER EDIT CARDS MUST BE THE LAST CARDS IN THIS PROGRAM DECK. THE FOLLOWING FORMS ARE PROVIDED TO AID IN MANUALLY PREPARING THESE EDIT CARDS OR UPDATING EXISTING EDIT CARDS. IF IT IS NECESSARY TO PREPARE OR MODIFY EDIT CARDS, FILL IN THE NECESSARY DATA IN THE FORMS PRIOR TO PUNCHING THE CARDS. CARD COLUMNS THAT ARE SHADED SHOULD BE LEFT BLANK.

THE LAST EDIT CARD IS THE "END EDIT CARD." THE INFORMATION IN THIS CARD INCLUDES:

CARD 0 MUST CONTAIN ALL SEVEN ENTRIES. REFERENCE THE COLUMN HEADING FOR THE NECESSARY ENTRIES.

1. AN "E" IN COLUMN 1.
2. THE PID FOR THIS PROGRAM (COL 2-3).
3. A TERMINATOR WORD OF "FFFF" (COL. 7-10).

[illegible]

